

**BC817KH series 45 V, 500 mA NPN general-purpose transistors** Rev. 1 — 15 December 2017

**Product data sheet** 

#### **Product profile** 1

### **1.1 General description**

NPN general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

#### Table 1. Product overview

Type number	Package	PNP complement	
	Nexperia	JEDEC	
BC817K-16H	SOT23	TO-236AB	-
BC817K-25H			-
BC817K-40H			-

### 1.2 Features and benefits

- · Three current gain selections
- · High power dissipation capability
- High-temperature applications up to 175 °C
- · AEC-Q101 qualified

#### 1.3 Applications

· General-purpose switching and amplification

### 1.4 Quick reference data

#### Table 2. Quick reference data

#### $T_{amb}$ = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	1	А
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA					
	BC817K-16H		[1]	100	-	250	-
	BC817K-25H		[1]	160	-	400	-
	BC817K-40H		[1]	250	-	600	-

[1] pulsed; tp  $\leq$  300 µs;  $\delta \leq$  0.02

# nexperia

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### 2 Pinning information

Table 3. Pinn	ing			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	E	emitter	3	С
3	С	collector		B – E sym123

### **3** Ordering information

Table 4. Ordering	information			
Type number Package				
	Name	Description	Version	
BC817K-16H	TO-236AB	Plastic surface-mounted package; 3 leads	SOT23	
BC817K-25H				
BC817K-40H				

### 4 Marking

Table 5. Marking		
Type number		Marking code
BC817K-16H		%HD
BC817K-25H	[1]	%HE
BC817K-40H	[1]	%HF

[1] % = placeholder for manufacturing site code

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#### **Limiting values** 5

#### Table 6. Limiting values

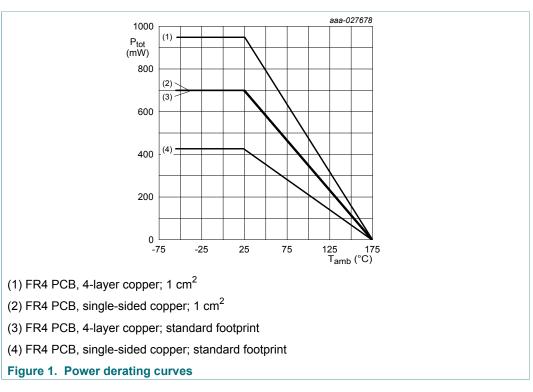
In accordance with the Absolute Maximum Rating System (IEC 60134).

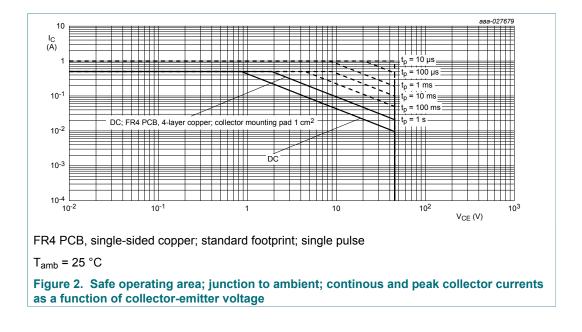
Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	open emitter		50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	7	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	425	mW
			[2]	-	700	mW
			[3]	-	700	mW
			[4]	-	950	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

[2] [3] [4]





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#### **Thermal characteristics** 6

#### Table 7. Thermal characteristics

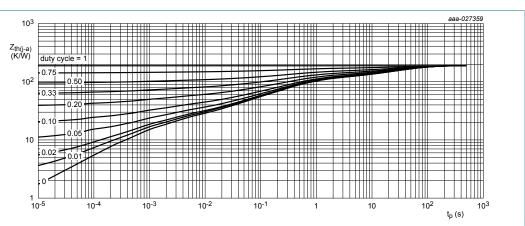
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction	in free air	[1]	-	-	353	K/W
	to ambient		[2]	-	-	215	K/W
		[3] [4]	[3]	-	-	215	K/W
			[4]	-	-	158	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	60	K/W

Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint. [1]

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

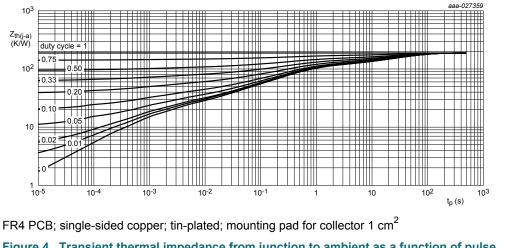
[4] Device mounted on an FR4 PCB; 4-layer copper; tin-plated and standard footprint.

Device mounted on an FR4 PCB; 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

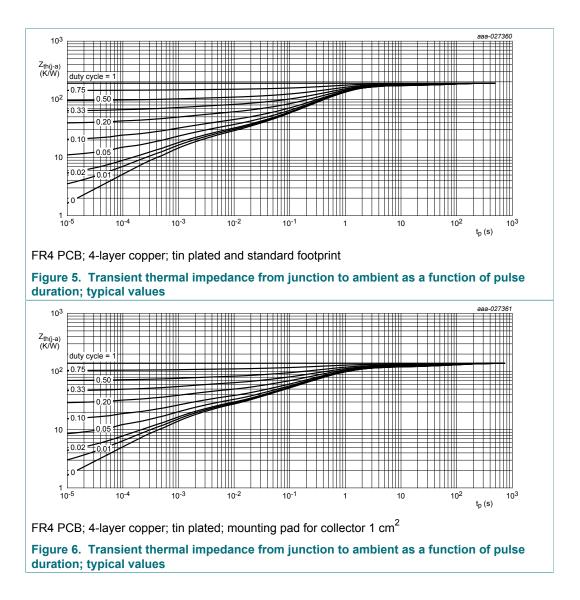


FR4 PCB; single-sided copper; tin-plated and standard footprint









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### 7 Characteristics

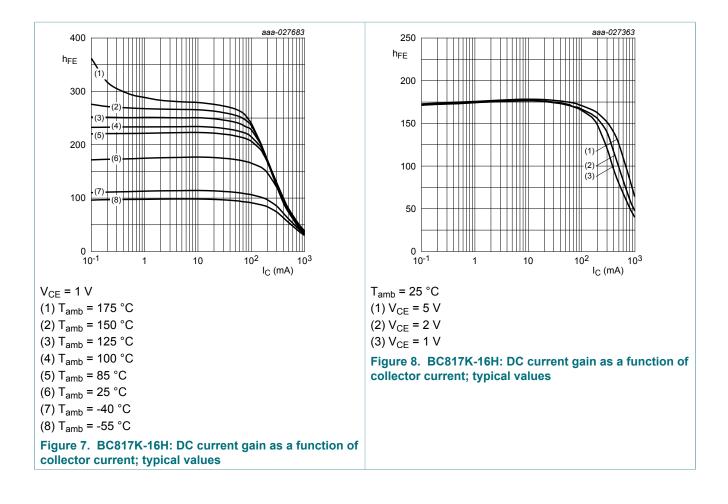
#### Table 8. Characteristics

#### $T_{amb}$ = 25 °C unless otherwise specified.

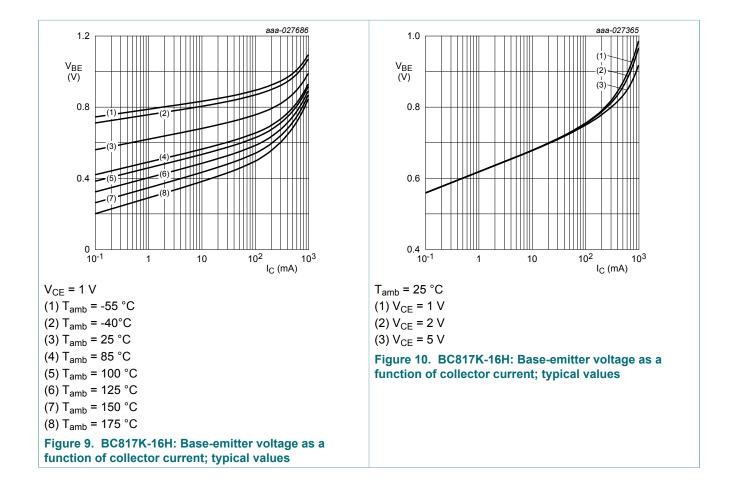
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0 A	<sub>C</sub> = 10 mA; I <sub>B</sub> = 0 A		-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A		7	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A		-	-	100	nA
h <sub>FE</sub>	DC current gain	·					
	BC817K-16H	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	100	-	250	
	BC817K-25H	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	160	-	400	
	BC817K-40H	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	250	-	600	
	BC817K-16H, -25H, -40H	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	700	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	1.2	V
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	-	-	1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	3	-	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = i_{c} = 0 \text{ A}; f = 1 \text{ MHz}$					
	BC817K-16H			-	44	-	pF
	BC817K-25H			-	39	-	pF
	BC817K-40H			-	39	-	pF

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

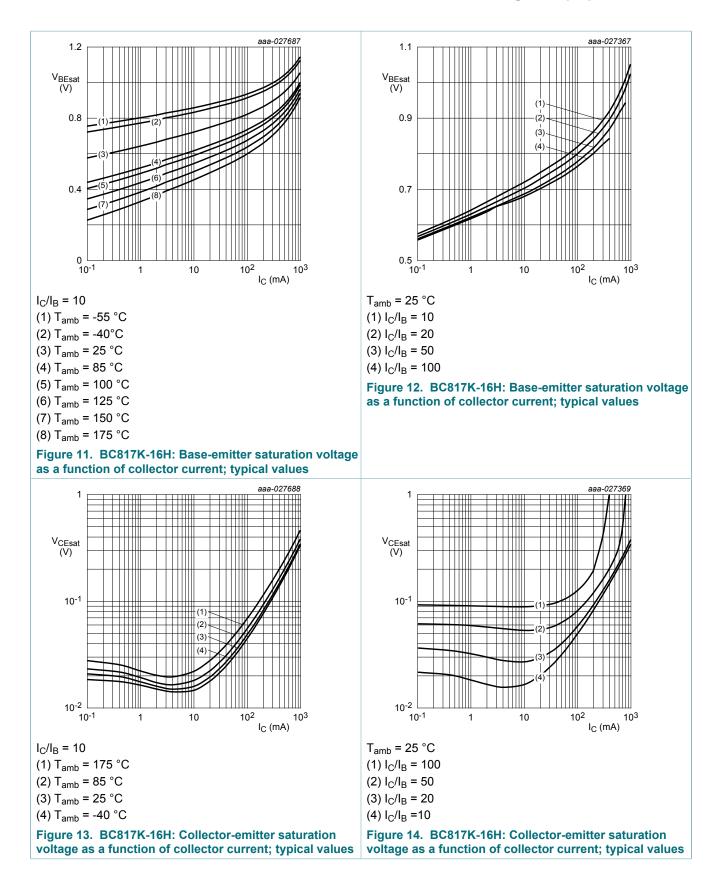
## **BC817KH series**



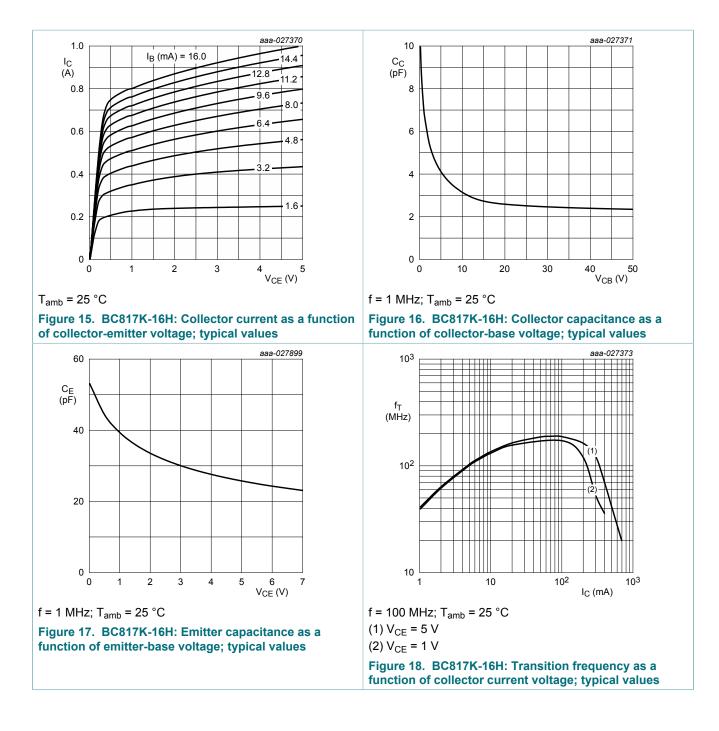
### **BC817KH series**



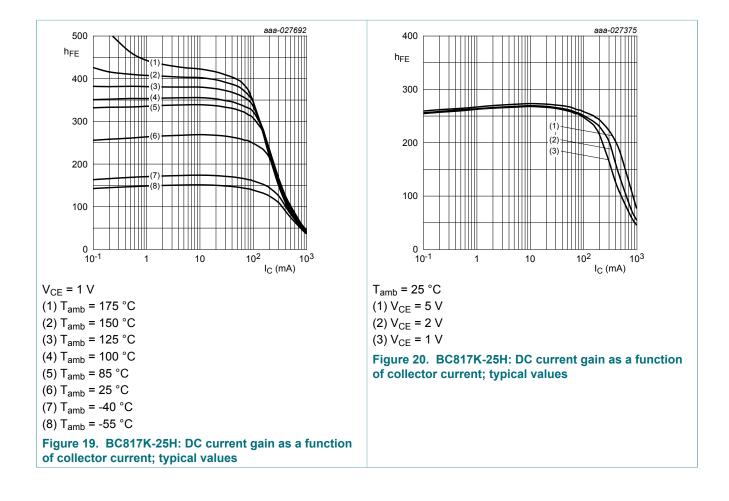
### **BC817KH series**



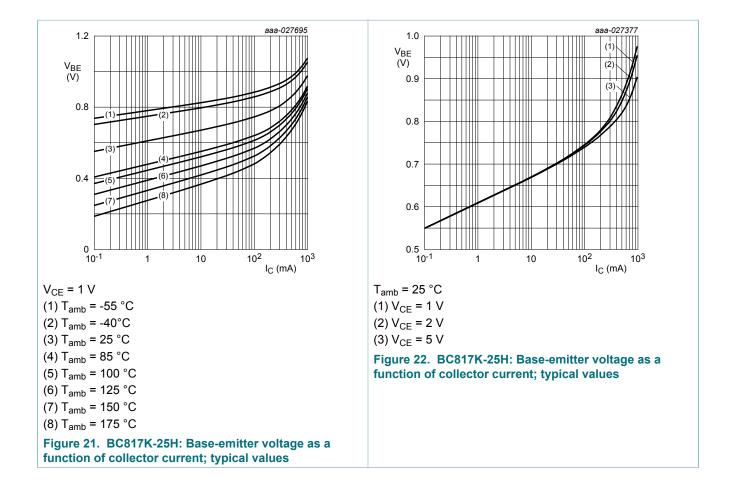
### **BC817KH series**



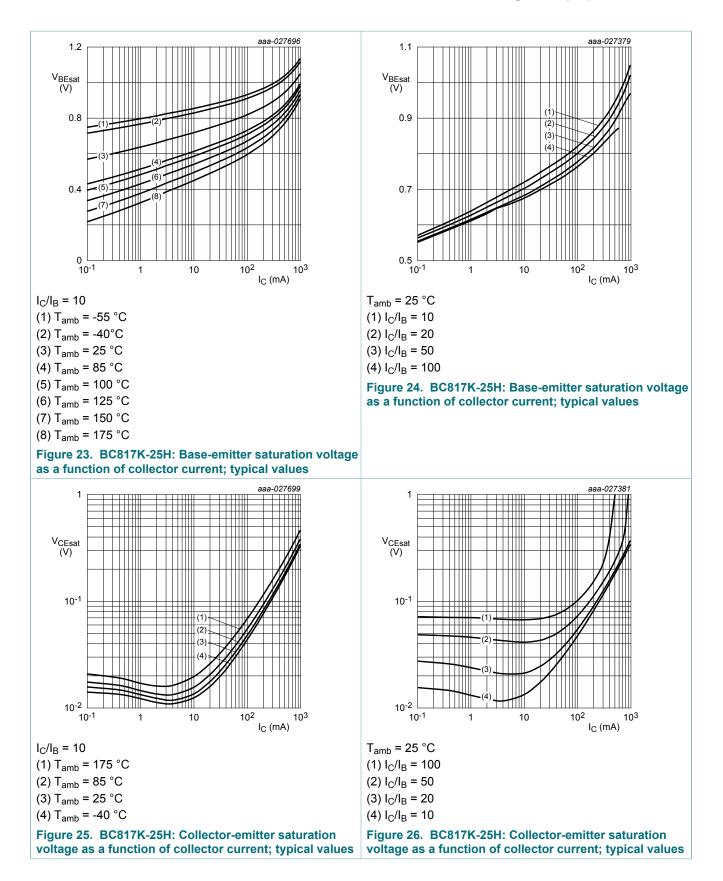
### **BC817KH series**



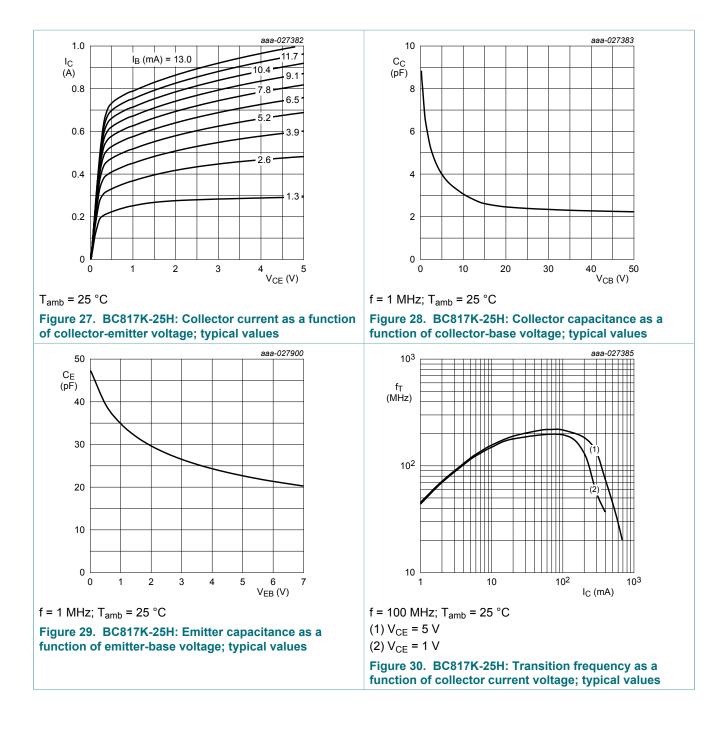
### **BC817KH series**



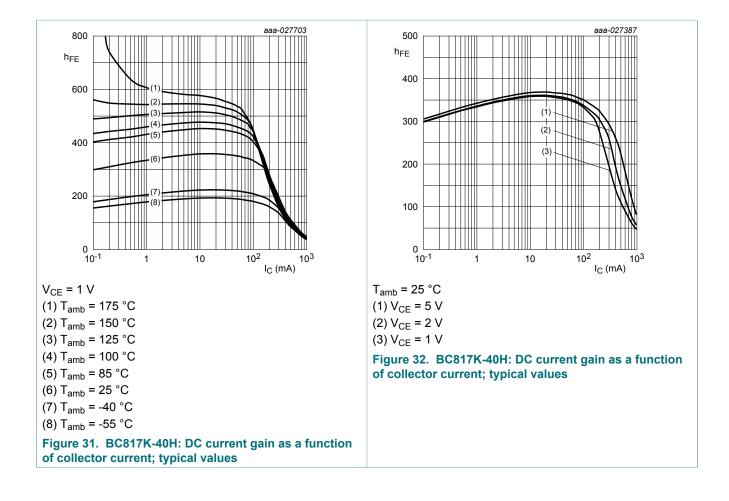
### **BC817KH series**



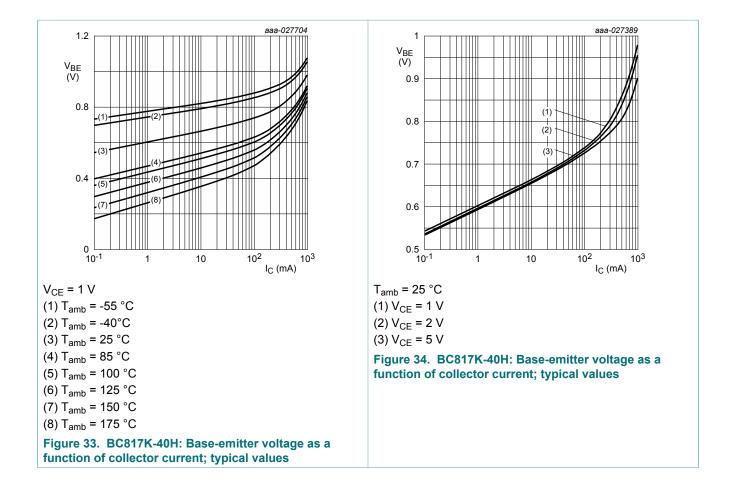
### **BC817KH series**



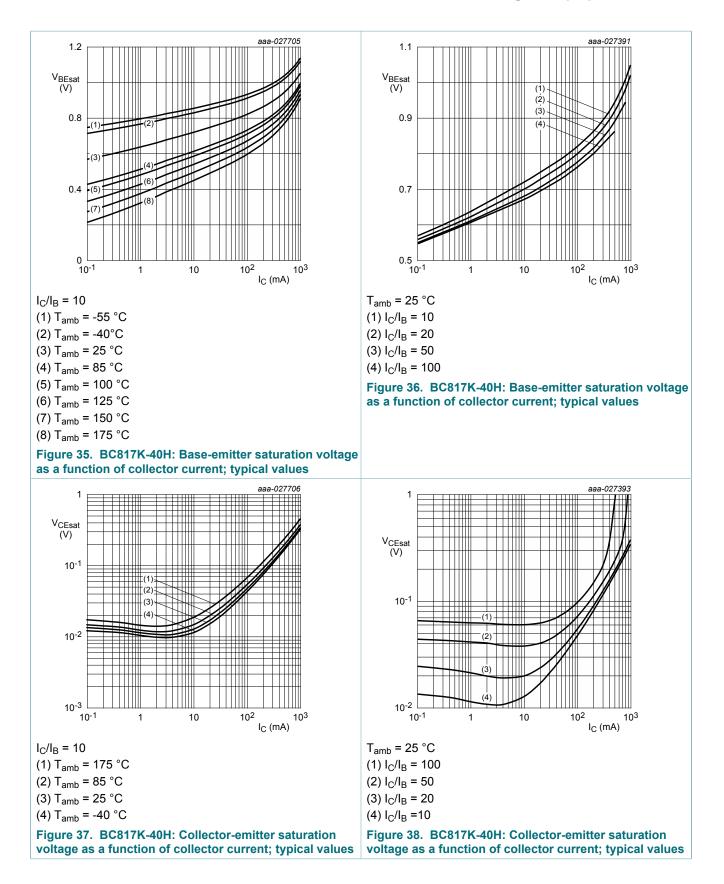
## **BC817KH series**



### **BC817KH series**

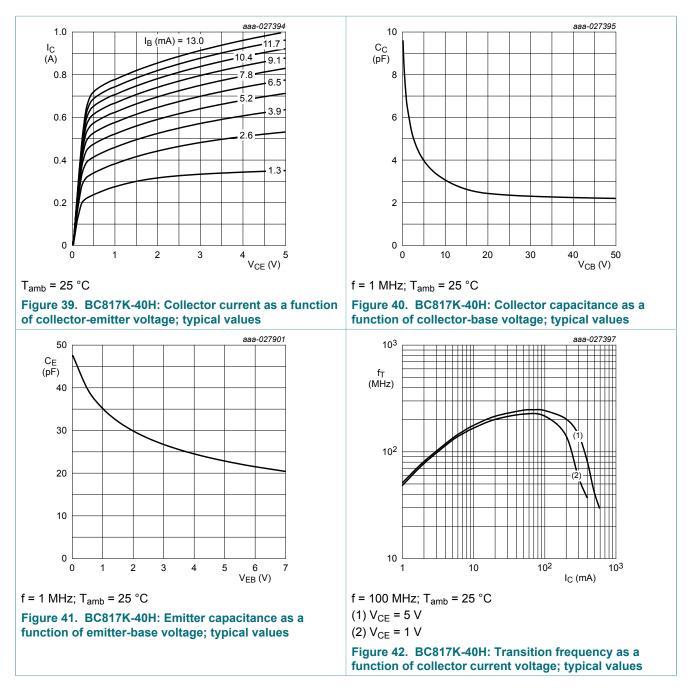


### **BC817KH series**



### **BC817KH series**

#### 45 V, 500 mA NPN general-purpose transistors



### 8 Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

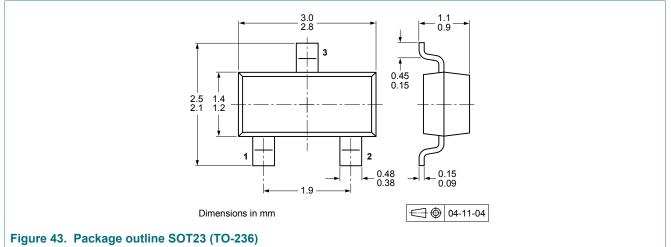
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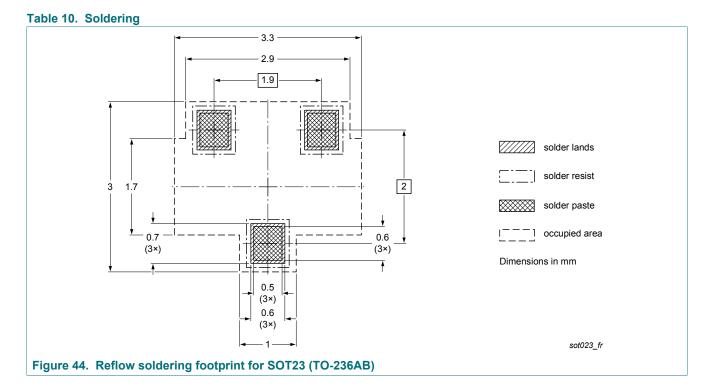
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### 9 Package outline

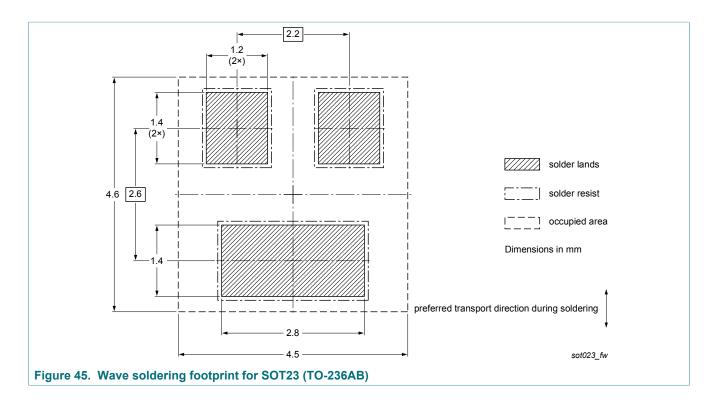
#### Table 9. Package outline



### **10 Soldering**



# **BC817KH series**



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### **11 Revision history**

#### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC817KH_SER v.1	20171215	Product data sheet	-	-

### 12 Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

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BC817KH SER **Product data sheet** 

#### 45 V, 500 mA NPN general-purpose transistors

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### **BC817KH series**

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