

# BC846DS 65 V, 100 mA NPN/NPN general-purpose transistor Rev. 01 — 17 July 2009 Produ

Product data sheet

#### **Product profile** 1.

# 1.1 General description

NPN/NPN general-purpose transistor pair in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package.

### 1.2 Features

- Low collector capacitance
- Low collector-emitter saturation voltage
- Closely matched current gain
- Reduces number of components and board space
- No mutual interference between the transistors
- AEC-Q101 qualified

### **1.3 Applications**

General-purpose switching and amplification

# 1.4 Quick reference data

#### Quick reference data Table 1.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per trans	istor					
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	65	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; $I_{C}$ = 2 mA	200	300	450	



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# 2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	emitter TR1		
2	base TR1		
3	collector TR2	0	
4	emitter TR2		
5	base TR2		
6	collector TR1		1 2 3
			sym020

# 3. Ordering information

Table 3. Order	ing inform	ation	
Type number	Package		
	Name	Description	Version
BC846DS	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457

# 4. Marking

Table 4. Marking codes	
Type number	Marking code
BC846DS	ZK

# 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per transis	tor				
V <sub>CBO</sub>	collector-base voltage	open emitter	-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	65	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	6	V
I <sub>C</sub>	collector current		-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 ms$	-	200	mA
I <sub>BM</sub>	peak base current	single pulse; $t_p \leq 1 \text{ ms}$	-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> _	250	mW
Per device					
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1] -	380	mW

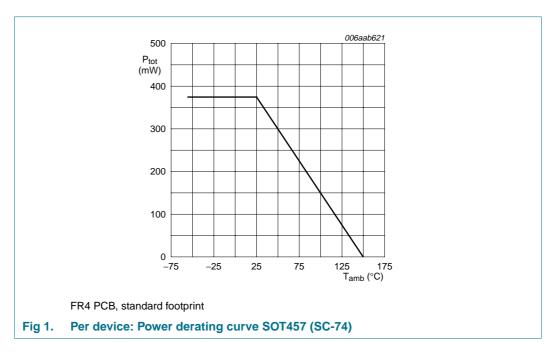
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Table 5.	Limiting	values	continued
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In accordance with the Absolute Maximum Rating System (IEC 60134).

			-		
Symbol	Parameter	Conditions	Min	Max	Unit
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



# 6. Thermal characteristics

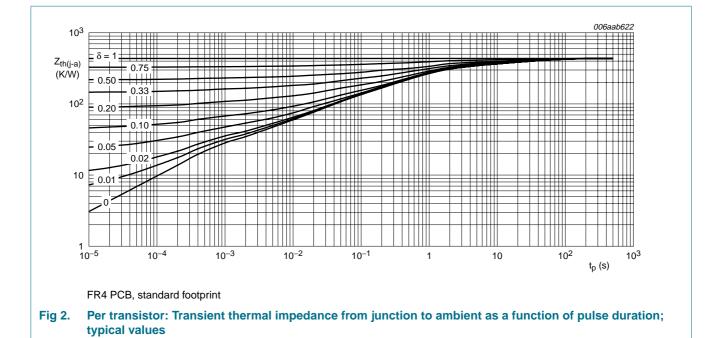
Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per trans	istor					
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	500	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	-	250	K/W
Per device						
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	328	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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# 7. Characteristics

#### Table 7.Characteristics

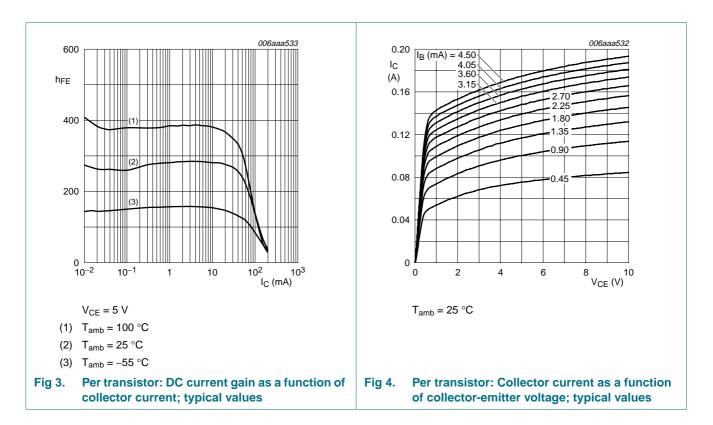
 $T_{amb} = 25 \circ C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per trans	sistor					
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}$	-	-	15	nA
current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$	-	-	5	μA	
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 6 V; I_C = 0 A$	-	-	100	nA
h <sub>FE</sub> DC current gain		$V_{CE} = 5 V$				
		I <sub>C</sub> = 10 μA	-	280	-	
		$I_{\rm C} = 2  \rm{mA}$	200	300	450	
V <sub>CEsat</sub>	collector-emitter	$I_{C} = 10 \text{ mA}; I_{B} = 0.5 \text{ mA}$	-	55	100	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA	-	200	300	mV
V <sub>BEsat</sub>	base-emitter	$I_{C} = 10 \text{ mA}; I_{B} = 0.5 \text{ mA}$	-	755	850	mV
	saturation voltage	$I_{C} = 100 \text{ mA}; I_{B} = 5 \text{ mA}$	-	1000	-	mV
V <sub>BE</sub>	base-emitter voltage	$V_{CE} = 5 V$				
		$I_{\rm C} = 2  \rm{mA}$	580	650	700	mV
		I <sub>C</sub> = 10 mA	-	-	770	mV

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$T_{amb} = 25$	$T_{amb}$ = 25 °C unless otherwise specified.					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; \text{ I}_{E} = \text{i}_{e} = 0 \text{ A};$ f = 1 MHz	-	1.9	-	pF
C <sub>e</sub>	emitter capacitance	$\label{eq:Veb} \begin{split} V_{EB} &= 0.5 \text{ V};  \text{I}_{C} = \text{i}_{c} = 0 \text{ A}; \\ \text{f} &= 1 \text{ MHz} \end{split}$	-	11	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz	100	-	-	MHz
NF	noise figure		-	1.9	-	dB
		$V_{CE} = 5 \text{ V}; I_C = 0.2 \text{ mA};$ $R_S = 2 \text{ k}\Omega; f = 1 \text{ kHz};$ B = 200  Hz	-	3.1	-	dB

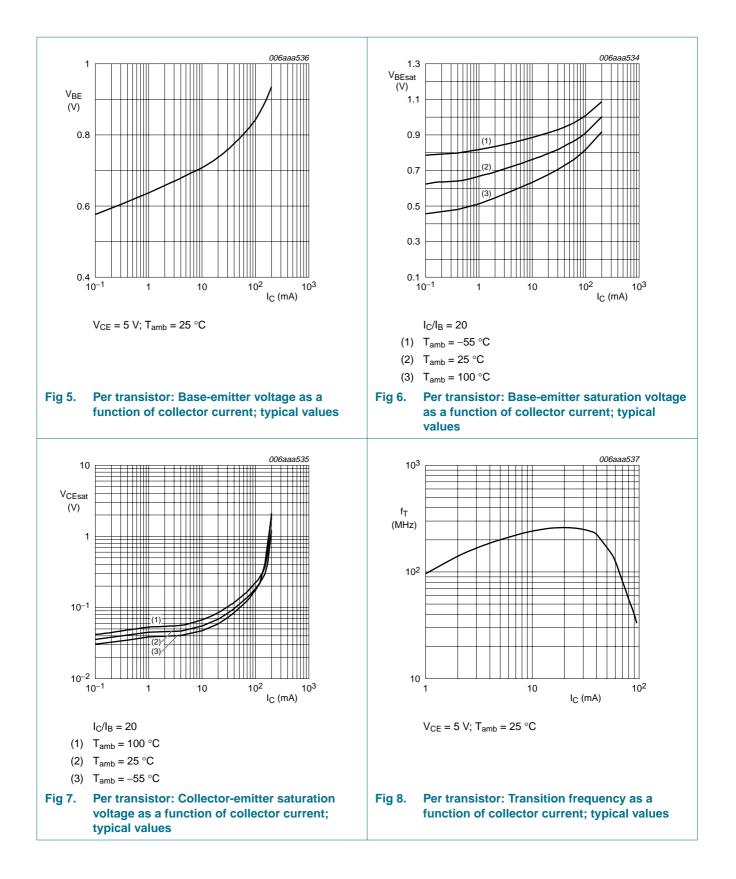
# Table 7. Characteristics ... continued



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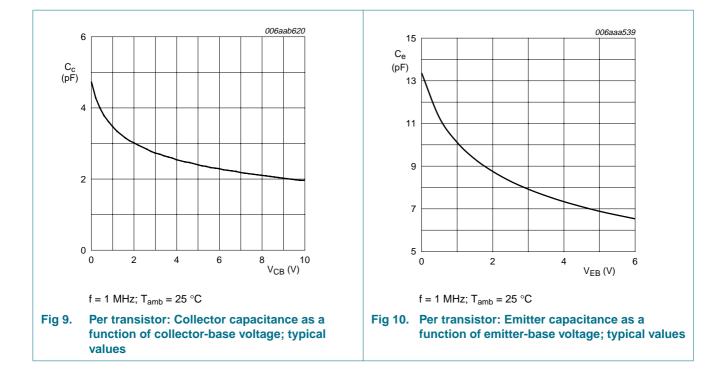
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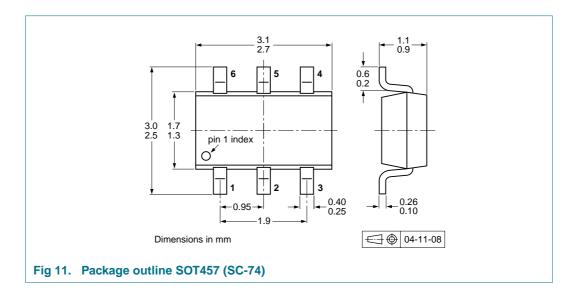
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# 8. Test information

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 9. Package outline

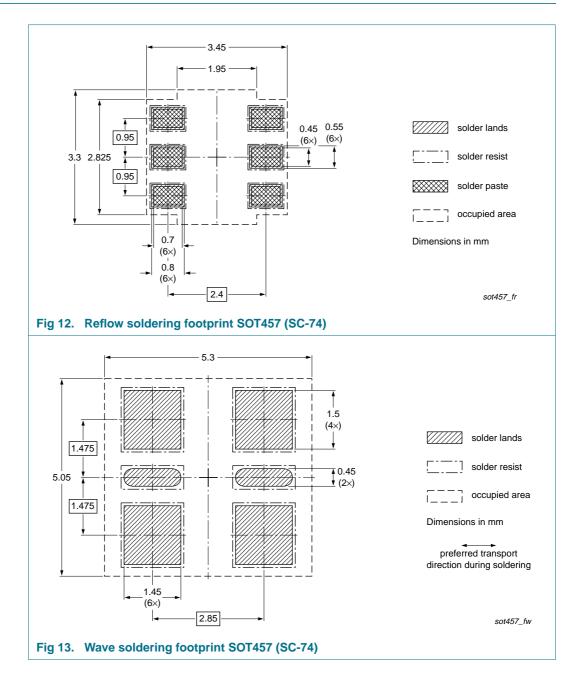


# **10. Packing information**

Please refer to packing information on www.nexperia.com.

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# **11. Soldering**



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# **12. Revision history**

Table 9. Revisio	on history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BC846DS	20090717	Product data sheet	-	-

# 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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