

BC847BPN

45 V, 100 mA NPN/PNP general-purpose transistor

Rev. 04 — 18 February 2009

Product data sheet

1. Product profile

1.1 General description

NPN/PNP general-purpose transistor pair in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

1.2 Features

- Low collector capacitance
- Low collector-emitter saturation voltage
- Closely matched current gain
- Reduces number of components and board space
- No mutual interference between the transistors

1.3 Applications

■ General-purpose switching and amplification

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per trans	istor; for the PNP transis	tor with negative polarity	/			
V_{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}$	200	-	450	

2. Pinning information

Table 2. Pinning

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	emitter TR1		
2	base TR1	654	6 5 4
3	collector TR2		TR2
4	emitter TR2	0	(TR1)
5	base TR2	□1 □2 □3	
6	collector TR1		1 2 3
			sym019



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3. Ordering information

Table 3. Ordering information

Type number	Package	Package			
	Name	Description	Version		
BC847BPN	SC-88	plastic surface-mounted package; 6 leads	SOT363		

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
BC847BPN	13*

^{[1] * = -:} made in Hong Kong

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit	
Per transis	Per transistor; for the PNP transistor with negative polarity					
V_{CBO}	collector-base voltage	open emitter	-	50	V	
V_{CEO}	collector-emitter voltage	open base	-	45	V	
V_{EBO}	emitter-base voltage	open collector	-	5	V	
I _C	collector current		-	100	mA	
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	200	mA	
I_{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	200	mA	
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	<u>[1]</u> _	220	mW	
			[2] _	250	mW	
Per device						
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	<u>[1]</u> _	300	mW	
			[2] _	400	mW	
Tj	junction temperature		-	150	°C	
T _{amb}	ambient temperature		-65	+150	°C	
T _{stg}	storage temperature		-65	+150	°C	

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

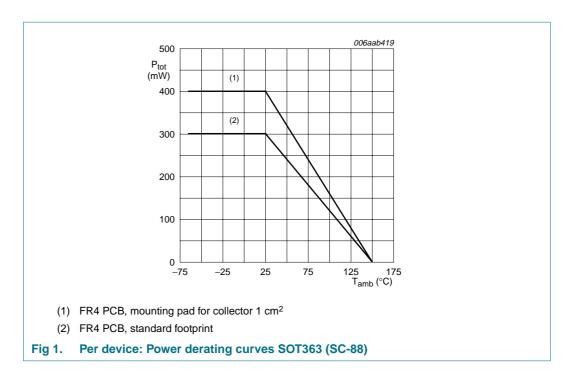
^{* =} p: made in Hong Kong

^{* =} t: made in Malaysia

^{* =} W: made in China

 $[\]label{eq:continuous} \ensuremath{\text{[2]}} \quad \text{Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm2.}$

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6. Thermal characteristics

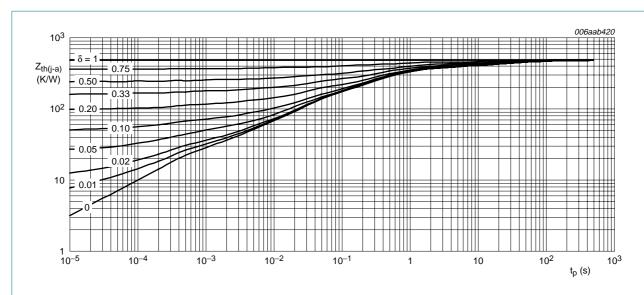
Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or					
R _{th(j-a)}	thermal resistance from junction to ambient		<u>[1]</u> _	-	568	K/W
			[2] _	-	500	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	230	K/W
Per device						
R _{th(j-a)}	thermal resistance from	in free air	<u>[1]</u> _	-	416	K/W
	junction to ambient		[2] _	-	313	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

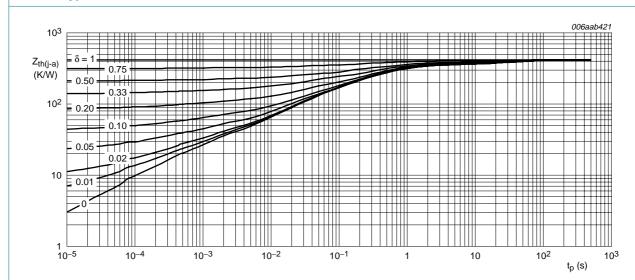
^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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FR4 PCB, standard footprint

Fig 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 1 cm²

Fig 3. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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7. Characteristics

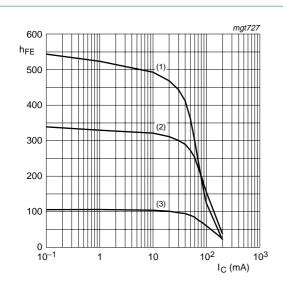
Table 7. Characteristics

 $T_{amb} = 25 \,^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per trans	Per transistor; for the PNP transistor with negative polarity					
I_{CBO}	collector-base cut-off	$V_{CB} = 30 \text{ V}; I_{E} = 0 \text{ A}$	-	-	15	nΑ
	current	$V_{CB} = 30 \text{ V; } I_E = 0 \text{ A;}$ $T_j = 150 ^{\circ}\text{C}$	-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$	-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}$	200	-	450	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	-	-	100	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA}$	[1] -	-	300	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	-	755	-	mV
V_{BE}	base-emitter voltage	$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$				
	TR1 (NPN)		580	655	700	mV
	TR2 (PNP)		600	655	750	mV
C _c	collector capacitance	$I_E = i_e = 0 A; V_{CB} = 10 V;$ f = 1 MHz				
	TR1 (NPN)		-	-	1.5	pF
	TR2 (PNP)		-	-	2.2	pF
C _e	emitter capacitance	$I_C = I_c = 0 \text{ A}; V_{EB} = 0.5 \text{ V};$ f = 1 MHz				
	TR1 (NPN)		-	11	-	pF
	TR2 (PNP)		-	10	-	pF
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V};$ f = 100 MHz	100	-	-	MHz

^[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

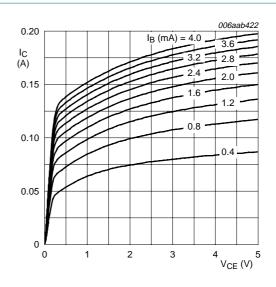
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$$V_{CE} = 5 V$$

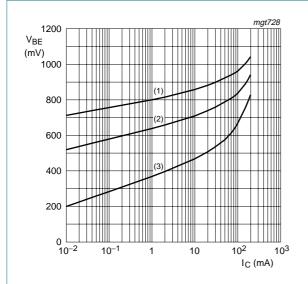
- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \,^{\circ}C$

Fig 4. TR1 (NPN): DC current gain as a function of collector current; typical values



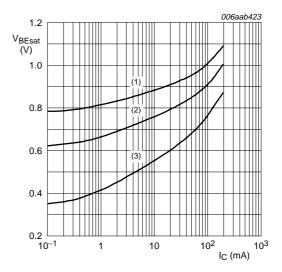
 $T_{amb} = 25 \, ^{\circ}C$

Fig 5. TR1 (NPN): Collector current as a function of collector-emitter voltage; typical values



- $V_{CE} = 5 V$
- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 6. TR1 (NPN): Base-emitter voltage as a function of collector current; typical values

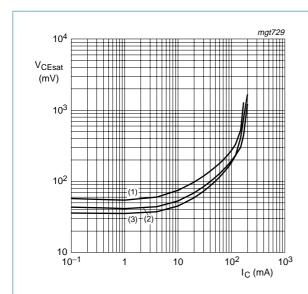


 $I_{\rm C}/I_{\rm B} = 20$

- (1) $T_{amb} = -55$ °C
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 7. TR1 (NPN): Base-emitter saturation voltage as a function of collector current; typical values

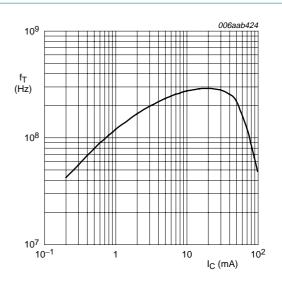
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$$I_{\rm C}/I_{\rm B} = 20$$

- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

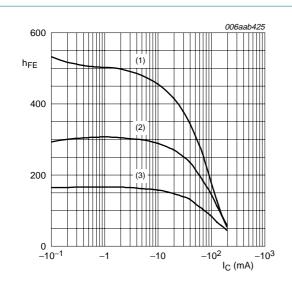
Fig 8. TR1 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values



$$V_{CE}$$
 = 5 V; f = 1 MHz; T_{amb} = 25 $^{\circ}C$

Fig 9. TR1 (NPN): Transition frequency as a function of collector current; typical values

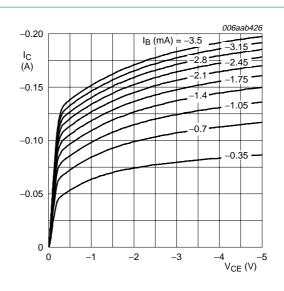
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$$V_{CE} = -5 \text{ V}$$

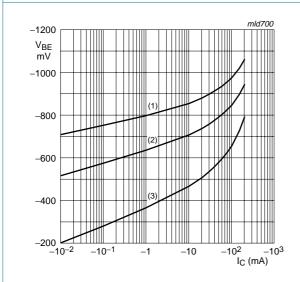
- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 10. TR2 (PNP): DC current gain as a function of collector current; typical values



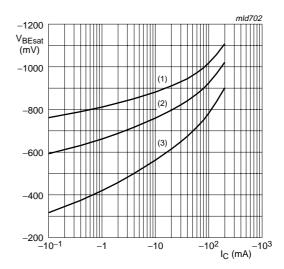
 T_{amb} = 25 °C

Fig 11. TR2 (PNP): Collector current as a function of collector-emitter voltage; typical values



- $V_{CE} = -5 \text{ V}$
- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 12. TR2 (PNP): Base-emitter voltage as a function of collector current; typical values

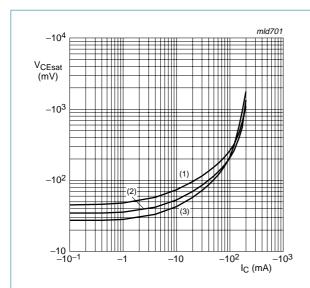


 $I_{\rm C}/I_{\rm B} = 20$

- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 13. TR2 (PNP): Base-emitter saturation voltage as a function of collector current; typical values

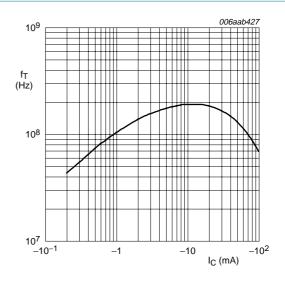
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$$I_{\rm C}/I_{\rm B} = 20$$

- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 14. TR2 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values

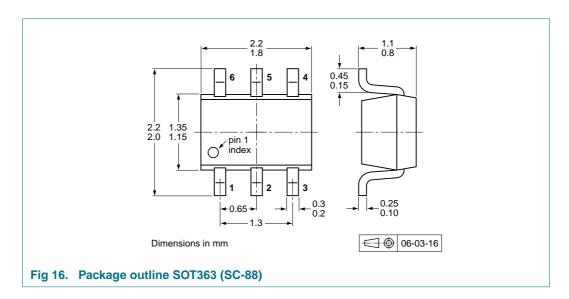


$$V_{CE} = -5 \text{ V}; f = 1 \text{ MHz}; T_{amb} = 25 \,^{\circ}\text{C}$$

Fig 15. TR2 (PNP): Transition frequency as a function of collector current; typical values

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8. Package outline



9. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description		Packing quantity	
				3000	10000
BC847BPN	SOT363	4 mm pitch, 8 mm tape and reel; T1	[2]	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	[3]	-125	-165

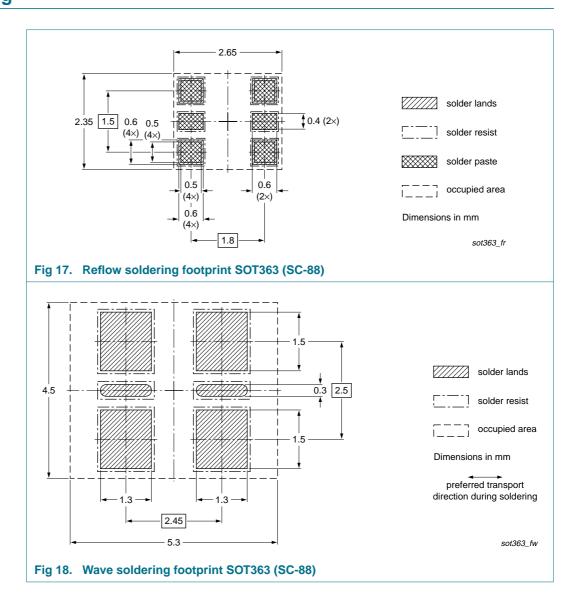
^[1] For further information and the availability of packing methods, see Section 13.

[2] T1: normal taping

[3] T2: reverse taping

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10. Soldering



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11. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BC847BPN_4	20090218	Product data sheet	-	BC847BPN_3		
Modifications:	 The format of NXP Sem 		edesigned to comply wi	th the new identity guidelines		
	 Legal texts have been adapted to the new company name where appropriate. 					
	Section 4 "Marking": updated					
	 Section 7 "Characteristics": enhanced 					
	Section 9 "Packing information": added					
	Section 10 "Soldering": added					
	 Section 12 "I 	<u>egal information":</u> updated				
BC847BPN_3	20011026	Product specification	-	BC847BPN_2		
BC847BPN_2	19990426	Preliminary specification	on -	BC847BPN_1		
BC847BPN_1	19970709	Preliminary specification	on -	-		

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12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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13. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to:

salesaddresses@nexperia.com

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