Product data sheet

1. General description

NPN/PNP transistor pair in a SOT666 plastic package.

2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm ultra thin package
- Excellent coplanarity due to straight leads
- Replaces two SC-75/SC-89 packaged transistors on same PCB area
- Reduced required PCB area
- · Reduced pick and place costs.
- AEC-Q101 qualified

3. Applications

- General purpose switching and amplification
- · Switch mode power supply complementary MOSFET driver
- · Complementary driver for audio amplifiers.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor	; for the PNP transistor	with negative polarity	•	'	'	'
V _{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	200	mA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA	200	-	450	



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		(TR1) TR2)
4	E2	emitter TR2	0	
5	B2	base TR2	1 2 3	E1 B1 C2
6	C1	collector TR1	SOT666	sym019

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BC847BVN		plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	SOT666			

7. Marking

Table 4. Marking codes

Type number	Marking code
BC847BVN	13

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or; for the PNP transistor wit	h negative polarity	,	'		
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
Per device	'	1				
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	Per transistor						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Reflow soldering is the only recommended soldering method.

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10. Characteristics

Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	tor; for the PNP transistor v	with negative polarity				
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA	200	-	450	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	100	mV
	saturation voltage	I_C = 100 mA; I_B = 5 mA; Pulsed test: $t_p \le$ 300 µs; $\delta \le$ 0.02	-	-	300	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	-	755	-	mV
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz	100	-	-	MHz
NPN transi	stor		'			
V_{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA	580	655	700	mV
Cc	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz	-	-	1.5	pF
C _e	emitter capacitance	$V_{EB} = 500 \text{ mV}; I_C = 0 \text{ A}; i_c = 0 \text{ A};$ f = 1 MHz	-	11	-	pF
PNP transi	stor		'			
V_{BE}	base-emitter voltage	V _{CE} = -5 V; I _C = -2 mA	-600	-655	-750	mV
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	2.2	pF
C _e	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz	-	10	-	pF

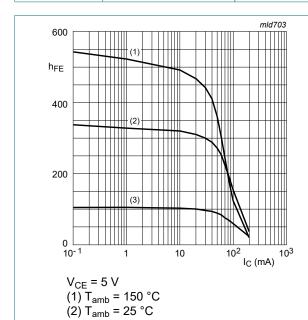


Fig. 1. NPN TR1: DC current gain as a function of collector current; typical values

(3) $T_{amb} = -55 \, ^{\circ}C$

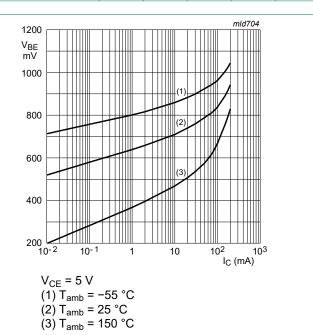
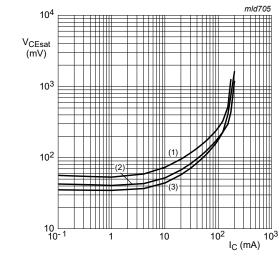


Fig. 2. NPN TR1: Base-emitter voltage as a function of collector current; typical values

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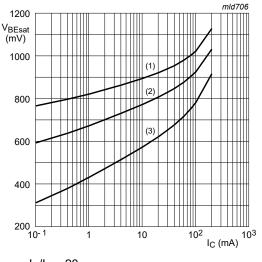


$$I_C/I_B = 20$$

$$(1) T_{amb} = 150^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

Fig. 3. **NPN TR1: Collector-emitter saturation voltage** as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

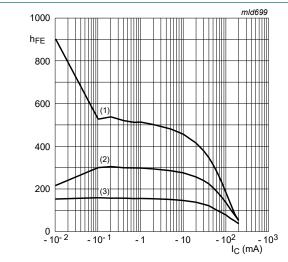
(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$I_{C}/I_{B} = 20$$

(1) $T_{amb} = -55 \,^{\circ}C$
(2) $T_{amb} = 25 \,^{\circ}C$
(3) $T_{amb} = 150 \,^{\circ}C$

NPN TR1: Base-emitter saturation voltage as a Fig. 4. function of collector current; typical values



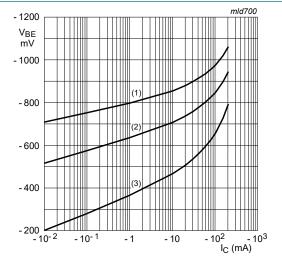
$$V_{CE}$$
 = -5 V

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

$$(2) T_{amb} = 25 °C$$

$$(3) T_{amb} = -55 °C$$

PNP TR2: DC current gain as a function of Fig. 5. collector current; typical values



$$V_{CE}$$
 = -5 V

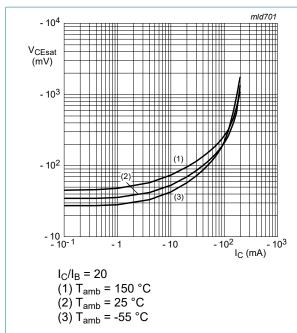
$$(1) T_{amb} = -55 °C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

PNP TR2: Base-emitter voltage as a function of Fig. 6. collector current; typical values

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PNP TR2: Collector-emitter saturation voltage Fig. 7. as a function of collector current; typical values

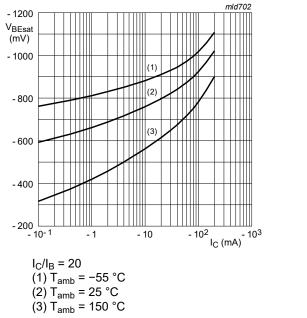


Fig. 8. PNP TR2: Base-emitter saturation voltage as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline

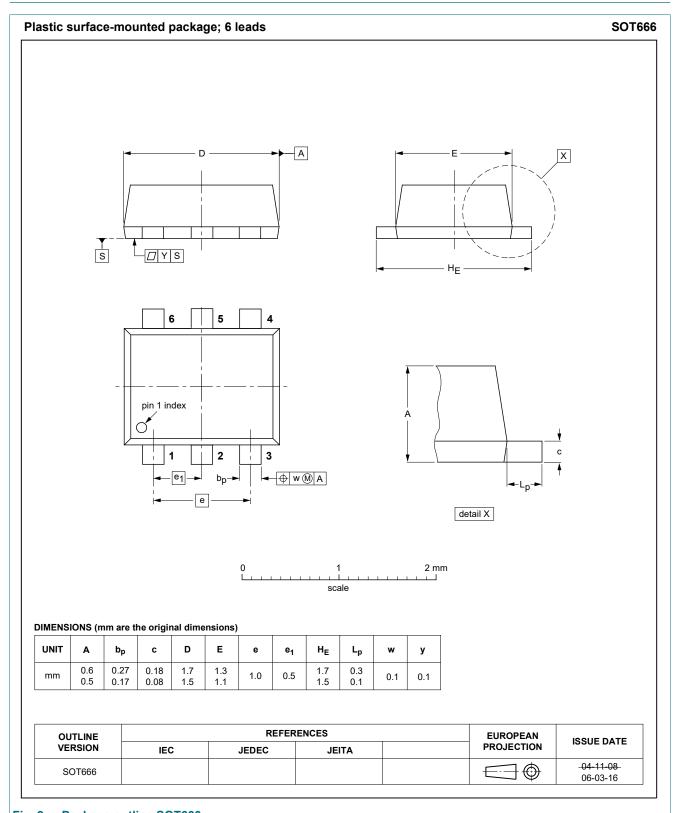
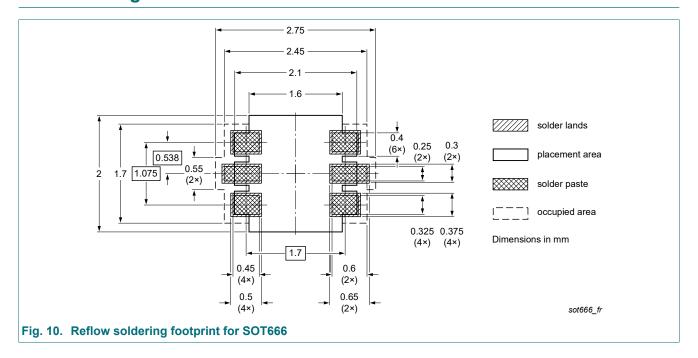


Fig. 9. Package outline SOT666

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13. Soldering



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14. Revision history

Table 8. Revision history

Table of Novicion motory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC847BVN v.3	20190520	Product data sheet	-	BC847BVN v.2			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
BC847BVN v.2	20011107	Product data sheet	-	BC847BVN v.1			
BC847BVN v.1	20010830	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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BC847BVN

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