1. General description

NPN general-purpose transistor in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	Package		PNP complement:
	Nexperia	JEDEC	
BC847AQB	SOT8015	MO-340BA	BC857AQB
BC847BQB			BC857BQB
BC847CQB			BC857CQB

2. Features and benefits

- · High power dissipation capability
- · Suitable for Automatic Optical Inspection (AOI) of solder joint
- · Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- Space restricted applications

4. Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	200	mA
h _{FE}	DC current gain					
	BC847AQB	V _{CE} = 5 V; I _C = 2 mA	110	-	220	
	BC847BQB		200	-	450	
	BC847CQB		420	-	800	



5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	Е	emitter	3	
3	С	collector		В
			1 2	E sym021
			Transparent top view	

6. Ordering information

Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BC847AQB	DFN1110D-3	plastic leadless extremely thin small outline package with	SOT8015			
BC847BQB		side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; body: 1.1 x 1.0 x 0.48 mm				
BC847CQB		50ay. 1.1 X 1.0 X 0.40 Hilli				

7. Marking

Table 5. Marking

Type number	Marking code
BC847AQB	A2
BC847BQB	A3
BC847CQB	A4

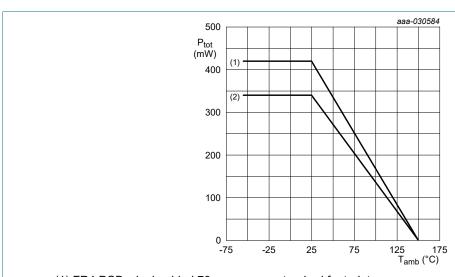
8. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
Ic	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	340	mW
			[2]	-	420	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided 70 µm copper; tin-plated and standard footprint.



- (1) FR4 PCB; single-sided 70 μm copper, standard footprint
- (2) FR4 PCB; single-sided 35 µm copper, standard footprint

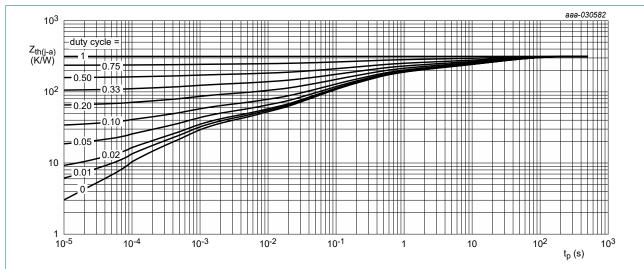
Fig. 1. Power derating curves DFN1110D-3 (SOT8015)

9. Thermal characteristics

Table 7. Thermal characteristics

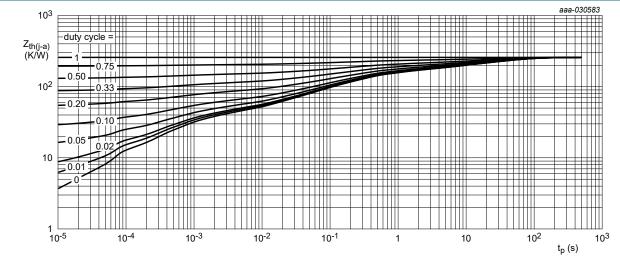
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	368	K/W
			[2]	-	-	298	K/W

- [1] Device mounted on an FR4 PCB; single-sided 35 µm copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided 70 μm copper; tin-plated and standard footprint.



FR4 PCB; single-sided 35 µm copper, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided 70 µm copper, standard footprint

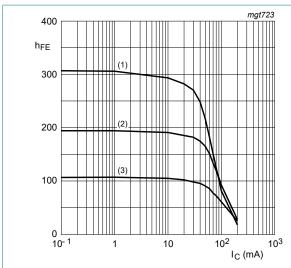
Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 8. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		50	-	-	V
V _{(BR)CES}	collector-emitter peak voltage	$I_C = 2 \text{ mA}; I_E = 0 \text{ A}$		45	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A		-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain				'		
	BC847AQB	V _{CE} = 5 V; I _C = 2 mA		110	-	220	
	BC847BQB			200	-	450	
	BC847CQB			420	-	800	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA		-	-	200	mV
	saturation voltage	I _C = 100 mA; I _B = 5 mA	[1]	-	-	400	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V ; I _C = 2 mA	[2]	580	-	700	mV
		V _{CE} = 5 V ; I _C = 10 mA	[2]	-	-	770	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA ; I _B = 0.5 mA		-	760	-	mV
	voltage	I _C = 100 mA ; I _B = 5 mA	[1]	-	900	-	mV
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz		100	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	-	1.5	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_E = I_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	11	-	pF
NF	noise figure	V_{CE} = 5 V; I_{C} = 200 μ A; R_{S} = 2 k Ω ; f = 1 kHz; B = 200 Hz		-	-	10	dB

 $[\]begin{array}{ll} [1] & \text{pulsed; } t_p \leq 300 \ \mu\text{s; } \delta \leq 0.02 \\ [2] & V_{BE} \ \text{decreases by about 2 mV/K with increasing temperature.} \end{array}$

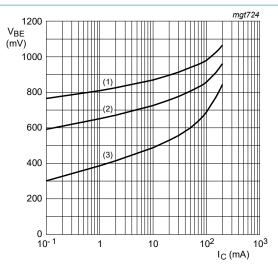


$$V_{CE} = 5 V$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 4. BC847AQB: DC current gain as a function of collector current; typical values



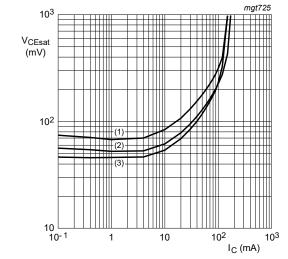
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. BC847AQB: Base-emitter voltage as a function of collector current; typical values



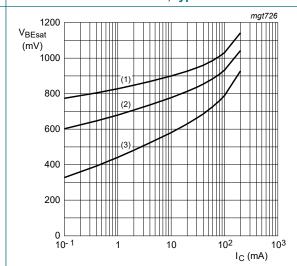
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = -55 °C

Fig. 6. BC847AQB: Collector-emitter saturation voltage as a function of collector current; typical values



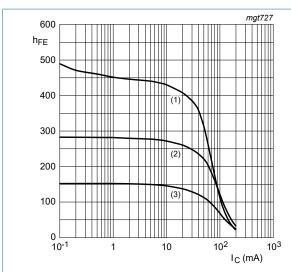
$$I_{\rm C}/I_{\rm B}$$
= 10

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

Fig. 7. BC847AQB: Base-emitter saturation voltage as a function of collector current; typical values



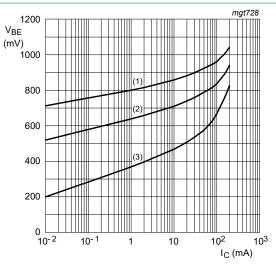
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

BC847BQB: DC current gain as a function of Fig. 8. collector current; typical values



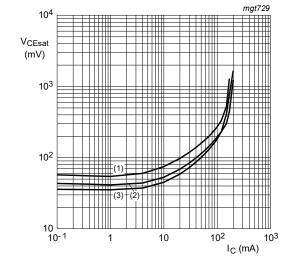
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

BC847BQB: Base-emitter voltage as a function Fig. 9. of collector current; typical values



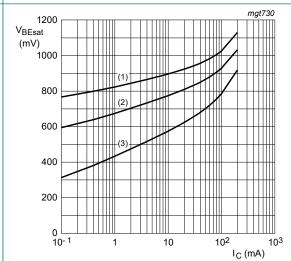
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

as a function of collector current; typical values



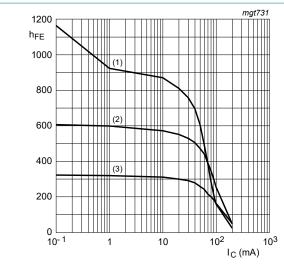
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 10. BC847BQB: Collector-emitter saturation voltage | Fig. 11. BC847BQB: Base-emitter saturation voltage as a function of collector current; typical values



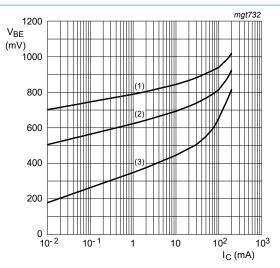
$$V_{CF} = 5 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 12. BC847CQB: DC current gain as a function of collector current; typical values



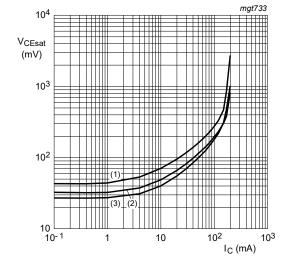
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

Fig. 13. BC847CQB: Base-emitter voltage as a function of collector current; typical values



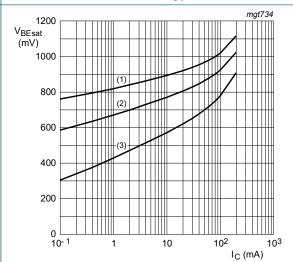
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb}$$
 = 150 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

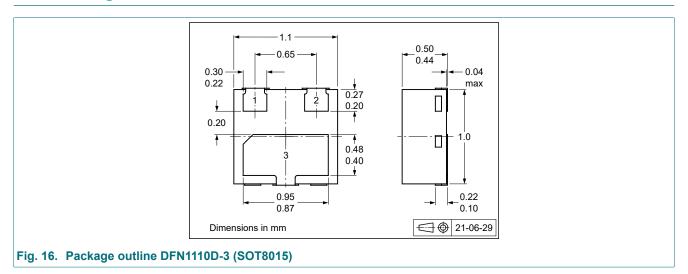
Fig. 14. BC847CQB: Collector-emitter saturation voltage | Fig. 15. BC847CQB: Base-emitter saturation voltage as a function of collector current; typical values

11. Test information

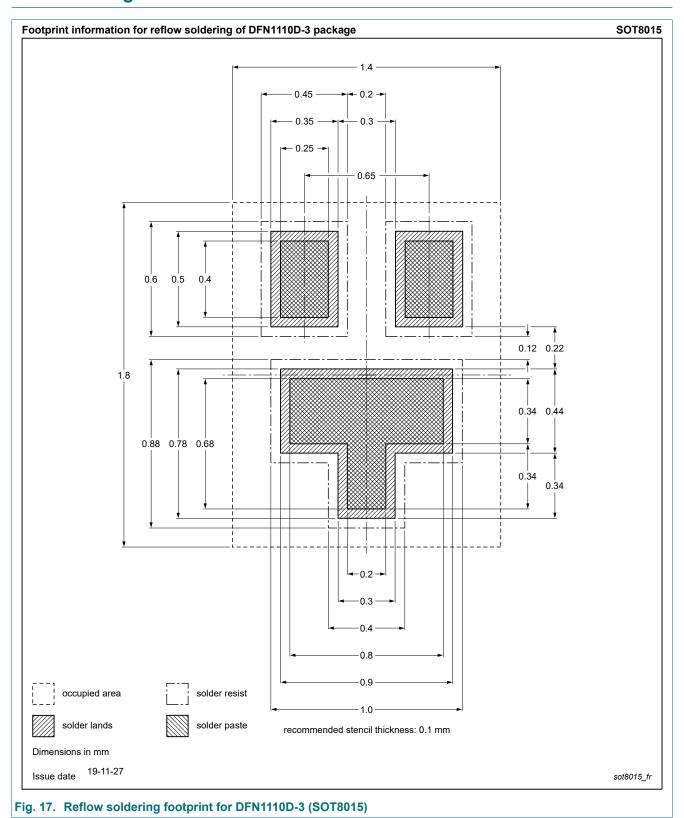
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 9. Revision history

Table 5. Itevision misto	y				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
BC847XQB_SER v.2	20210908	Product data sheet	-	BC847XQB_SER v.1	
Modifications:	 Pinning: Simplified outline replaced Characteristics: Conditions corrected at C_e Package outline: Drawing replaced by minimized version 				
BC847XQB_SER v.1	20200124	Product data sheet	-	-	

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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45 V, 100 mA NPN general-purpose transistor

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