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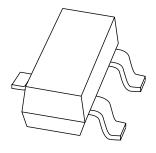
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Kind regards,

Team Nexperia

## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# BCW29; BCW30 PNP general purpose transistors

Product data sheet Supersedes data of 1999 Apr 13 2004 Jan 13



## PNP general purpose transistors

**BCW29**; **BCW30** 

#### **FEATURES**

• Low current (max. 100 mA)

• Low voltage (max. 32 V).

#### **APPLICATIONS**

• General purpose switching and amplification.

#### **DESCRIPTION**

PNP transistor in a SOT23 plastic package. NPN complements: BCW31 and BCW32.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
BCW29	C1*
BCW30	C2*

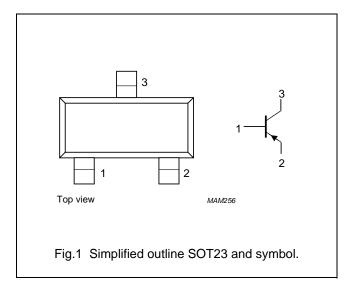
#### Note

1. \* = p: Made in Hong Kong.

\* = t : Made in Malaysia. \* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE	PACKAGE				
NUMBER	NAME	DESCRIPTION VERS			
BCW29	_	plastic surface mounted package; 3 leads	SOT23		
BCW30					

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-32	V
V <sub>CEO</sub>	collector-emitter voltage	open base; I <sub>C</sub> = −2 mA	_	-32	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	<b>-</b> 5	٧
I <sub>C</sub>	collector current (DC)		-	-100	mA
I <sub>CM</sub>	peak collector current		-	-200	mA
I <sub>BM</sub>	peak base current		_	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

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# PNP general purpose transistors

BCW29; BCW30

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	$I_E = 0$ ; $V_{CB} = -32 \text{ V}$	_	_	-100	nA
		$I_E = 0$ ; $V_{CB} = -32 \text{ V}$ ; $T_j = 100 \text{ °C}$	_	_	-10	μА
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = -5 V	_	_	-100	nA
h <sub>FE</sub>	DC current gain	$I_C = -10 \mu A; V_{CE} = -5 V$				
	BCW29		_	90	_	
	BCW30		_	150	_	
	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$				
	BCW29		120	_	260	
	BCW30		215	_	500	
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-80	-300	mV
V	voltage	$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}$	_	-150	_	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-720	_	mV
		$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}$	_	-810	_	mV
V <sub>BE</sub>	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	-	-750	mV
C <sub>c</sub>	collector capacitance	$I_E = I_e = 0$ ; $V_{CB} = -10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	4.5	_	pF
f <sub>T</sub>	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure	$I_C$ = -200 $\mu$ A; $V_{CE}$ = -5 V; $R_S$ = 2 $k\Omega$ ; $f$ = 1 $k$ Hz; $B$ = 200 Hz	_	_	10	dB

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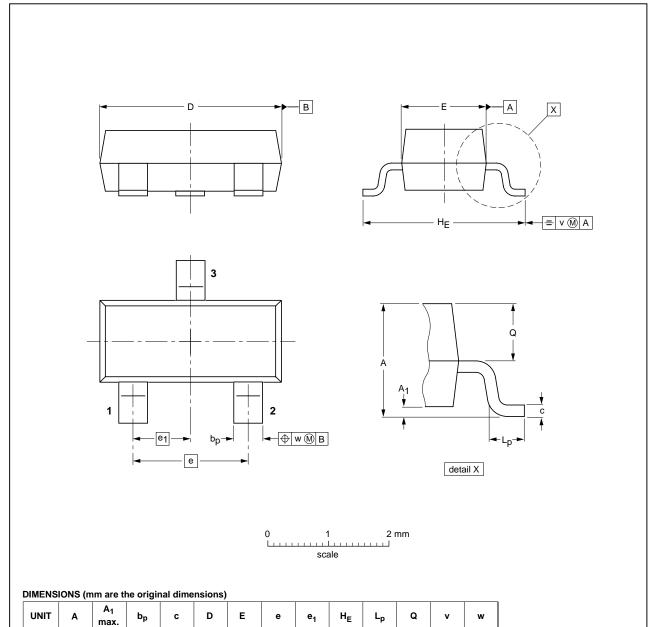
# PNP general purpose transistors

BCW29; BCW30

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION ISSUE DAT	
SOT23		TO-236AB				<del>04-11-04</del> 06-03-16

0.45

0.1

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0.38

0.9

### PNP general purpose transistors

BCW29; BCW30

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

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## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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