

60 V, 1 A NPN power bipolar transistors Rev. 1 — 22 August 2019

Product data sheet

1. Product profile

1.1. General description

NPN power transistors in a medium power SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package	PNP complement	
	Nexperia	JEDEC	
BCX55T	SOT89 SC-62		BCX52T
BCX55-10T			BCX52-10T
BCX55-16T			BCX52-16T

1.2. Features and benefits

- High collector current capability I_C and I_{CM}
- Three current gain selections
- High power dissipation capability
- AEC-Q101 qualified

1.3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- Power management
- Amplifiers

1.4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	60	V
I _C	collector current		-	-	1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	2	A

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60 V, 1 A NPN power bipolar transistors

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h _{FE}	DC current gain						
	BCX55T	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCX55-10T		[1]	63	-	160	
	BCX55-16T		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

2. Pinning information

Table 3. Pinning				
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		С
2	С	collector		в
3	В	base		
			3 2 1	E sym042

3. Ordering information

Table 4. Ordering information

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Type number	Package	Package					
	Name	Description	Version				
BCX55T	SC-62	plastic, surface-mounted package; 3 leads; 1.5 mm pitch;	SOT89				
BCX55-10T		4.5 mm x 2.5 mm x 1.5 mm body					
BCX55-16T							

4. Marking

Table 5. Marking				
Type number	Marking code			
BCX55T	B2			
BCX55-10T	B3			
BCX55-16T	B4			

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

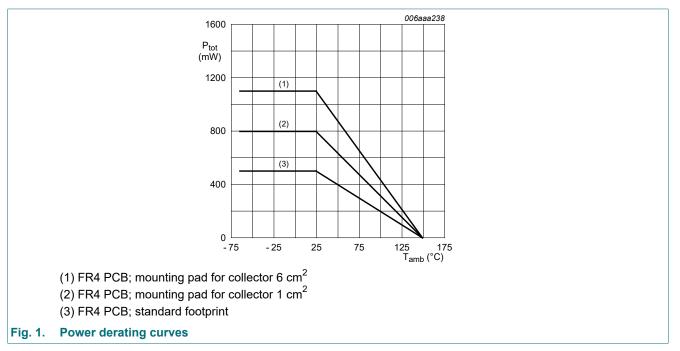
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		60	V
V _{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		2	А
I _B	base current			-	200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	300	mA
P _{tot}	total power dissipation	power dissipation $T_{amb} \le 25 \text{ °C}$		-	500	mW
			[2]	-	800	mW
			[3]	-	1100	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm². [2]

[3]



6. Thermal characteristics

Table 7. Thermal characteristics

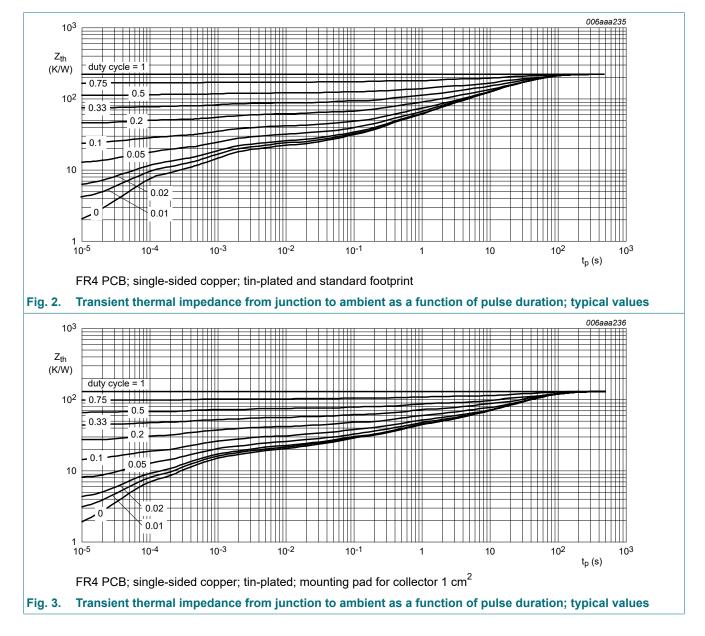
 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} tl	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]	-	-	157	K/W
			[3]	-	-	114	K/W

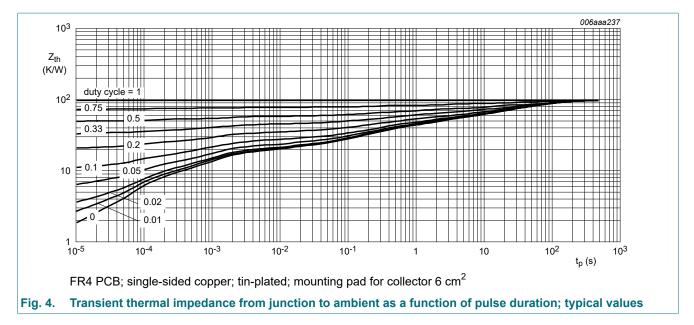
[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².

[3] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



60 V, 1 A NPN power bipolar transistors



7. Characteristics

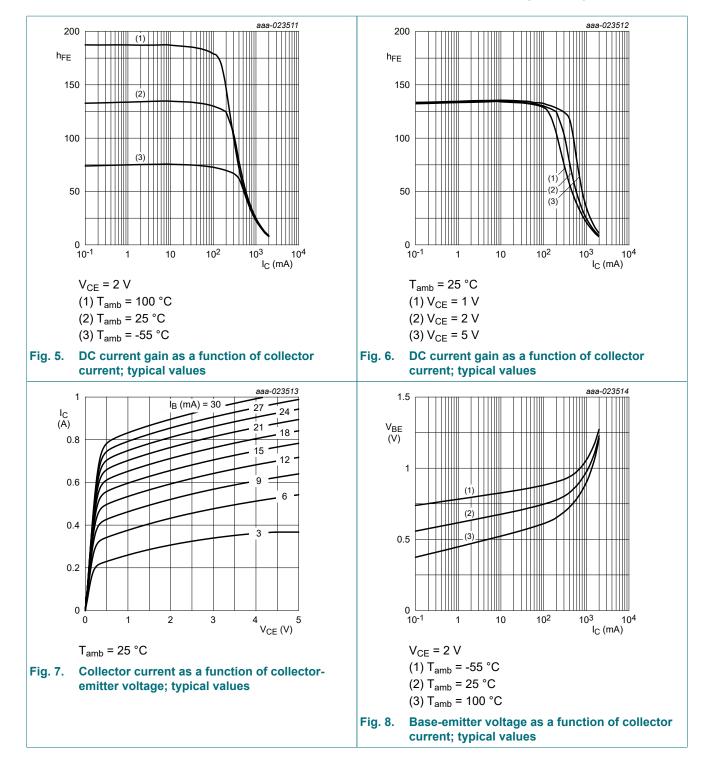
Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

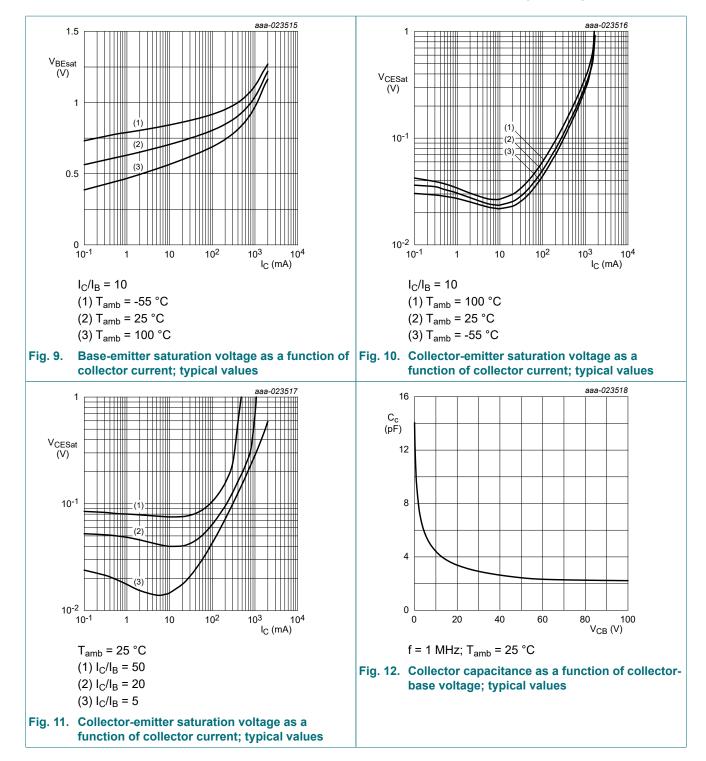
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		60	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _E = 0 A		60	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain						_
	BCX55T, -10T, -16T	V _{CE} = 2 V; I _C = 5 mA		63	-	-	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCX55T	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCX55-10T	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	160	
	BCX55-16T	V _{CE} = 2 V; I _C = 150 mA	[1]	100	-	250	_
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	500	mV
V _{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1]	-	-	1	V
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		-	155	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	4.5	-	pF

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

60 V, 1 A NPN power bipolar transistors



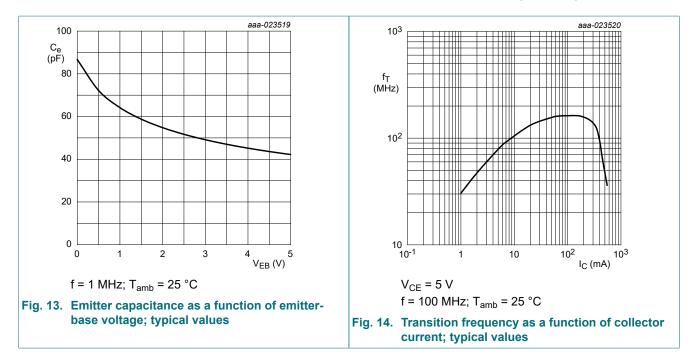
60 V, 1 A NPN power bipolar transistors



BCX55T_SER

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60 V, 1 A NPN power bipolar transistors

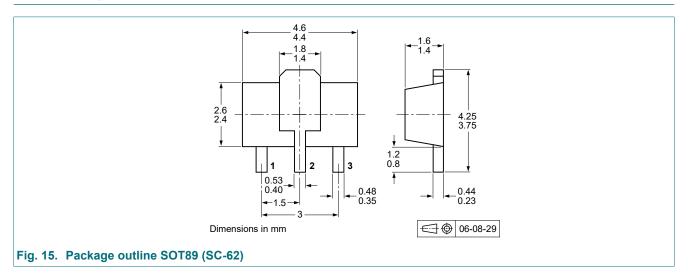


8. Test information

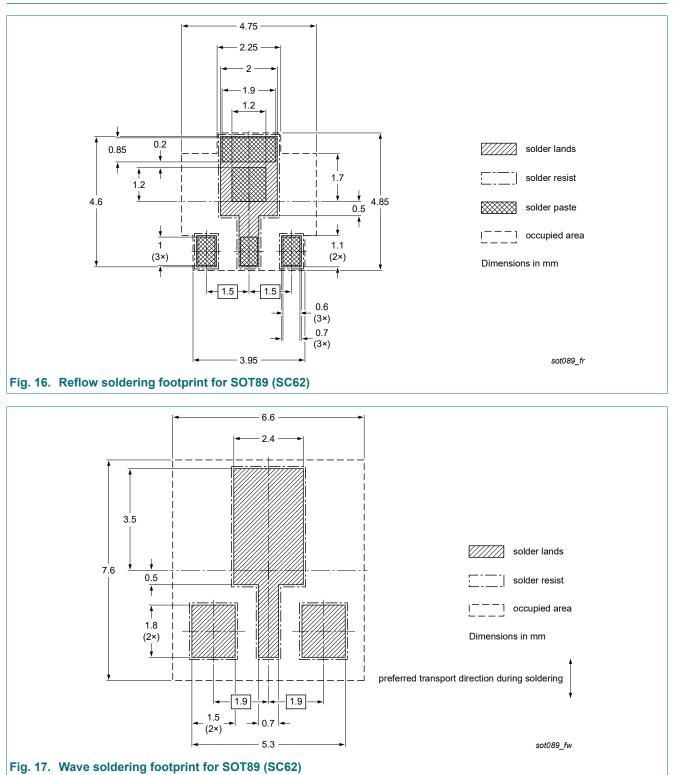
8.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

9. Package outline



10. Soldering



11. Revision history

Table 9. Revision history					
Document ID	Release date	Data sheet status	Change notice	Supersedes	
BCX55T_SER v.1	20190822	Product data sheet	-	-	

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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Contents

1
1
1
1
1
2
2
2
3
4
5
8
8
8
9
10
11

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