**Product data sheet** 

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

## 3. Applications

- Relay driver
- · High-speed line driver
- · Low-side load switch
- Switching circuits

## 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
$V_{GS}$	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-	1	Α
Static chara	acteristics		'			'	
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 1 \text{ A}; T_j = 25 \text{ °C}$		-	230	270	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



30 V, N-channel Trench MOSFET

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	SOT23	G S 017aaa255

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package						
	Name	Description	Version				
BSH103BK		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23				

# 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
BSH103BK	UQ%

[1] % = placeholder for manufacturing site code

30 V, N-channel Trench MOSFET

## 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
V <sub>GS</sub>	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	1	Α
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	0.6	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	4	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	330	mW
			[1]	-	480	mW
		T <sub>sp</sub> = 25 °C		-	2.1	W
T <sub>j</sub>	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-draii	n diode			1		,
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.4	Α

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

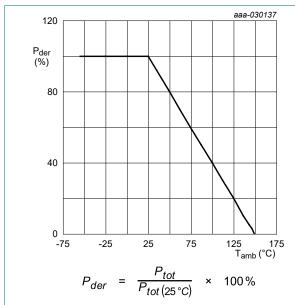


Fig. 1. Normalized total power dissipation as a function of ambient temperature

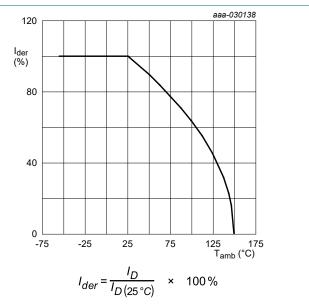


Fig. 2. Normalized continous drain current as a function of ambient temperature

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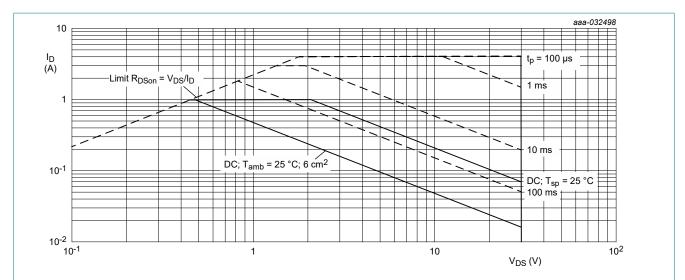


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

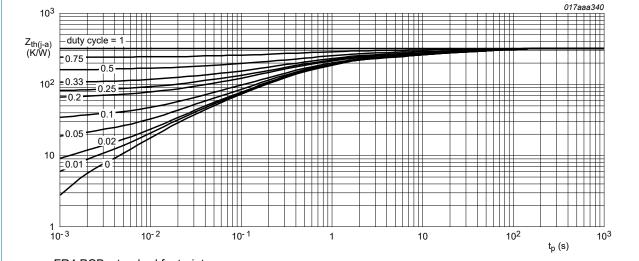
30 V, N-channel Trench MOSFET

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

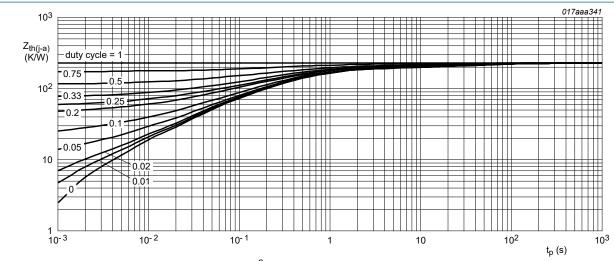
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	325	374	K/W
junction to ambient		[2]	-	227	260	K/W	
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	50	60	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 6 cm<sup>2</sup>

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

30 V, N-channel Trench MOSFET

# 10. Characteristics

## **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.75	1	1.25	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μΑ
		V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-10	μΑ
		$V_{GS} = 4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μΑ
		$V_{GS} = -4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	-1	μΑ
R <sub>DSon</sub> drain-source resistance	drain-source on-state	$V_{GS} = 4.5 \text{ V}; I_D = 1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	230	270	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 150 °C	-	380	450	mΩ
		$V_{GS} = 2.5 \text{ V}; I_D = 0.9 \text{ A}; T_j = 25 \text{ °C}$	-	295	355	mΩ
		$V_{GS} = 1.8 \text{ V}; I_D = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	470	670	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS} = 5 \text{ V}; I_D = 1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	2.9	-	S
$R_{G}$	gate resistance	f = 1 MHz	-	18.3	-	Ω
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 1 A; V <sub>GS</sub> = 4.5 V;	-	8.0	1.2	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.1	-	nC
$Q_{GD}$	gate-drain charge		-	0.2	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	79.3	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	11.8	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	7.6	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 1 A; V <sub>GS</sub> = 4.5 V;	-	2	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	3	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	6	-	ns
t <sub>f</sub>	fall time		-	3	-	ns
Source-dra	in diode		'			
$V_{SD}$	source-drain voltage	I <sub>S</sub> = 0.4 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.7	1.2	V

#### 30 V, N-channel Trench MOSFET

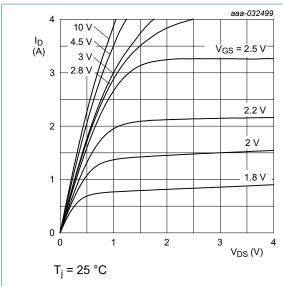


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

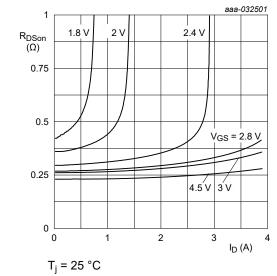


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

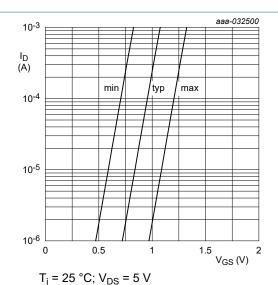


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

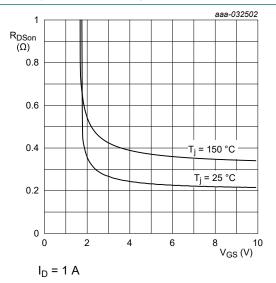


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

#### 30 V, N-channel Trench MOSFET

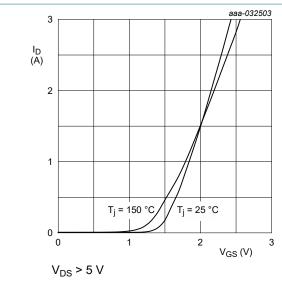


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

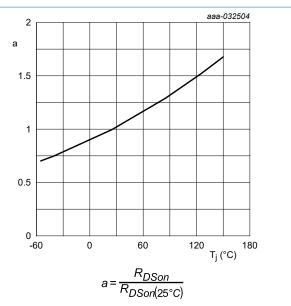


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

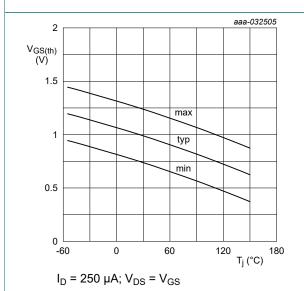


Fig. 12. Gate-source threshold voltage as a function of junction temperature

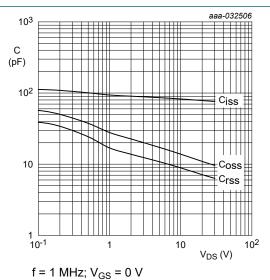


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

### 30 V, N-channel Trench MOSFET

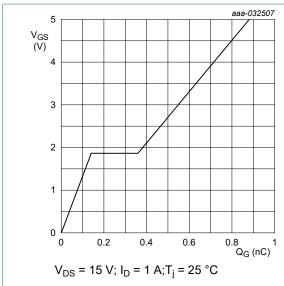


Fig. 14. Gate-source voltage as a function of gate charge; typical values

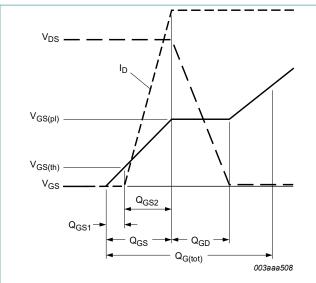


Fig. 15. Gate charge waveform definitions

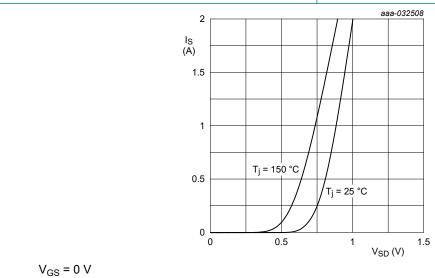
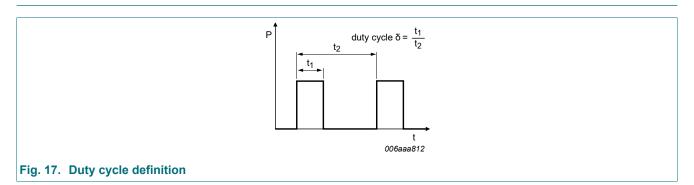


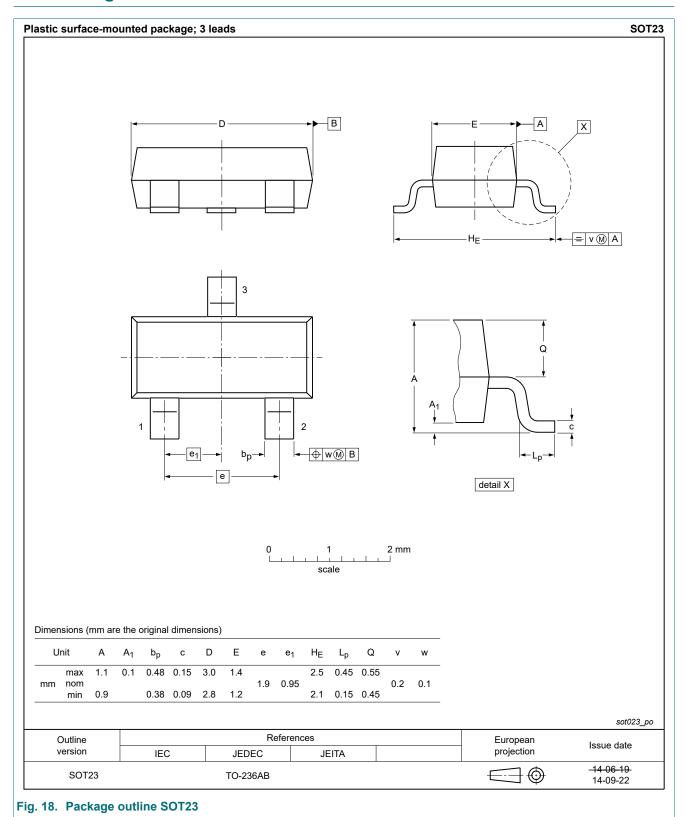
Fig. 16. Source current as a function of source-drain voltage; typical values

## 11. Test information



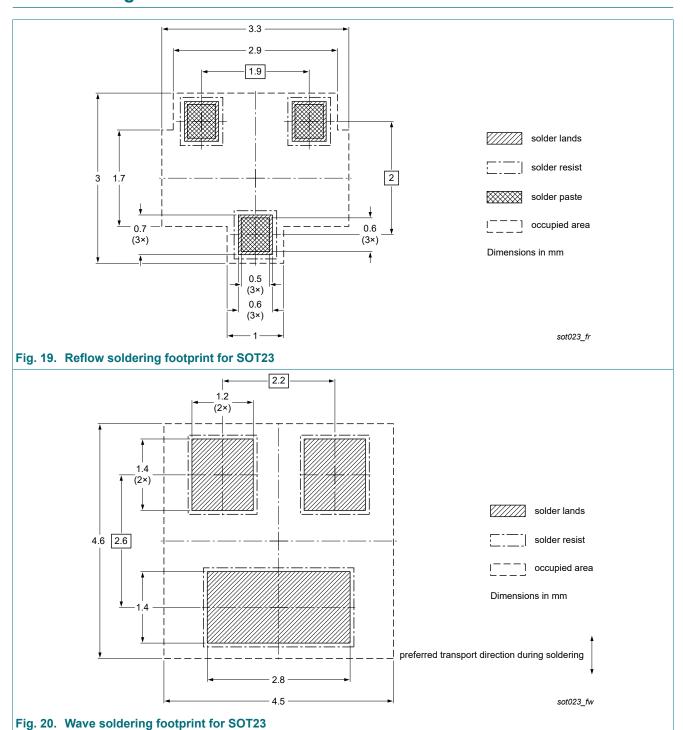
30 V, N-channel Trench MOSFET

# 12. Package outline



30 V, N-channel Trench MOSFET

# 13. Soldering



## 14. Revision history

**Table 8. Revision history** 

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSH103BK v.1	20210212	Product data sheet	-	-

BSH103BK

#### 30 V, N-channel Trench MOSFET

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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## 30 V, N-channel Trench MOSFET

## **Contents**

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal characteristics	5
10. Characteristics	6
11. Test information	9
12. Package outline	10
13. Soldering	11
14. Revision history	11
15. Legal information	

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