

BUK7M27-80E

N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33
19 September 2016 Product data sheet

1. General description

Standard level N-channel MOSFET in an LFPAK33 (Power33) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

2. Features and benefits

- Q101 Compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True standard level gate with V_{GS(th)} rating of greater than 1 V at 175 °C

3. Applications

- 12 V, 24 V and 48 V automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		-	-	80	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	30	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>		-	-	62	W
Static characteristics							
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 25 \text{ °C}; Fig. 11$		-	22	27	mΩ
Dynamic characteristics							
Q_{GD}	gate-drain charge	I _D = 5 A; V _{DS} = 64 V; V _{GS} = 10 V; T _j = 25 °C; <u>Fig. 13</u> ; <u>Fig. 14</u>		-	6.2	-	nC



N-channel 80 V, 27 m Ω standard level MOSFET in LFPAK33

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline		(Graphic symbol
1	S	Source				D I
2	S	Source				
3	S	Source				G T T
4	G	Gate				mbb076 S
mb	D	Mounting base; connected to drain	LI	FPAK33 (SOT1210)		

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BUK7M27-80E	LFPAK33	Plastic single ended surface mounted package (LFPAK33); 8 leads	SOT1210			

7. Marking

Table 4. Marking codes

Type number	Marking code
BUK7M27-80E	72780E

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C	-	80	V
V_{DGR}	drain-gate voltage	R_{GS} = 20 k Ω	-	80	V
V_{GS}	gate-source voltage	DC; T _j ≤ 175 °C	-20	20	V
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>	-	62	W
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 2</u>	-	30	Α
		V _{GS} = 10 V; T _{mb} = 100 °C; <u>Fig. 2</u>	-	21.3	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 \text{ °C}$; Fig. 3	-	121	Α
T _{stg}	storage temperature		-55	175	°C

BUK7M27-80E

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N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33

Symbol	Parameter	Conditions		Min	Max	Unit	
T _j	junction temperature			-55	175	°C	
Source-drain diode							
I _S	source current	T _{mb} = 25 °C		-	30	Α	
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	121	Α	
Avalanche ruggedness							
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 30 A; V_{sup} ≤ 80 V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped; Fig. 4	[1][2]	-	28.3	mJ	

- [1] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.
- [2] Refer to application note AN10273 for further information.

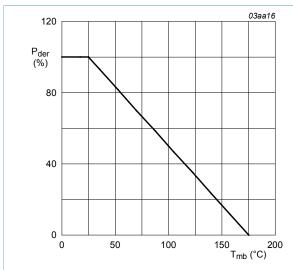


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

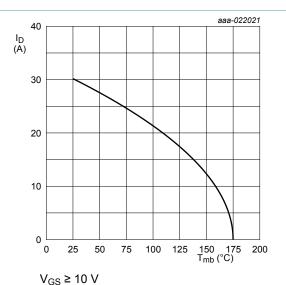
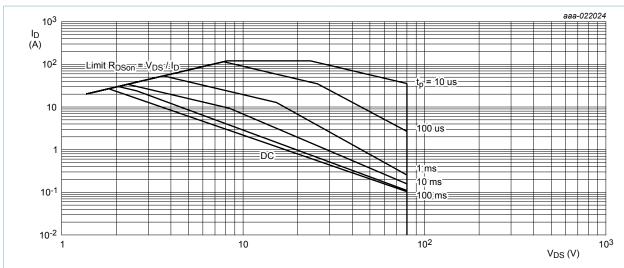


Fig. 2. Continuous drain current as a function of mounting base temperature

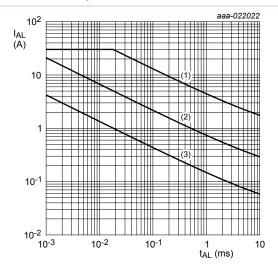
$$I_D = 30A \times \sqrt{\frac{175^{\circ}C - T_{mb}}{150^{\circ}C}} \text{ for } T_{mb} \ge 25^{\circ}C$$

N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33



 T_{mb} = 25 °C; I_{DM} is a single pulse

Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage



(1) $T_{j \text{ (init)}}$ = 25 °C; (2) $T_{j \text{ (init)}}$ = 150 °C; (3) Repetitive Avalanche

Fig. 4. Avalanche rating; avalanche current as a function of avalanche time

9. Thermal characteristics

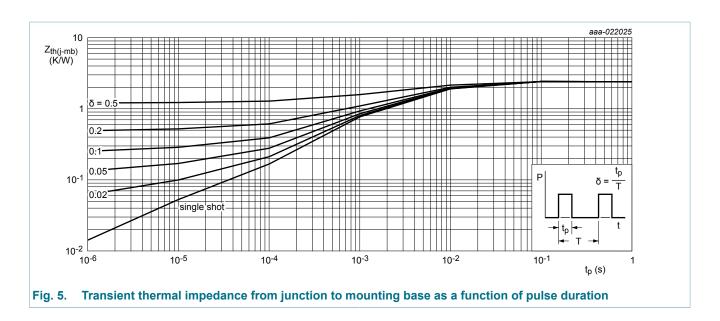
Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 5	-	2.01	2.43	K/W

BUK7M27-80E

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N-channel 80 V, 27 m Ω standard level MOSFET in LFPAK33



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		,			
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	80	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	72	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 9; Fig. 10	2.4	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 9	-	-	4.5	V
	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	1	-	-	V	
I _{DSS}	drain leakage current	V _{DS} = 80 V; V _{GS} = 0 V; T _j = 25 °C	-	0.01	1	μA
		V _{DS} = 80 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 5 A; T _j = 25 °C; <u>Fig. 11</u>	-	22	27	mΩ
resistance		V _{GS} = 10 V; I _D = 5 A; T _j = 175 °C; Fig. 12	-	-	68	mΩ
Dynamic ch	naracteristics		,	'		
Q _{G(tot)}	total gate charge	I _D = 5 A; V _{DS} = 64 V; V _{GS} = 10 V;	-	19.5	-	nC
Q_{GS}	gate-source charge	T _j = 25 °C; <u>Fig. 13</u> ; <u>Fig. 14</u>	-	4.1	-	nC
Q_{GD}	gate-drain charge		-	6.2	-	nC

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
C _{iss}	input capacitance	$V_{DS} = 25 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$		-	982	1306	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 15</u>		-	109	130	pF
C _{rss}	reverse transfer capacitance	_		-	65	89	pF
t _{d(on)}	turn-on delay time	V_{DS} = 60 V; R_{L} = 2 Ω ; V_{GS} = 10 V; $R_{G(ext)}$ = 5 Ω ; T_{j} = 25 °C		-	5.6	-	ns
t _r	rise time			-	8.6	-	ns
t _{d(off)}	turn-off delay time			-	13.5	-	ns
t _f	fall time	1		-	8.7	-	ns
Source-dra	in diode	1					-
V_{SD}	source-drain voltage	$I_S = 5 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}; Fig. 16$		-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 5 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$		-	21.6	-	ns
Q _r	recovered charge	V _{DS} = 25 V; T _j = 25 °C		-	24.2	-	nC

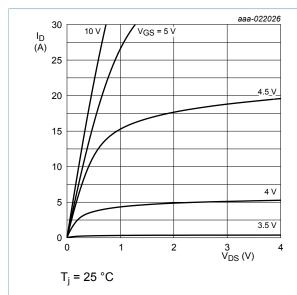


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

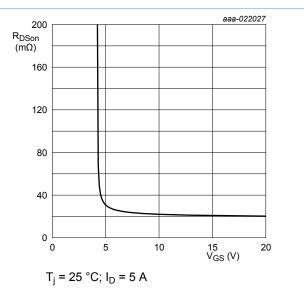


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

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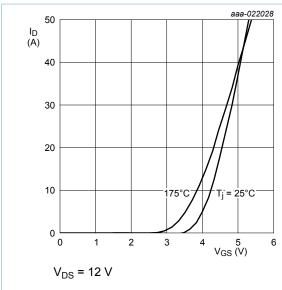


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

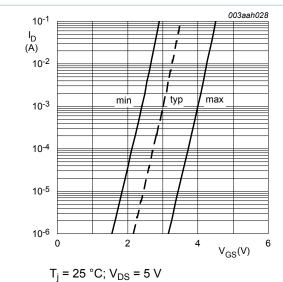


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

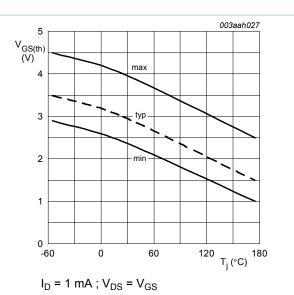


Fig. 9. Gate-source threshold voltage as a function of junction temperature

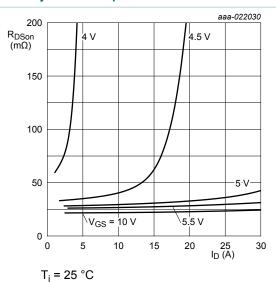


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

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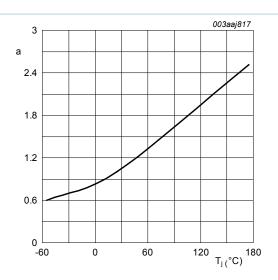


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

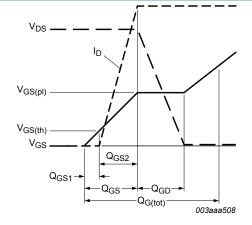
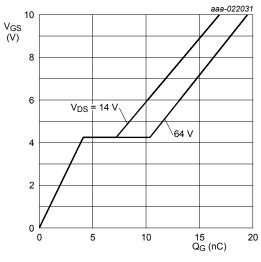
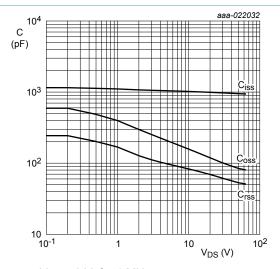


Fig. 14. Gate charge waveform definitions



 $T_j = 25 \,^{\circ}C; I_D = 5 \,^{\circ}A$

Fig. 13. Gate-source voltage as a function of gate charge; typical values

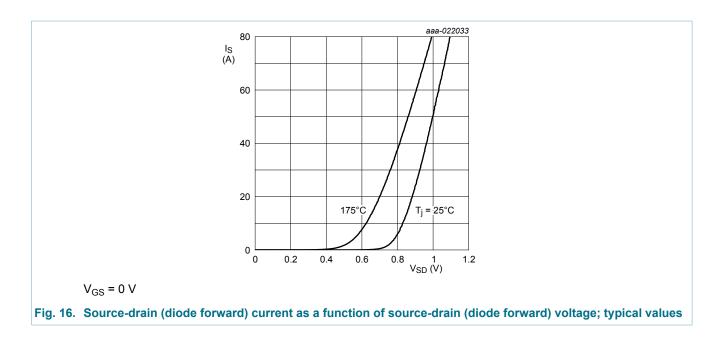


 $V_{GS} = 0 V; f = 1 MHz$

Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

8 / 13

N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33

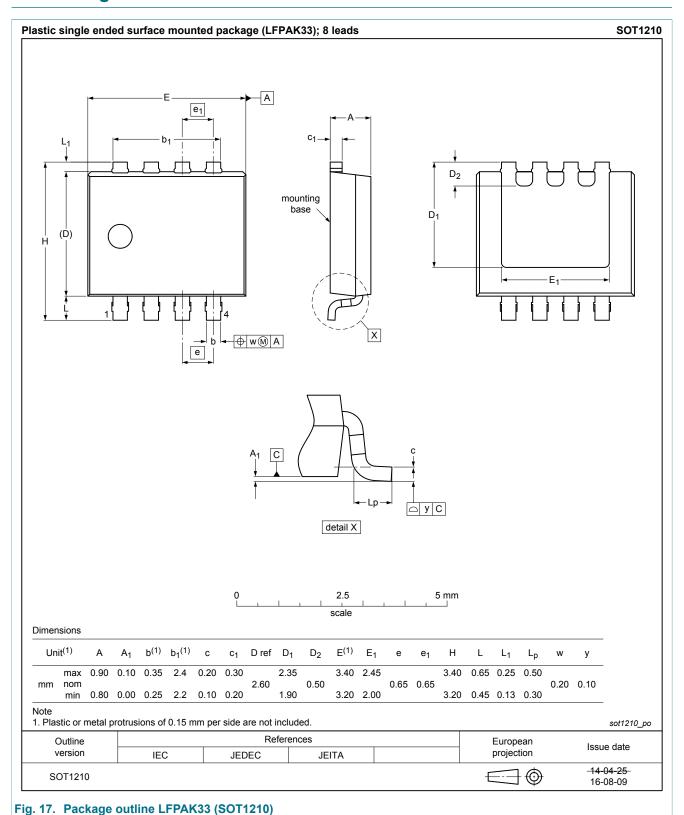


11. Application information

For guidance on how to use and understand this datasheet, please refer to application note <u>AN11158</u> "Understanding power MOSFET datasheet parameters".

N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33

12. Package outline



BUK7M27-80E

N-channel 80 V, 27 mΩ standard level MOSFET in LFPAK33

13. Legal information

13.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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N-channel 80 V, 27 m Ω standard level MOSFET in LFPAK33

14. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	
8	Limiting values	2
9	Thermal characteristics	
10	Characteristics	5
11	Application information	9
12	Package outline	10
13	Legal information	11
13.1	Data sheet status	11
13.2	Definitions	11
13.3	Disclaimers	11
13.4	Trademarks	12

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