HEF4001B Quad 2-input NOR gate Rev. 11 — 19 November 2021

Product data sheet

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1. General description

The HEF4001B is a quad 2-input NOR gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{DD} .

2. Features and benefits

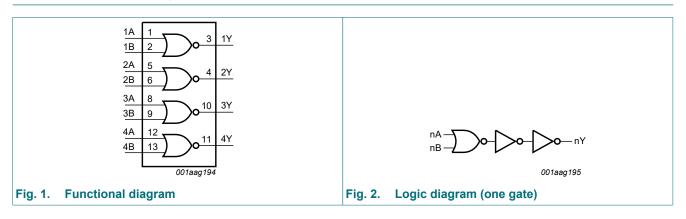
- Wide supply voltage range from 3.0 V to 15.0 V
- CMOS low power dissipation
- High noise immunity
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Complies with JEDEC standard JESD 13-B
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-B exceeds 200 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

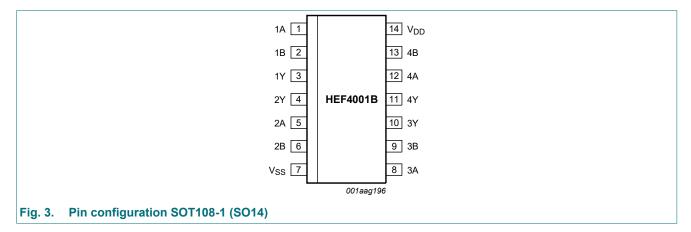
Type number	Package			
	Temperature range	Name	Description	Version
HEF4001BT	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1

4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A	1, 5, 8, 12	input
1B, 2B, 3B, 4B	2, 6, 9, 13	input
1Y, 2Y, 3Y, 4Y	3, 4, 10, 11	output
V _{SS}	7	ground (0 V)
V _{DD}	14	supply voltage

6. Functional description

Table 3. Function table

H = *HIGH* voltage level; *L* = *LOW* voltage level.

Input C		Output
nA	nB	nY
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to V_{SS} = 0 V (ground).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DD}	supply voltage		-0.5	+18	V
I _{IK}	input clamping current	V_{I} < -0.5 V or V_{I} > V_{DD} + 0.5 V	-	±10	mA
VI	input voltage		-0.5	V _{DD} + 0.5	V
I _{OK}	output clamping current	V_{O} < -0.5 V or V_{O} > V_{DD} + 0.5 V	-	±10	mA
I _{I/O}	input/output current		-	±10	mA
I _{DD}	supply current		-	50	mA
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature		-40	+125	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to + 125 °C [1] -	500	mW
Р	power dissipation	per output	-	100	mW

[1] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C.

8. Recommended operating conditions

Table 5. Re	commended operating conditions					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DD}	supply voltage		3	-	15	V
VI	input voltage		0	-	V _{DD}	V
T _{amb}	ambient temperature	in free air	-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{DD} = 5 V	-	-	3.75	µs/V
		V _{DD} = 10 V	-	-	0.5	µs/V
		V _{DD} = 15 V	-	-	0.08	µs/V

9. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 V$; $V_I = V_{SS}$ or V_{DD} ; unless otherwise specified.

Symbol	Parameter	Conditions	V_{DD}	T _{amb} = -40 °C		T _{amb} = -40 °C T _{amb} = +25 °C		T _{amb} = +85 °C		T _{amb} = +125 °C		Unit
				Min	Мах	Min	Мах	Min	Мах	Min	Мах	
V _{IH}	HIGH-level	I _O < 1 μA	5 V	3.5	-	3.5	-	3.5	-	3.5	-	V
	input voltage		10 V	7.0	-	7.0	-	7.0	-	7.0	-	V
			15 V	11.0	-	11.0	-	11.0	-	11.0	-	V
V _{IL}	LOW-level input voltage	I _O < 1 μA	5 V	-	1.5	-	1.5	-	1.5	-	1.5	V
			10 V	-	3.0	-	3.0	-	3.0	-	3.0	V
			15 V	-	4.0	-	4.0	-	4.0	-	4.0	V
V _{OH}	HIGH-level I _O <	I ₀ < 1 μΑ	5 V	4.95	-	4.95	-	4.95	-	4.95	-	V
	output voltage	voltage	10 V	9.95	-	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	14.95	-	V

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Symbol	Parameter	Conditions	V _{DD}	T _{amb} =	-40 °C	T _{amb} =	+25 °C	T _{amb} =	+85 °C	T _{amb} =	+125 °C	Unit
				Min	Max	Min	Max	Min	Max	Min	Max	1
V _{OL}	LOW-level	I _O < 1 μA	5 V	-	0.05	-	0.05	-	0.05	-	0.05	V
	output voltage		10 V	-	0.05	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	-	0.05	V
I _{OH}	HIGH-level	V _O = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	-	-1.1	mA
	output current	V _O = 4.6 V	5 V	-	-0.64	-	-0.5	-	-0.36	-	-0.36	mA
	V _O = 9.5 V V _O = 13.5 V	V _O = 9.5 V	10 V	-	-1.6	-	-1.3	-	-0.9	-	-0.9	mA
		V _O = 13.5 V	15 V	-	-4.2	-	-3.4	-	-2.4	-	-2.4	mA
I _{OL}	LOW-level	V _O = 0.4 V	5 V	0.64	-	0.5	-	0.36	-	0.36	-	mA
	output current	V _O = 0.5 V	10 V	1.6	-	1.3	-	0.9	-	0.9	-	mA
		V _O = 1.5 V	15 V	4.2	-	3.4	-	2.4	-	2.4	-	mA
I _I	input leakage current		15 V	-	±0.1	-	±0.1	-	±1.0	-	±1.0	μA
I _{DD}	supply current	all valid input	5 V	-	0.25	-	0.25	-	7.5	-	7.5	μA
		combinations; I _O = 0 A	10 V	-	0.5	-	0.5	-	15.0	-	15.0	μA
		10 - 0 A	15 V	-	1.0	-	1.0	-	30.0	-	30.0	μA
CI	input capacitance			-	-	-	7.5	-	-	-	-	pF

10. Dynamic characteristics

Table 7. Dynamic characteristics

 T_{amb} = 25 °C unless otherwise specified.; for waveforms see Fig. 4; for test circuit see Fig. 5.

Symbol	Parameter	Extrapolation formula [1]	V _{DD}	Min	Тур	Max	Unit
t _{PHL}	HIGH to LOW propagation delay	33 + 0.55 × C _L	5 V	-	60	120	ns
		14 + 0.23 × C _L	10 V	-	25	50	ns
		12 + 0.16 × C _L	15 V	-	20	40	ns
t _{PLH}	LOW to HIGH propagation delay	23 + 0.55 × C _L	5 V	-	50	100	ns
		14 + 0.23 × C _L	10 V	-	25	45	ns
		12 + 0.16 × C _L	15 V	-	20	35	ns
t _{THL}	HIGH to LOW output transition time	10 + 1.00 × C _L	5 V	-	60	120	ns
		9 + 0.42 × C _L	10 V	-	30	60	ns
		6 + 0.28 × C _L	15 V	-	20	40	ns
t _{TLH}	LOW to HIGH output transition time	10 + 1.00 × C _L	5 V	-	60	120	ns
		9 + 0.42 × C _L	10 V	-	30	60	ns
		6 + 0.28 × C _L	15 V	-	20	40	ns

[1] The typical value of the propagation delay and output transition time can be calculated with the extrapolation formula (C_L in pF).

Table 8. Dynamic power dissipation

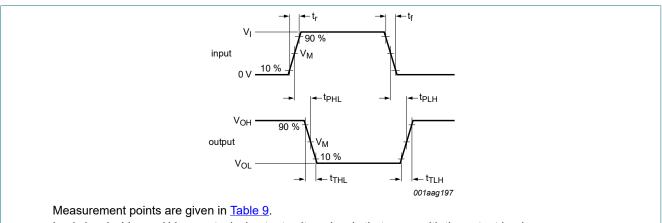
 $V_{SS} = 0 V; t_r = t_f \le 20 ns; T_{amb} = 25 \ ^{\circ}C.$

Symbol	Parameter	V_{DD}	Typical formula	Where
P _D	dynamic power dissipation	5 V		f _i = input frequency in MHz;
		10 V	$P_{D} = 5000 \times f_{i} + \Sigma(f_{o} \times C_{L}) \times V_{DD}^{2} (\mu W)$	$f_o =$ output frequency in MHz; $C_L =$ output load capacitance in pF;
		15 V	$P_{D} = 14200 \times f_{i} + \Sigma (f_{o} \times C_{L}) \times V_{DD}^{2} (\mu W)$	$\Sigma(f_o \times C_L) = sum of the outputs;$
				V _{DD} = supply voltage in V.

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10.1. Waveforms and test circuit

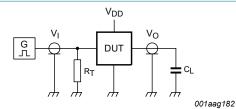


Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 4. Propagation delay, output transition time

Table 9. Measurement points

Supply voltage	Input	Output
V _{DD}	V _M	V _M
5 V to 15 V	0.5V _{DD}	0.5V _{DD}



Test data is given in Table 10.

Definitions for test circuit:

DUT = Device Under Test.

 C_L = load capacitance including jig and probe capacitance.

 R_T = termination resistance should be equal to the output impedance Z_o of the pulse generator.

Fig. 5. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input		Load
V _{DD}	VI	t _r , t _f	CL
5 V to 15 V	V_{SS} or V_{DD}	≤ 20 ns	50 pF

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11. Package outline

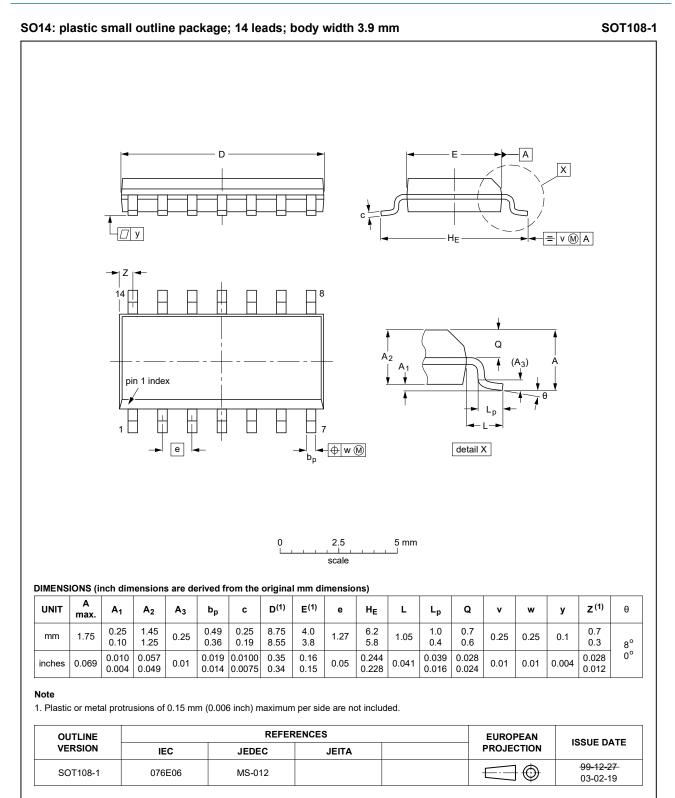


Fig. 6. Package outline SOT108-1 (SO14)

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12. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

13. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
HEF4001B v.11	20211119	Product data sheet	-	HEF4001B v.10	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. <u>Table 4</u>: Derating values for P_{tot} total power dissipation updated. <u>Section 1</u> and <u>Section 2</u> updated. 				
HEF4001B v.10	20151210	Product data sheet	-	HEF4001B v.9	
Modifications:	Type number HEF4001BP (SOT27-1) removed.				
HEF4001B v.9	20111121	Product data sheet	-	HEF4001B v.8	
Modifications:	 Legal pages updated. Changes in <u>Section 1</u> and <u>Section 2</u> Section "Applications" removed. 				
HEF4001B v.8	20110913	Product data sheet	-	HEF4001B v.7	
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HEF4001B_CNV v.2	19950101	Product specification	-	-	

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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