HEF4555B

1-of-4 decoder/demultiplexer Rev. 7 — 15 October 2018

Product data sheet

1. General description

The HEF4555B contains two 1-of-4 decoders/demultiplexers. Each has two address inputs (nA0 and nA1, an active LOW enable input (nE) and four mutually exclusive outputs which are active HIGH (nY0 to nY3). When used as a decoder, nE when HIGH, forces nY0 to nY3 LOW. When used as a demultiplexer, the appropriate output is selected by the information on nA0 and nA1 with nE as data input. All unselected outputs are LOW.

It operates over a recommended V_{DD} power supply range of 3 V to 15 V referenced to V_{SS} (usually ground). Unused inputs must be connected to V_{DD} , V_{SS} , or another input.

2. Features and benefits

- · Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Specified from -40 °C to +85 °C
- Complies with JEDEC standard JESD 13-B

3. Applications

- Code conversion
- Address decoding
- Demultiplexing: when using the enable input as data input

4. Ordering information

Table 1. Ordering information

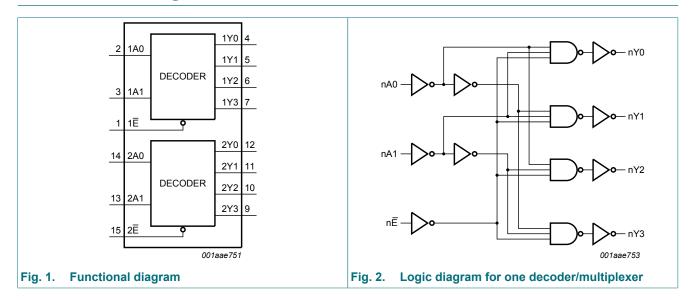
All types operate from -40 °C to +85 °C.

Type number	Package						
	Name	Description	Version				
HEF4555BT	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1				



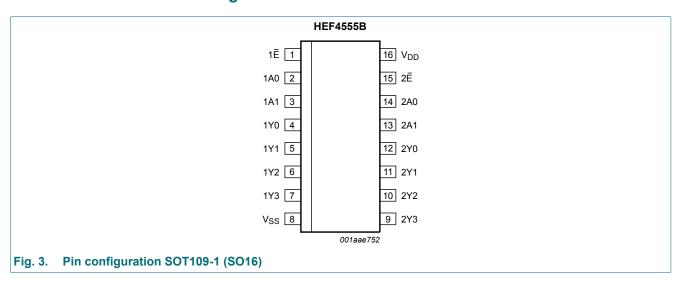
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5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A0, 1A1, 2A0, 2A1	2, 3, 14, 13	address input
1E, 2E	1, 15	enable input (active LOW
1Y0, 1Y1, 1Y2, 1Y3, 2Y0, 2Y1, 2Y2, 2Y3	4, 5, 6, 7, 12, 11, 10, 9	output (active HIGH)
V_{DD}	16	supply voltage
V _{SS}	8	ground (GND)

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7. Functional description

Table 3. Function selection

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$

Inputs			Outputs	Outputs			
nĒ	nA0	nA1	nY0	nY1	nY2	nY3	
L	L	L	Н	L	L	L	
L	Н	L	L	Н	L	L	
L	L	Н	L	L	Н	L	
L	Н	Н	L	L	L	Н	
Н	X	X	L	L	L	L	

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DD}	supply voltage		-0.5	+18	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V _{DD} + 0.5	V
I _{OK}	output clamping current	$V_{O} < -0.5 \text{ V or } V_{O} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
I _{I/O}	input/output current		-	±10	mA
I_{DD}	supply current		-	50	mA
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature		-40	+85	°C
P _{tot}	total power dissipation	SO16 package [1]	-	500	mW
Р	power dissipation	per output	-	100	mW

^[1] For SO16 package: P_{tot} derates linearly with 8 mW/K above 70 °C.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DD}	supply voltage		3	-	15	V
V _I	input voltage		0	-	V_{DD}	V
T _{amb}	ambient temperature	in free air	-40	-	+85	°C
Δt/ΔV	input transition rise and fall rate	V _{DD} = 5 V	-	-	3.75	μs/V
		V _{DD} = 10 V	-	-	0.5	μs/V
		V _{DD} = 15 V	-	-	0.08	μs/V

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10. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 \ V$; $V_I = V_{SS}$ or V_{DD} unless otherwise specified.

Symbol	Parameter	Parameter Conditions V _{DD} T _{amb} = -40 °		-40 °C	T _{amb} =	25 °C	T _{amb} = 85 °C		Unit	
				Min	Max	Min	Max	Min	Max	1
V _{IH}	HIGH-level input voltage	I _O < 1 μΑ	5 V	3.5	-	3.5	-	3.5	-	V
			10 V	7.0	-	7.0	-	7.0	-	V
			15 V	11.0	-	11.0	-	11.0	-	V
V _{IL}	LOW-level input voltage	I _O < 1 μΑ	5 V	-	1.5	-	1.5	-	1.5	V
			10 V	-	3.0	-	3.0	-	3.0	V
			15 V	-	4.0	-	4.0	-	4.0	V
V _{OH}	HIGH-level output voltage		5 V	4.95	-	4.95	-	4.95	-	V
		$V_I = V_{SS}$ or V_{DD}	10 V	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	V
V _{OL}	LOW-level output voltage	$ I_O < 1 \mu A;$ $V_I = V_{SS}$ or V_{DD}	5 V	-	0.05	-	0.05	-	0.05	V
			10 V	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	V
I _{OH}	HIGH-level output current	V _O = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	mA
		V _O = 4.6 V	5 V	-	-0.52	-	-0.44	-	-0.36	mA
		V _O = 9.5 V	10 V	-	-1.3	-	-1.1	-	-0.9	mA
		V _O = 13.5 V	15 V	-	-3.6	-	-3.0	-	-2.4	mA
I _{OL}	LOW-level output current	V _O = 0.4 V	5 V	0.52	-	0.44	-	0.36	-	mA
		V _O = 0.5 V	10 V	1.3	-	1.1	-	0.9	-	mA
		V _O = 1.5 V	15 V	3.6	-	3.0	-	2.4	-	mA
I _I	input leakage current	V _{DD} = 15 V	15 V	-	±0.3	-	±0.3	-	±1.0	μΑ
I _{DD}	supply current	I _O = 0 A;	5 V	-	20	-	20	-	150	μΑ
		$V_I = V_{SS}$ or V_{DD}	10 V	-	40	-	40	-	300	μΑ
			15 V	-	80	-	80	-	600	μΑ
Cı	input capacitance		-	-	-	-	7.5	-	-	pF

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11. Dynamic characteristics

Table 7. Dynamic characteristics

 V_{SS} = 0 V; T_{amb} = 25 °C; for test circuit see Fig. 5; unless otherwise specified.

Symbol	Parameter	Conditions	V _{DD}	Extrapolation formula	Min	Тур	Max	Unit
t _{PHL}	HIGH to LOW	nAn to nYn;	5 V [1]	88 ns + (0.55 ns/pF)C _L	-	115	230	ns
	propagation delay	see Fig. 4	10 V	34 ns + (0.23 ns/pF)C _L	-	45	90	ns
			15 V	22 ns + (0.16 ns/pF)C _L	-	30	65	ns
		nE to nYn;	5 V [1]	98 ns + (0.55 ns/pF)C _L	-	125	250	ns
		see Fig. 4	10 V	39 ns + (0.23 ns/pF)C _L	-	50	95	ns
			15 V	22 ns + (0.16 ns/pF C _L	-	30	65	ns
t _{PLH}	LOW to HIGH propagation delay	nAn to nYn; see Fig. 4 nE to nYn;	5 V [1]	113 ns + (0.55 ns/pF)C _L	-	140	280	ns
			10 V	44 ns + (0.23 ns/pF)C _L	-	55	105	ns
			15 V	32 ns + (0.16 ns/pF)C _L	-	40	75	ns
			5 V [1]	123 ns + (0.55 ns/pF)C _L	-	150	295	ns
		see Fig. 4	10 V	44 ns + (0.23 ns/pF)C _L	-	55	110	ns
			15 V	32 ns + (0.16 ns/pF)C _L	-	40	75	ns
t _t	transition time	nYn; see Fig. 4	5 V [1][2]	10 ns + (1.00 ns/pF)C _L	-	60	120	ns
			10 V	9 ns + (0.42 ns/pF)C _L	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF)C _L	-	20	40	ns

^[1] The typical values of the propagation delay and transition times are calculated from the extrapolation formulas shown (C_L in pF).

Table 8. Dynamic power dissipation P_D

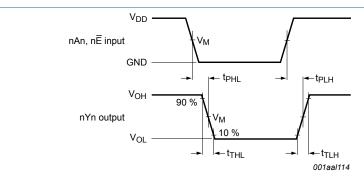
 P_D can be calculated from the formulas shown. $V_{SS} = 0 \text{ V}$; $t_r = t_f \le 20 \text{ ns}$; $T_{amb} = 25 \text{ °C}$.

Symbol	Parameter	V _{DD}	Typical formula for P _D (μW)	Where:
P_{D}	dynamic power	5 V	,	f _i = input frequency in MHz,
	dissipation	10 V	$P_D = 18800 \times f_i + \Sigma (f_0 \times C_L) \times V_{DD}^2$	f _o = output frequency in MHz, C _L = output load capacitance in pF,
		15 V	$P_D = 45700 \times f_i + \Sigma (f_0 \times C_L) \times V_{DD}^2$	V_{DD} = supply voltage in V, $\Sigma(f_0 \times C_L)$ = sum of the outputs.

^[2] Transition time t_t is the same as the HIGH to LOW and LOW to HIGH transition times t_{THL} and t_{TLH} .

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11.1. Waveforms and test circuit



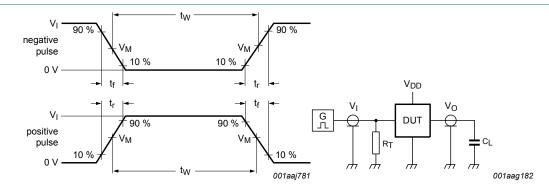
Measurement points are given in Table 9.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 4. Inputs nAn, nE to output nYn propagation delay and nYn output transition time

Table 9. Measurement points

Supply voltage	Input	Output
V_{DD}	V _M	V _M
5 V to 15 V	0.5V _{DD}	0.5V _{DD}



Test data is given in Table 10.

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator;

 V_{EXT} = External voltage for measuring switching times.

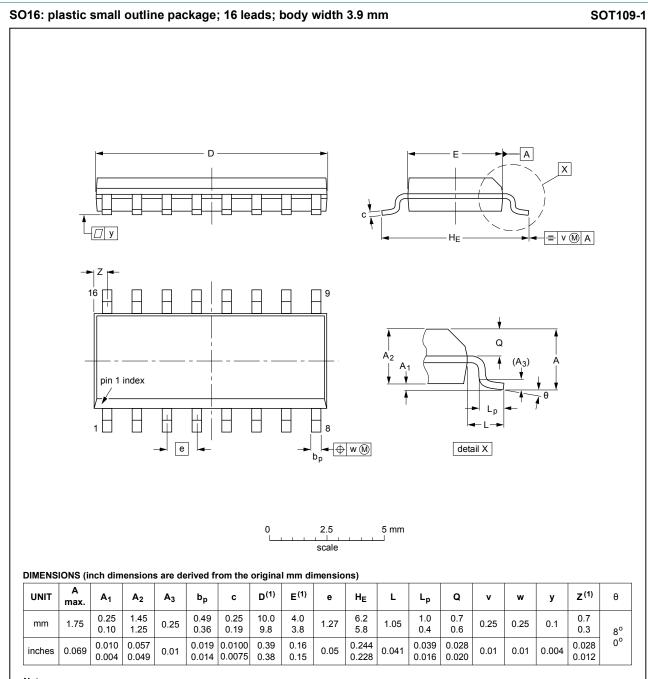
Fig. 5. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Load	
V_{DD}	V _I	$t_r = t_f$	CL
5 V to 15 V	V_{DD}	≤ 20 ns	50 pF

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12. Package outline



Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE		REFER	ENCES		EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT109-1	076E07	MS-012				99-12-27 03-02-19	

Fig. 6. Package outline SOT109-1 (SO16)

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13. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
HEF4555B v.7	20181015	Product data sheet	-	HEF4555B v.6	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidel Nexperia. Legal texts have been adapted to the new company name where appropriate. 				
HEF4555B v.6	20160401	Product data sheet	-	HEF4555B v.5	
Modifications:	Type number	HEF4555BP (SOT38-4) remov	ed.		
HEF4555B v.5	20111118	Product data sheet	-	HEF4555B v.4	
Modifications:	• <u>Table 6</u> : I _{OH} m	ninimum values changed to ma	ximum		
HEF4555B v.4	20100106	Product data sheet	-	HEF4555B_CNV v.3	
HEF4555B_CNV v.3	19950101	Product specification	-	HEF4555B_CNV v.2	
HEF4555B_CNV v.2	19950101	Product specification	-	-	

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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