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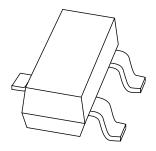
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Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS

# DATA SHEET



# MMBT2222A NPN switching transistor

Product data sheet Supersedes data of 2000 Apr 11 2004 Jan 16



# **NPN** switching transistor

#### MMBT2222A

#### **FEATURES**

- High current (max. 600 mA)
- Low voltage (max. 40 V).

#### **APPLICATIONS**

• Switching and linear amplification.

#### **DESCRIPTION**

NPN switching transistor in a SOT23 plastic package. PNP complement: PMBT2907A.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>	
MMBT2222A	7C*	

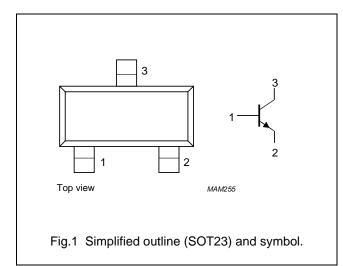
#### Note

\* = p : Made in Hong Kong.
 \* = t : Made in Malaysia.

\* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE		PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION	
MMBT2222A	_	plastic surface mounted package; 3 leads SC		

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	_	75	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	40	V
$V_{EBO}$	emitter-base voltage	open collector	_	6	V
I <sub>C</sub>	collector current (DC)		_	600	mA
I <sub>CM</sub>	peak collector current		_	800	mA
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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# NPN switching transistor

MMBT2222A

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V	_	10	nA
		$I_E = 0$ ; $V_{CB} = 60 \text{ V}$ ; $T_j = 125 ^{\circ}\text{C}$	_	10	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	10	nA
h <sub>FE</sub>	DC current gain	$I_C = 0.1 \text{ mA}; V_{CE} = 10 \text{ V}$	35	_	
		$I_C = 1 \text{ mA}; V_{CE} = 10 \text{ V}$	50	_	
		I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V	75	_	
		$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V};$ $T_{amb} = -55 \text{ °C}$	35	_	
		I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 10 V	100	300	
		I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 1 V	50	_	
		I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 10 V	40	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 150 \text{ mA}$ ; $I_B = 15 \text{ mA}$ ; note 1	_	300	mV
		$I_C = 500 \text{ mA}$ ; $I_B = 50 \text{ mA}$ ; note 1	_	1	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 150 \text{ mA}$ ; $I_B = 15 \text{ mA}$ ; note 1	0.6	1.2	V
		$I_C = 500 \text{ mA}$ ; $I_B = 50 \text{ mA}$ ; note 1	_	2	V
C <sub>c</sub>	collector capacitance	$I_E = I_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	8	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 500 \text{ mV}$ ; $f = 1 \text{ MHz}$	_	25	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 20 mA; V <sub>CE</sub> = 20 V; f = 100 MHz	300	_	MHz
F	noise figure	$I_C$ = 100 μA; $V_{CE}$ = 5 V; $R_S$ = 1 kΩ; $f$ = 1 kHz	_	4	dB
Switching ti	imes (between 10% and 90% levels); (see F	ig.2)	ı	II.	'
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 150 mA; I <sub>Bon</sub> = 15 mA;	_	35	ns
t <sub>d</sub>	delay time	I <sub>Boff</sub> = −15 mA	_	15	ns
t <sub>r</sub>	rise time	1	_	20	ns
t <sub>off</sub>	turn-off time	1	_	250	ns
t <sub>s</sub>	storage time	1	_	200	ns
t <sub>f</sub>	fall time	7	_	60	ns

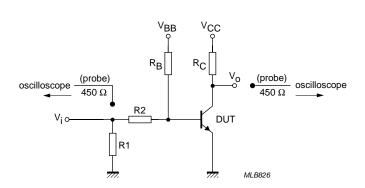
#### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

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# NPN switching transistor

# MMBT2222A



$$\begin{split} &V_i = 9.5 \; V; \; T = 500 \; \mu s; \; t_p = 10 \; \mu s; \; t_r = t_f \leq 3 \; ns. \\ &R1 = 68 \; \Omega; \; R2 = 325 \; \Omega; \; R_B = 325 \; \Omega; \; R_C = 160 \; \Omega. \\ &V_{BB} = -3.5 \; V; \; V_{CC} = 29.5 \; V. \\ &Oscilloscope: input impedance \; Z_i = 50 \; \Omega. \end{split}$$

Fig.2 Test circuit for switching times.

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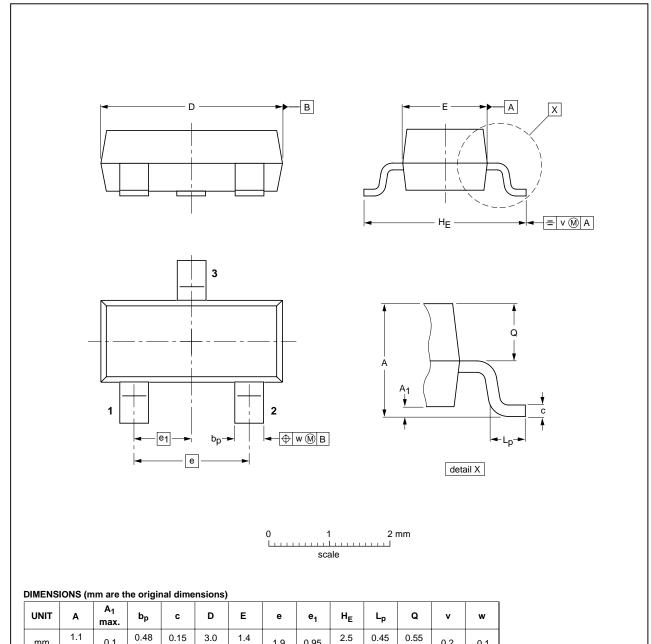
# NPN switching transistor

# MMBT2222A

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>-04-11-04</del> 06-03-16

0.1

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0.38

0.9

### NPN switching transistor

#### MMBT2222A

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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#### **Contact information**

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