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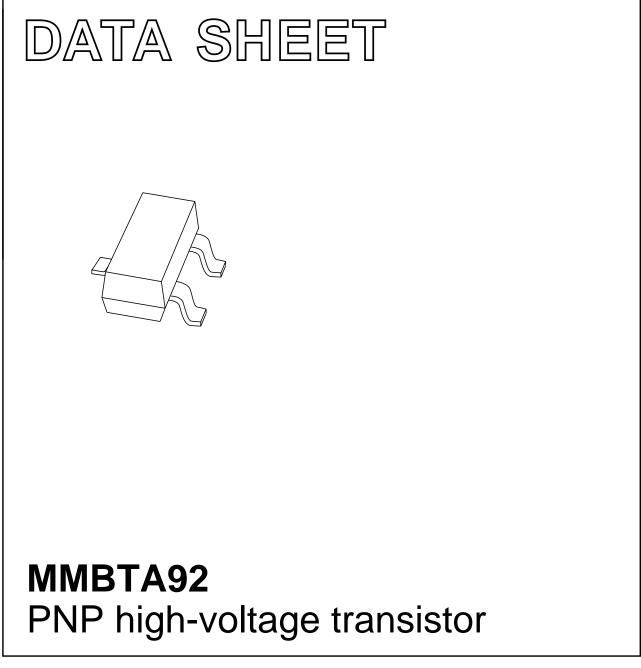
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If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 2000 Apr 11 2004 Jan 16



FEATURES

- Low current (max. 100 mA)
- High voltage (max. 300 V).

APPLICATIONS

- Telephony
- Professional communication equipment.

DESCRIPTION

PNP high-voltage transistor in a SOT23 plastic package. NPN complement: MMBTA42.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
MMBTA92	7E*	

Note

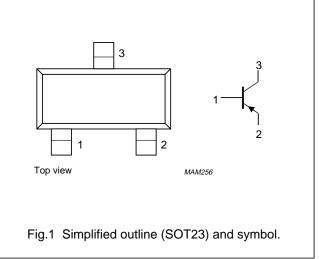
- 1. * = p : Made in Hong Kong.
 - * = t : Made in Malaysia.

* = W : Made in China.

ORDERING INFORMATION

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ТҮРЕ	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
MMBTA92	_	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	-300	V
V _{CEO}	collector-emitter voltage	open base	-	-300	V
V _{EBO}	emitter-base voltage	open collector	-	-5	V
I _C	collector current (DC)		-	-100	mA
I _{CM}	peak collector current		-	-200	mA
I _{BM}	peak base current		-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$; note 1	-	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature			150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

MMBTA92

MMBTA92

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

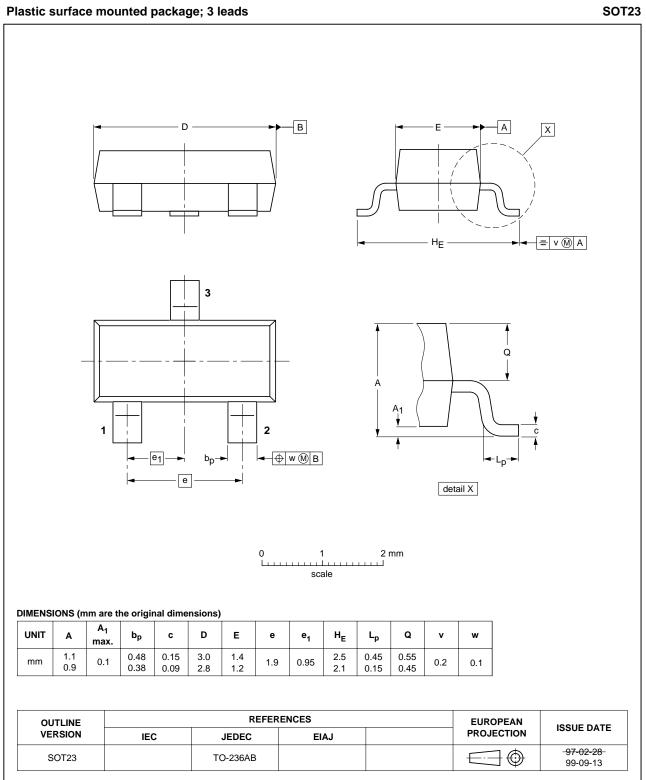
 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -200 V	-	-250	nA
I _{EBO}	emitter cut-off current	$I_{\rm C} = 0; V_{\rm EB} = -3 \text{ V}$	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; note 1			
		$I_{\rm C} = -1 \rm{mA}$	25	-	
		I _C = -10 mA	40	-	
		I _C = -30 mA	25	-	
V _{CEsat}	collector-emitter saturation voltage	$I_{\rm C} = -20 \text{ mA}; I_{\rm B} = -2 \text{ mA}$	-	-500	mV
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C} = -20 \text{ mA}; I_{\rm B} = -2 \text{ mA}$	-	-900	mV
C _c	collector capacitance	$I_E = i_e = 0; V_{CB} = -20 V;$ f = 1 MHz	-	6	pF
f _T	transition frequency	$I_{C} = -10 \text{ mA}; V_{CE} = -20 \text{ V};$ f = 100 MHz	50	-	MHz

Note

1. Pulse test: $t_p \leq 300 \ \mu s; \ \delta \leq 0.02.$

PACKAGE OUTLINE



MMBTA92

MMBTA92

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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