



NHDTC123JU/143ZU/114YU series

80 V, 100 mA NPN resistor-equipped transistors

Rev. 1 — 17 July 2020

Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) family in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	R1	R2	Package		PNP complement:
	k Ω	k Ω	Nexperia	JEITA	
NHDTC123JU	2.2	47	SOT323	SC-70	NHDTA123JU
NHDTC143ZU	4.7	47			NHDTA143ZU
NHDTC114YU	10	47			NHDTA114YU

2. Features and benefits

- 100 mA output current capability
- High breakdown voltage
- Built-in resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- Digital applications
- Cost saving alternative for BC846 series in digital applications
- Controlling IC inputs
- Switching loads

4. Quick reference data

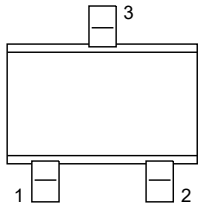
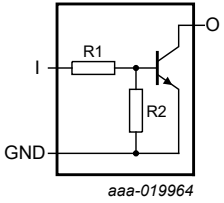
Table 2. Quick reference data

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	80	V
I_O	output current		-	-	100	mA

5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		
2	GND	GND (emitter)		
3	O	output (collector)		

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
NHDTC123JU	SC-70	plastic surface-mounted package; 3 leads	SOT323
NHDTC143ZU			
NHDTC114YU			

7. Marking

Table 5. Marking

Type number	Marking code [1]
NHDTC123JU	5P%
NHDTC143ZU	5R%
NHDTC114YU	5N%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit	
V_{CBO}	collector-base voltage	open emitter	-	80	V	
V_{CEO}	collector-emitter voltage	open base	-	80	V	
V_{EBO}	emitter-base voltage	open collector	-	7	V	
V_i	input voltage					
	NHDTC123JU		-7	+20	V	
	NHDTC143ZU		-7	+30	V	
	NHDTC114YU		-7	+40	V	
I_O	output current		-	100	mA	
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	235	mW
			[2]	-	315	mW
T_j	junction temperature		-	150	°C	
T_{amb}	ambient temperature		-55	150	°C	
T_{stg}	storage temperature		-65	150	°C	

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint.

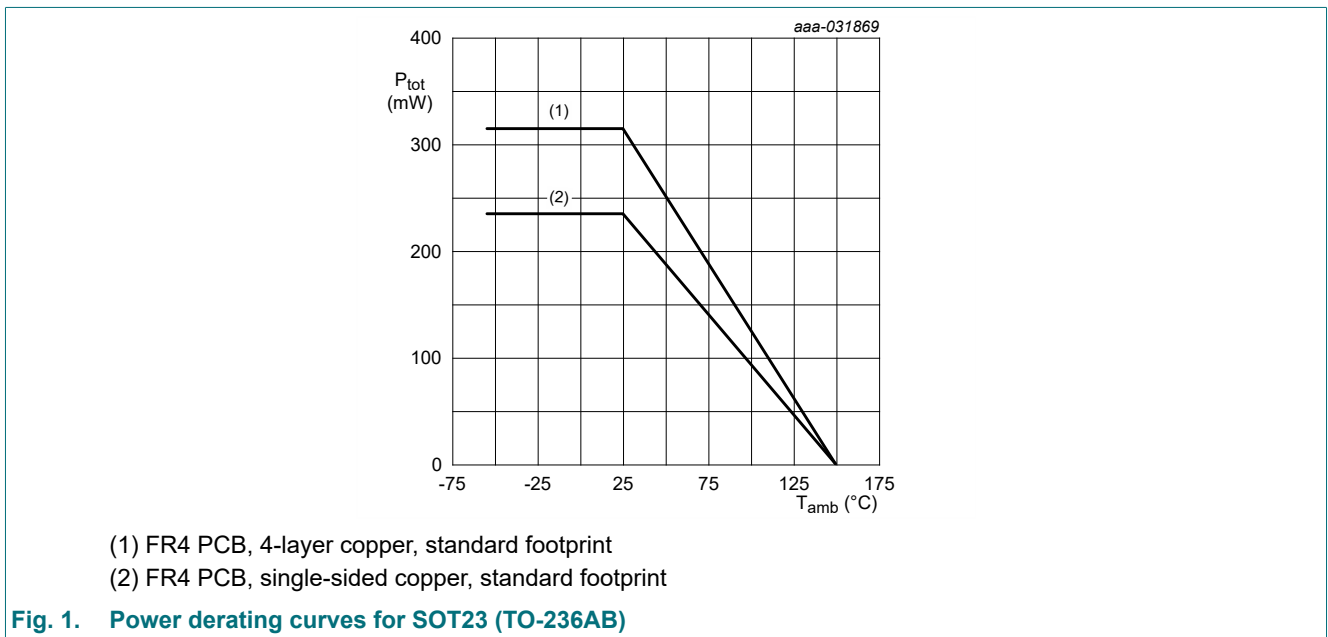


Fig. 1. Power derating curves for SOT23 (TO-236AB)

9. Thermal characteristics

Table 7. Thermal characteristics

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	532	K/W
			[2]	-	-	397	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	150	K/W

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.

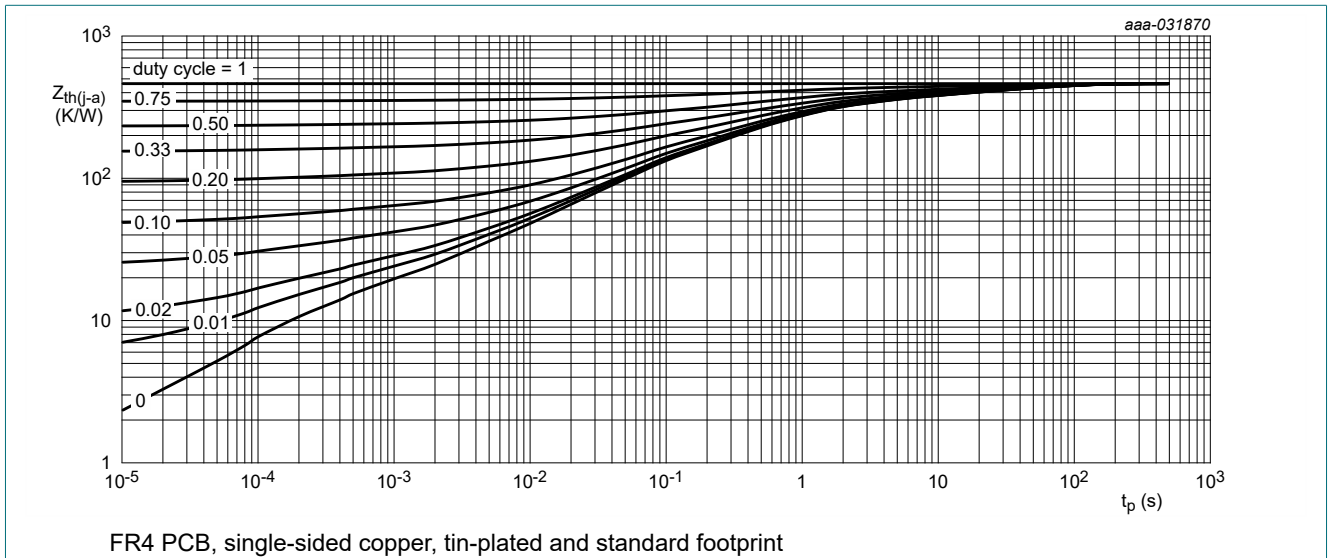


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

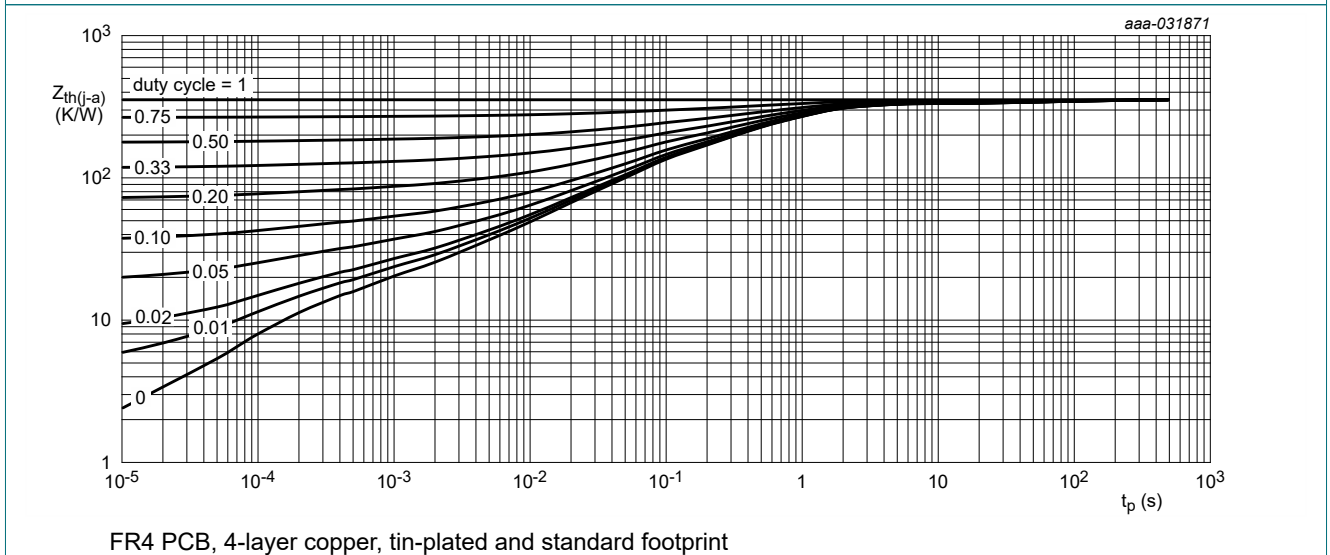


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 8. Characteristics
 $T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100\ \mu\text{A}; I_E = 0\ \text{A}$	80	-	-	V	
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2\ \text{mA}; I_B = 0\ \text{A}$	80	-	-	V	
I_{CBO}	collector-base cut-off current	$V_{CB} = 80\ \text{V}; I_E = 0\ \text{A}$	-	-	100	nA	
I_{CEO}	collector-emitter cut-off current	$V_{CE} = 60\ \text{V}; I_B = 0\ \text{A}$	-	-	100	nA	
		$V_{CE} = 60\ \text{V}; I_B = 0\ \text{A}; T_j = 150\text{ °C}$	-	-	5	μA	
I_{EBO}	emitter-base cut-off current						
	NHDTC123JU	$V_{EB} = 7\ \text{V}; I_C = 0\ \text{A}$	-	-	270	μA	
	NHDTC143ZU		-	-	260	μA	
	NHDTC114YU		-	-	230	μA	
h_{FE}	DC current gain	$V_{CE} = 5\ \text{V}; I_C = 10\ \text{mA}$	100	-	-		
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10\ \text{mA}; I_B = 0.5\ \text{mA}$	-	-	100	mV	
$V_{I(off)}$	off-state input voltage						
	NHDTC123JU	$V_{CE} = 5\ \text{V}; I_C = 100\ \mu\text{A}$	-	595	500	mV	
	NHDTC143ZU		-	625	500	mV	
	NHDTC114YU		-	690	500	mV	
$V_{I(on)}$	on-state input voltage						
	NHDTC123JU	$V_{CE} = 0.3\ \text{V}; I_C = 10\ \text{mA}$	1.2	0.81	-	V	
	NHDTC143ZU		1.4	0.95	-	V	
	NHDTC114YU		1.6	1.22	-	V	
R1	bias resistor 1 (input)						
	NHDTC123JU		[1]	1.54	2.2	2.86	k Ω
	NHDTC143ZU			3.3	4.7	6.1	k Ω
	NHDTC114YU			7	10	13	k Ω
R2/R1	bias resistor ratio						
	NHDTC123JU		[1]	17	21	26	
	NHDTC143ZU			8	10	12	
	NHDTC114YU			3.7	4.7	5.7	
f_T	transition frequency	$V_{CE} = 5\ \text{V}; I_C = 10\ \text{mA}; f = 100\ \text{MHz}$	[2]	-	170	-	MHz
C_c	collector capacitance	$V_{CB} = 10\ \text{V}; I_E = I_C = 0\ \text{A}; f = 1\ \text{MHz}$	-	-	2.5	pF	

[1] See section "Test information" for resistor calculation and test conditions

[2] Characteristics of built-in transistor

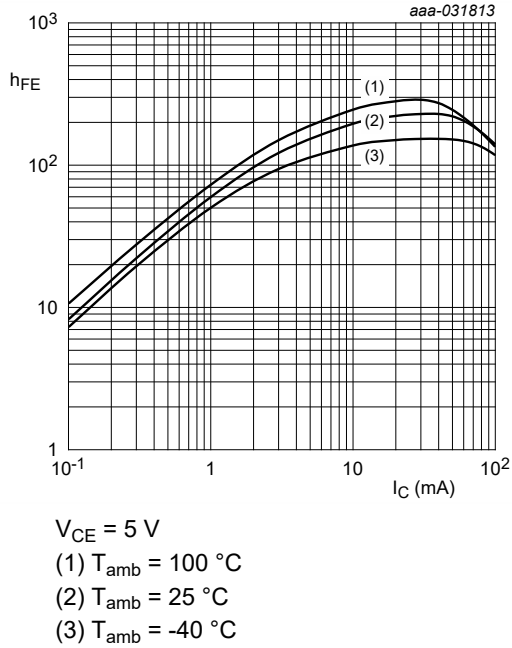


Fig. 4. NHDTC123JU: DC current gain as a function of collector current; typical values

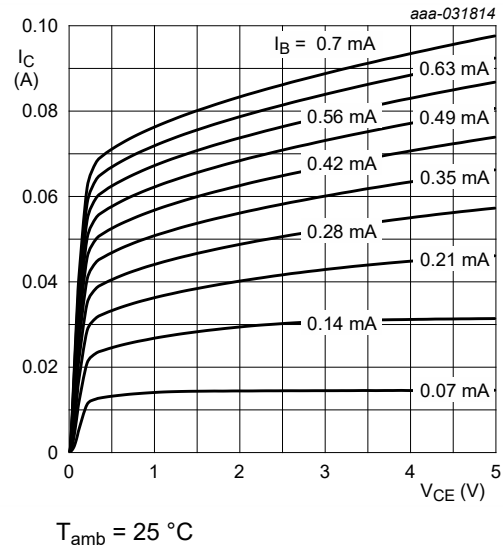


Fig. 5. NHDTC123JU: Collector current as a function of collector-emitter voltage; typical values

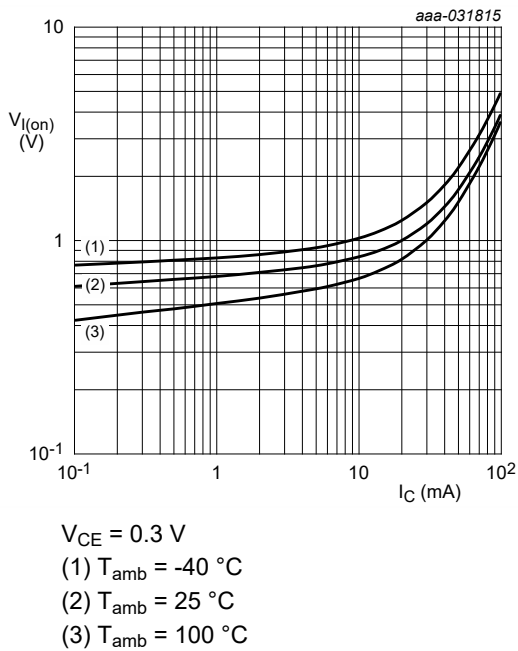


Fig. 6. NHDTC123JU: On-state input voltage as a function of collector current; typical values

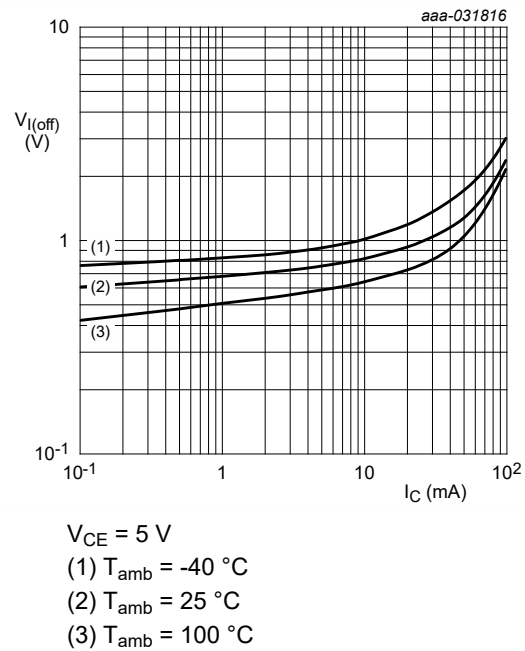
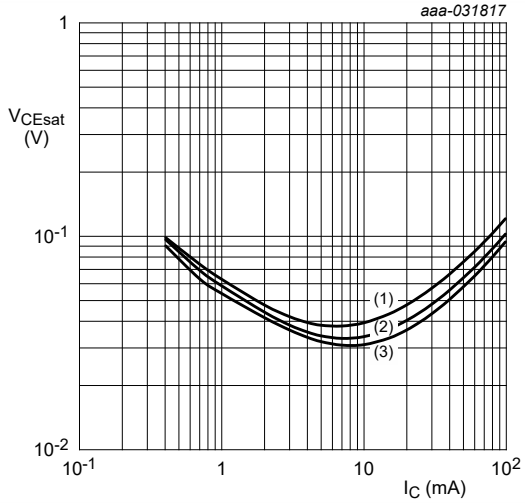


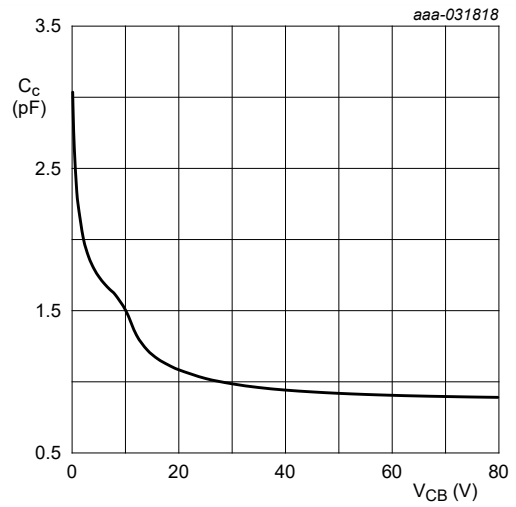
Fig. 7. NHDTC123JU: Off-state input voltage as a function of collector current; typical values



$I_C/I_B = 20$

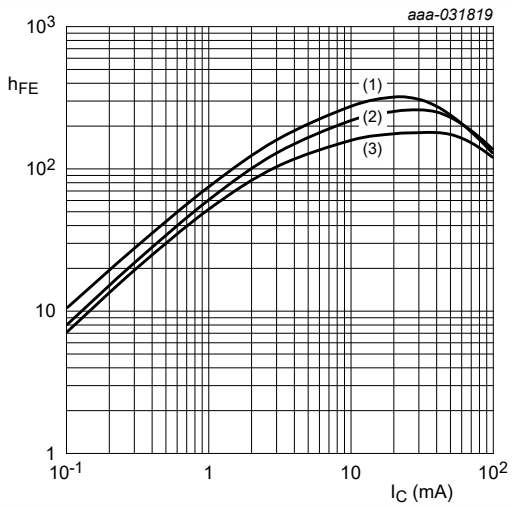
- (1) $T_{amb} = 100\text{ °C}$
- (2) $T_{amb} = 25\text{ °C}$
- (3) $T_{amb} = -40\text{ °C}$

Fig. 8. NHDTC123JU: Collector-emitter saturation voltage as a function of collector current; typical values



- $f = 1\text{ MHz}$
- $T_{amb} = 25\text{ °C}$

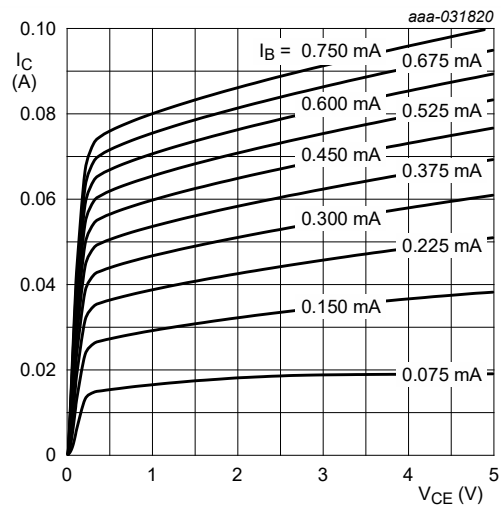
Fig. 9. NHDTC123JU: Collector capacitance as a function of collector-base voltage; typical values



$V_{CE} = 5\text{ V}$

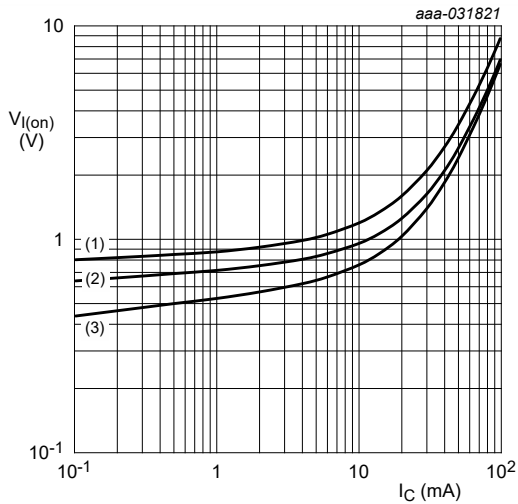
- (1) $T_{amb} = 100\text{ °C}$
- (2) $T_{amb} = 25\text{ °C}$
- (3) $T_{amb} = -40\text{ °C}$

Fig. 10. NHDTC143ZU: DC current gain as a function of collector current; typical values



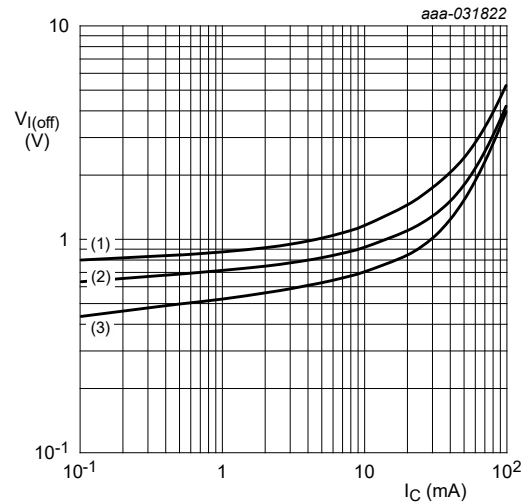
$T_{amb} = 25\text{ °C}$

Fig. 11. NHDTC143ZU: Collector current as a function of collector-emitter voltage; typical values



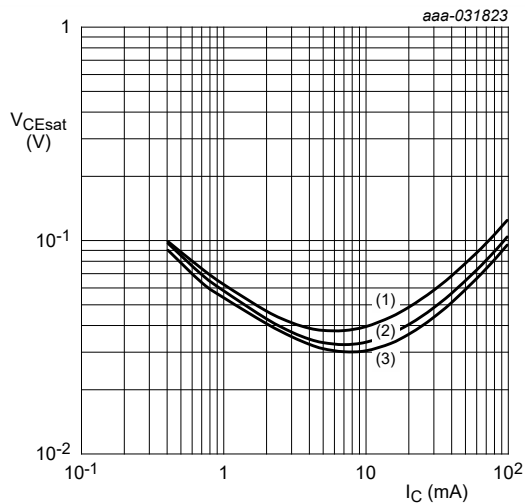
$V_{CE} = 0.3 \text{ V}$
 (1) $T_{amb} = -40 \text{ }^\circ\text{C}$
 (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
 (3) $T_{amb} = 100 \text{ }^\circ\text{C}$

Fig. 12. NHDTC143ZU: On-state input voltage as a function of collector current; typical values



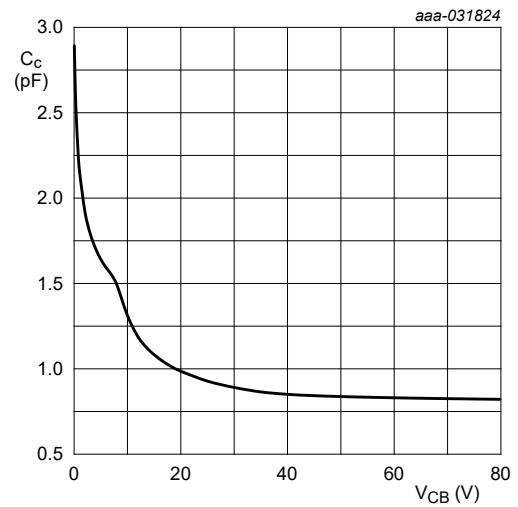
$V_{CE} = 5 \text{ V}$
 (1) $T_{amb} = -40 \text{ }^\circ\text{C}$
 (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
 (3) $T_{amb} = 100 \text{ }^\circ\text{C}$

Fig. 13. NHDTC143ZU: Off-state input voltage as a function of collector current; typical values



$I_C/I_B = 20$
 (1) $T_{amb} = 100 \text{ }^\circ\text{C}$
 (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
 (3) $T_{amb} = -40 \text{ }^\circ\text{C}$

Fig. 14. NHDTC143ZU: Collector-emitter saturation voltage as a function of collector current; typical values



$f = 1 \text{ MHz}$
 $T_{amb} = 25 \text{ }^\circ\text{C}$

Fig. 15. NHDTC143ZU: Collector capacitance as a function of collector-base voltage; typical values

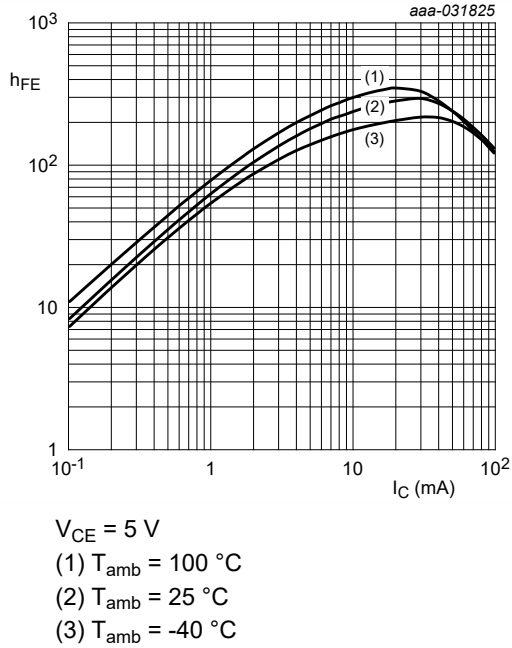


Fig. 16. NHDTC114YU: DC current gain as a function of collector current; typical values

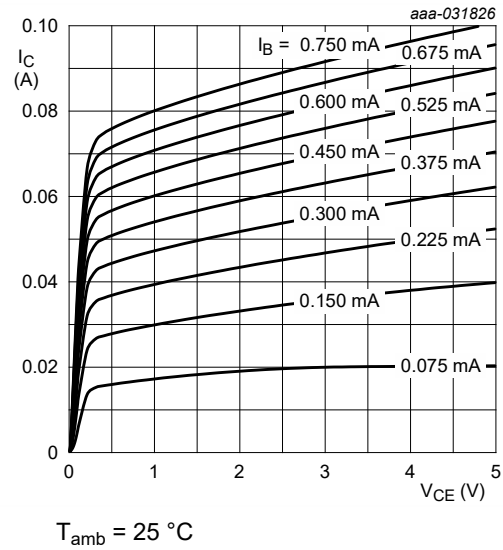


Fig. 17. NHDTC114YU: Collector current as a function of collector-emitter voltage; typical values

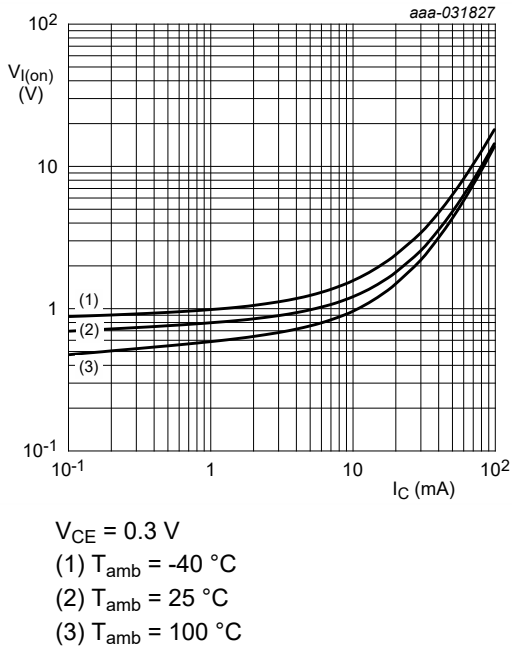


Fig. 18. NHDTC114YU: On-state input voltage as a function of collector current; typical values

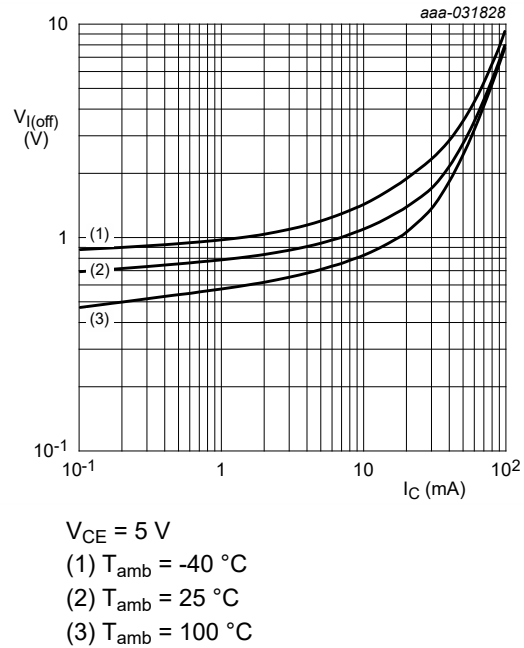
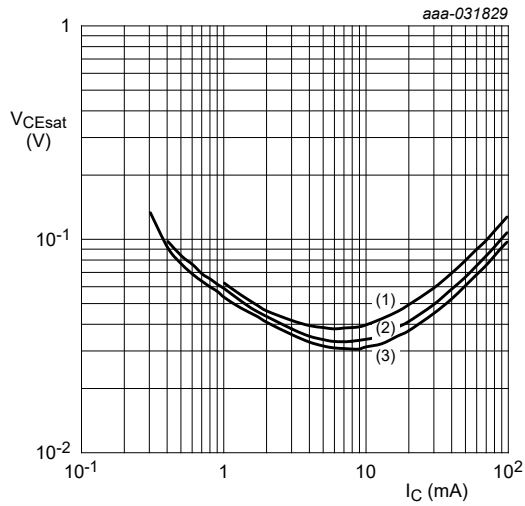
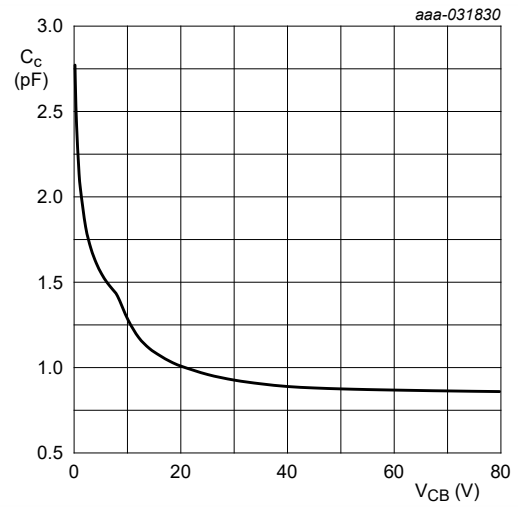


Fig. 19. NHDTC114YU: Off-state input voltage as a function of collector current; typical values



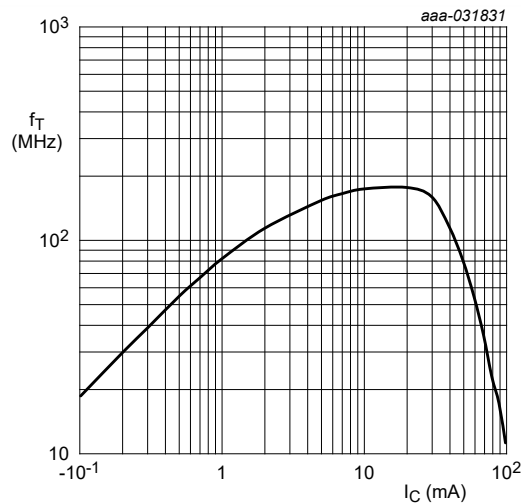
$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -40\text{ }^\circ\text{C}$

Fig. 20. NHDTC114YU: Collector-emitter saturation voltage as a function of collector current; typical values



$f = 1\text{ MHz}$
 $T_{amb} = 25\text{ }^\circ\text{C}$

Fig. 21. NHDTC114YU: Collector capacitance as a function of collector-base voltage; typical values



$f = 100\text{ MHz}$
 $V_{CE} = 5\text{ V}$
 $T_{amb} = 25\text{ }^\circ\text{C}$

Fig. 22. Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Resistor calculation

- Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I12) - V(I11)}{I12 - I11}$$

- Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$

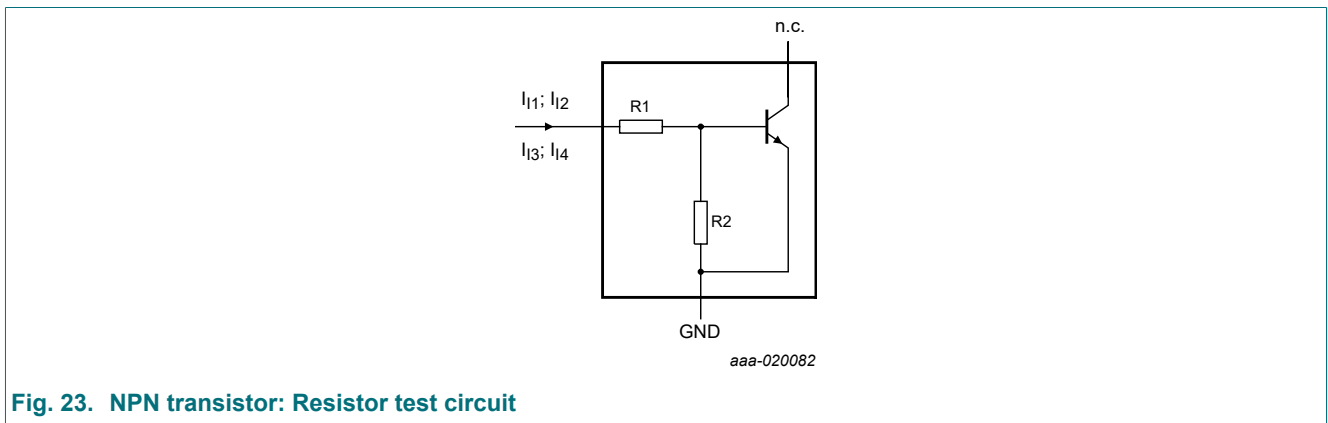


Fig. 23. NPN transistor: Resistor test circuit

Resistor test conditions

Table 9. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I ₁₁	I ₁₂	I ₁₃	I ₁₄
NHDTTC123JU	2.2	47	1.6 mA	2.4 mA	-55 μA	-105 μA
NHDTTC143ZU	4.7	47	1.2 mA	1.8 mA	-55 μA	-105 μA
NHDTTC114YU	10	47	0.8 mA	1.1 mA	-55 μA	-105 μA

14. Revision history

Table 10. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NHDTC123JU_143ZU_114YU_SER v.1	20200717	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	3
9. Thermal characteristics.....	4
10. Characteristics.....	5
11. Test information.....	11
12. Package outline.....	12
13. Soldering.....	12
14. Revision history.....	13
15. Legal information.....	14

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[SMUN5114DW1T1G](#) [SMUN2111T1G](#) [NSVDTC144EM3T5G](#) [DTC124ECA-TP](#) [DTC123TM3T5G](#) [DTA114ECA-TP](#) [DTA113EM3T5G](#)
[DCX115EK-7-F](#) [DTC113EM3T5G](#) [NSVMUN5135DW1T1G](#) [NSVMUN2237T1G](#)