

# NP100T12P2T3

1200 V, 100 A, Power Integrated Module

Rev. 1 — 20 February 2024

Product data sheet

## 1. General description

IGBT power module provides ultra-low conduction loss as well as short circuit ruggedness. They are designed for applications such as inverters for motor drivers and servo drivers.

## 2. Features and benefits

- Low switching losses and low saturation voltage  $V_{CE(sat)}$
- 10  $\mu$ s short circuit capability
- $V_{CE(sat)}$  with positive temperature coefficient
- Maximum junction temperature 175 °C
- Low stray inductance package
- Fast and soft reverse recovery anti-parallel free-wheeling diode
- RoHS compliant product
- Integrated NTC thermistor temperature sensor

## 3. Applications

- Inverter for motor drivers and servo drivers
- AC/DC servo drive amplifier

## 4. Ordering information

Table 1. Ordering information

Type number	Package		
	Name	Description	Version
NP100T12P2T3	NP2-35P	plastic house; through hole solderable pin with copper baseplate; 35 pins; 62.5 mm × 122.5 mm × 17 mm body	SOT8053-1

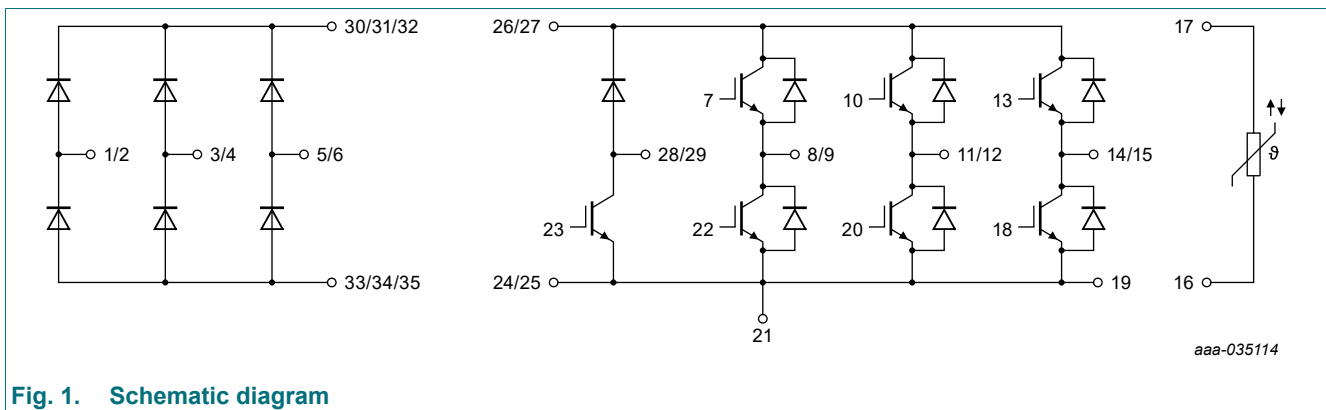


Fig. 1. Schematic diagram

## 5. Limiting values

Table 2. IGBT

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Inverter</b>					
$V_{CES}$	collector-emitter voltage	$T_j = 25\text{ °C}$	-	1200	V
$I_C$	DC collector current	$T_{case} = 100\text{ °C}; T_{jmax} = 175\text{ °C}$	-	100	A
$I_{CRM}$	repetitive peak collector current	$t_p = 1\text{ ms}$	-	200	A
$V_{GES}$	gate to emitter voltage		-	±20	V
<b>Brake-chopper</b>					
$V_{CES}$	collector-emitter voltage	$T_j = 25\text{ °C}$	-	1200	V
$I_C$	DC collector current	$T_{case} = 100\text{ °C}; T_{jmax} = 175\text{ °C}$	-	50	A
$I_{CRM}$	repetitive peak collector current	$t_p = 1\text{ ms}$	-	100	A
$V_{GES}$	gate to emitter voltage		-	±20	V

Table 3. Diode

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Inverter</b>					
$V_{RRM}$	repetitive peak reverse voltage	$T_j = 25\text{ °C}$	-	1200	V
$I_F$	continuous DC forward current	$T_{case} = 100\text{ °C}; T_{jmax} = 175\text{ °C}$	-	100	A
$I_{FRM}$	repetitive peak forward current	$t_p = 1\text{ ms}$	-	200	A
$I^2t$	$I^2t$ -value	$V_R = 0\text{ V}; t_p = 10\text{ ms}; T_j = 125\text{ °C}$	-	1795	A <sup>2</sup> s
		$V_R = 0\text{ V}; t_p = 10\text{ ms}; T_j = 150\text{ °C}$	-	1488	A <sup>2</sup> s
<b>Rectifier</b>					
$V_{RRM}$	repetitive peak reverse voltage	$T_j = 25\text{ °C}$	-	1600	V
$I_{FRMSM}$	maximum RMS forward current per chip	$T_{case} = 100\text{ °C}$	-	100	A
$I_{RMSM}$	maximum RMS forward current at rectifier output	$T_{case} = 100\text{ °C}$	-	100	A
$I_{FSM}$	surge forward current	$t_p = 10\text{ ms}; T_j = 25\text{ °C}$	-	1272	A
		$t_p = 10\text{ ms}; T_j = 150\text{ °C}$	-	983	A
$I^2t$	$I^2t$ -value	$t_p = 10\text{ ms}; T_j = 25\text{ °C}$	-	8099	A <sup>2</sup> s
		$t_p = 10\text{ ms}; T_j = 150\text{ °C}$	-	4840	A <sup>2</sup> s
<b>Brake-chopper</b>					
$V_{RRM}$	repetitive peak reverse voltage	$T_j = 25\text{ °C}$	-	1200	V
$I_F$	continuous DC forward current	$T_{case} = 100\text{ °C}; T_{jmax} = 175\text{ °C}$	-	50	A
$I_{FRM}$	repetitive peak forward current	$t_p = 1\text{ ms}$	-	100	A
$I^2t$	$I^2t$ -value	$V_R = 0\text{ V}; t_p = 10\text{ ms}; T_j = 125\text{ °C}$	-	360	A <sup>2</sup> s
		$V_R = 0\text{ V}; t_p = 10\text{ ms}; T_j = 150\text{ °C}$	-	336	A <sup>2</sup> s

## 6. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$R_{th(j-c)}$	thermal resistance from junction to case	per IGBT	inverter	-	-	0.26	K/W
			brake-chopper	-	-	0.48	K/W
		per diode	inverter	-	-	0.45	K/W
			rectifier	-	-	0.36	K/W
			brake-chopper	-	-	1.2	K/W

## 7. Electrical characteristics

Table 5. IGBT

$T_j = 25\text{ °C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
<b>Inverter</b>							
$BV_{CES}$	collector-emitter breakdown voltage	$V_{GE} = 0\text{ V}; I_C = 1\text{ mA}$	1200	-	-	V	
$I_{CES}$	collector-emitter cutoff current	$V_{GE} = 0\text{ V}; V_{CE} = V_{CES}$	-	-	1	mA	
$I_{GES}$	gate leakage current	$V_{CE} = 0\text{ V}; V_{GE} = V_{GES}$	-	-	$\pm 500$	nA	
$V_{GE(th)}$	gate emitter threshold voltage	$V_{CE} = 10\text{ V}; I_C = 3.8\text{ mA}$	5	6.0	6.8	V	
$R_G$	internal gate resistor	$f = 1\text{ MHz}$	-	9.1	-	$\Omega$	
$V_{CE(sat)}$	collector-emitter saturation voltage	$I_C = 100\text{ A}; V_{GE} = 15\text{ V}$	$T_j = 25\text{ °C}$	-	1.65	1.95	V
			$T_j = 125\text{ °C}$	-	1.8	-	V
			$T_j = 150\text{ °C}$	-	1.85	-	V
$C_{ies}$	input capacitance	$V_{GE} = 0\text{ V}; V_{CE} = 25\text{ V};$ $f = 100\text{ kHz}$	-	8.2	-	nF	
$C_{oes}$	output capacitance		-	1.51	-	nF	
$C_{res}$	reverse transfer capacitance		-	0.29	-	nF	
$Q_g$	total gate charge	$V_{CC} = 960\text{ V}; I_C = 100\text{ A};$ $V_{GE} = \pm 15\text{ V}$	-	0.57	-	$\mu\text{C}$	
$t_{d(on)}$	turn-on delay time	$V_{CC} = 600\text{ V}; I_C = 100\text{ A};$ $V_{GE} = \pm 15\text{ V}; R_{Gon} = 1.5\text{ }\Omega;$ $R_{Goff} = 1.5\text{ }\Omega; L_S = 50\text{ nH}$	$T_j = 25\text{ °C}$	-	122	-	ns
			$T_j = 125\text{ °C}$	-	129	-	ns
			$T_j = 150\text{ °C}$	-	136	-	ns
$t_r$	rise time		$T_j = 25\text{ °C}$	-	23	-	ns
			$T_j = 125\text{ °C}$	-	25	-	ns
			$T_j = 150\text{ °C}$	-	26	-	ns
$t_{d(off)}$	turn-off delay time		$T_j = 25\text{ °C}$	-	231	-	ns
			$T_j = 125\text{ °C}$	-	288	-	ns
			$T_j = 150\text{ °C}$	-	304	-	ns
$t_f$	fall time	$T_j = 25\text{ °C}$	-	134	-	ns	
		$T_j = 125\text{ °C}$	-	215	-	ns	
		$T_j = 150\text{ °C}$	-	216	-	ns	

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
E <sub>on</sub>	turn-on switching loss	V <sub>CC</sub> = 600 V; I <sub>C</sub> = 100 A; V <sub>GE</sub> = ±15 V; R <sub>Gon</sub> = 1.5 Ω; L <sub>S</sub> = 50 nH; di/dt = 4500 A/μs	T <sub>j</sub> = 25°C	-	5.3	-	mJ
			T <sub>j</sub> = 125°C	-	8.7	-	mJ
			T <sub>j</sub> = 150°C	-	10	-	mJ
E <sub>off</sub>	turn-off switching loss	V <sub>CC</sub> = 600 V; I <sub>C</sub> = 100 A; V <sub>GE</sub> = ±15 V; R <sub>Goff</sub> = 1.5 Ω; L <sub>S</sub> = 50 nH; du/dt = 5790 V/μs	T <sub>j</sub> = 25°C	-	5.1	-	mJ
			T <sub>j</sub> = 125°C	-	7.7	-	mJ
			T <sub>j</sub> = 150°C	-	8.8	-	mJ
I <sub>sc</sub>	short circuit data	V <sub>GE</sub> = 15 V; V <sub>CC</sub> = 800 V; T <sub>j</sub> = 150 °C; t <sub>p</sub> ≤ 10 μs	T <sub>j</sub> = 150°C	-	397	-	A
R <sub>th(j-c)</sub>	thermal resistance, junction to case	per IGBT	-	-	0.26	K/W	
T <sub>jop</sub>	operating junction temperature		-40	-	150	°C	
<b>Brake-chopper</b>							
BV <sub>CES</sub>	collector-emitter breakdown voltage	V <sub>GE</sub> = 0 V; I <sub>C</sub> = 1 mA		1200	-	-	V
I <sub>CES</sub>	collector-emitter cutoff current	V <sub>GE</sub> = 0 V; V <sub>CE</sub> = V <sub>CES</sub>		-	-	1	mA
I <sub>GES</sub>	gate leakage current	V <sub>CE</sub> = 0 V; V <sub>GE</sub> = V <sub>GES</sub>		-	-	±500	nA
V <sub>GE(th)</sub>	gate emitter threshold voltage	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1.7 mA		5	6.0	6.8	V
R <sub>G</sub>	internal gate resistor	f = 1 MHz		-	7.1		Ω
V <sub>CE(sat)</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 50 A; V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25°C	-	1.65	1.95	V
			T <sub>j</sub> = 125°C	-	1.8	-	V
			T <sub>j</sub> = 150°C	-	1.85	-	V
C <sub>ies</sub>	input capacitance	V <sub>GE</sub> = 0 V; V <sub>CE</sub> = 25 V; f = 100 kHz		-	3.65	-	nF
C <sub>oes</sub>	output capacitance			-	0.72	-	nF
C <sub>res</sub>	reverse transfer capacitance			-	0.12	-	nF
Q <sub>g</sub>	total gate charge	V <sub>CC</sub> = 960 V; I <sub>C</sub> = 50 A; V <sub>GE</sub> = ±15 V		-	0.26	-	μC
t <sub>d(on)</sub>	turn-on delay time	V <sub>CC</sub> = 600 V; I <sub>C</sub> = 50 A; V <sub>GE</sub> = ±15 V; R <sub>Gon</sub> = 15 Ω; R <sub>Goff</sub> = 15 Ω; L <sub>S</sub> = 50 nH	T <sub>j</sub> = 25°C	-	64	-	ns
			T <sub>j</sub> = 125°C	-	65	-	ns
			T <sub>j</sub> = 150°C	-	67	-	ns
t <sub>r</sub>	rise time		T <sub>j</sub> = 25°C	-	31	-	ns
			T <sub>j</sub> = 125°C	-	66	-	ns
			T <sub>j</sub> = 150°C	-	68	-	ns
t <sub>d(off)</sub>	turn-off delay time		T <sub>j</sub> = 25°C	-	147	-	ns
			T <sub>j</sub> = 125°C	-	178	-	ns
			T <sub>j</sub> = 150°C	-	187	-	ns
t <sub>f</sub>	fall time	T <sub>j</sub> = 25°C	-	144	-	ns	
		T <sub>j</sub> = 125°C	-	196	-	ns	
		T <sub>j</sub> = 150°C	-	213	-	ns	

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
E <sub>on</sub>	turn-on switching loss	V <sub>CC</sub> = 600 V; I <sub>C</sub> = 50 A; V <sub>GE</sub> = ±15 V; R <sub>Gon</sub> = 15 Ω; L <sub>S</sub> = 50 nH; dI/dt = 1590 A/μs	T <sub>j</sub> = 25°C	-	5.1	-	mJ
			T <sub>j</sub> = 125°C	-	7.2	-	mJ
			T <sub>j</sub> = 150°C	-	8.2	-	mJ
E <sub>off</sub>	turn-off switching loss	V <sub>CC</sub> = 600 V; I <sub>C</sub> = 50 A; V <sub>GE</sub> = ±15 V; R <sub>Goff</sub> = 15 Ω; L <sub>S</sub> = 50 nH; dV/dt = 6040 V/μs	T <sub>j</sub> = 25°C	-	1.93	-	mJ
			T <sub>j</sub> = 125°C	-	2.59	-	mJ
			T <sub>j</sub> = 150°C	-	2.81	-	mJ
I <sub>sc</sub>	short circuit data	V <sub>GE</sub> = 15 V; V <sub>CC</sub> = 800 V; T <sub>j</sub> = 150 °C; t <sub>p</sub> ≤ 10 μs	-	167	-	A	
R <sub>th(j-c)</sub>	thermal resistance, junction to case	per IGBT	-	-	0.48	K/W	
T <sub>jop</sub>	operating junction temperature		-40	-	150	°C	

Table 6. Diode

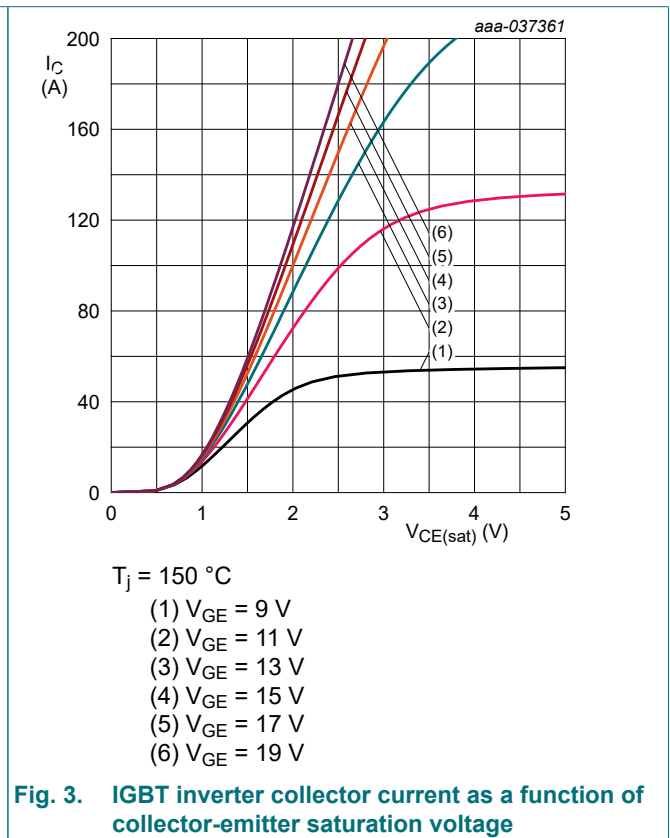
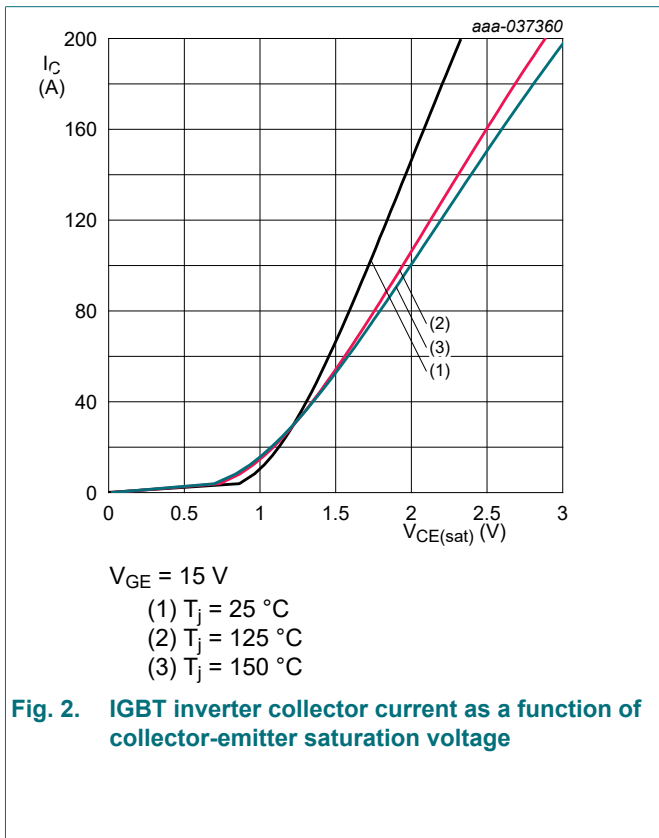
T<sub>j</sub> = 25 °C unless otherwise specified.

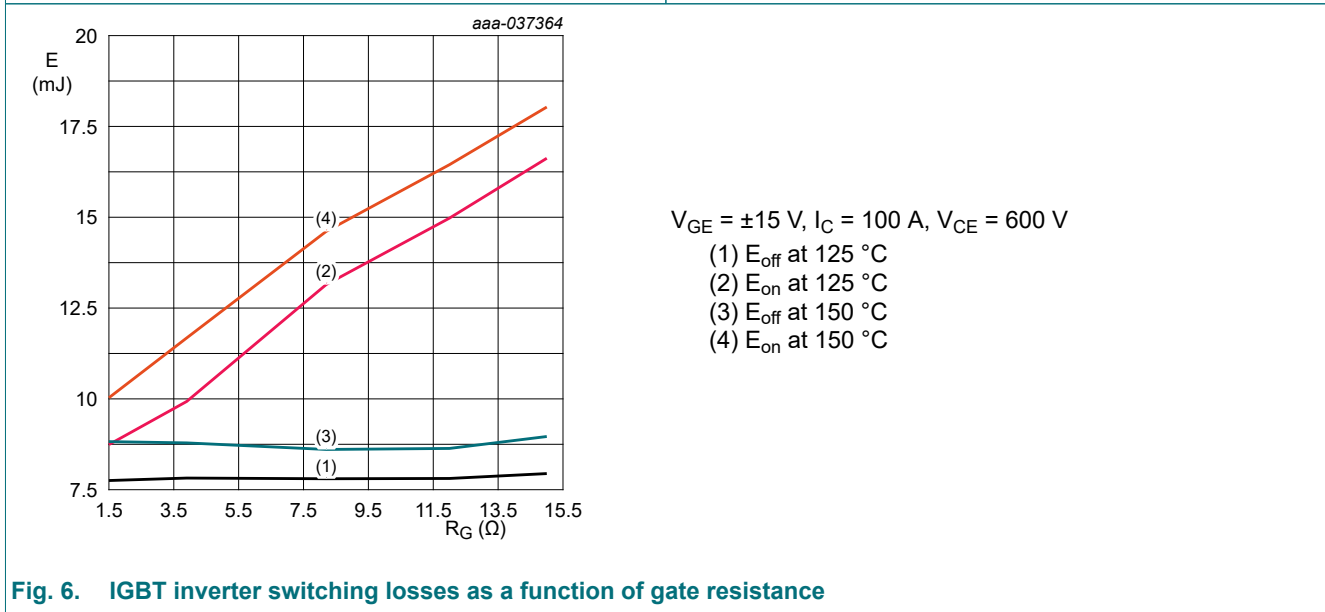
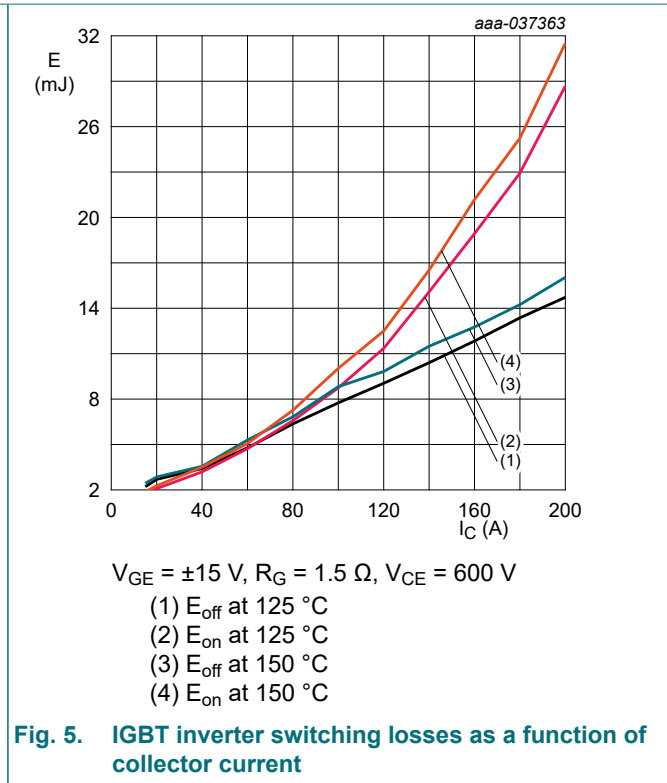
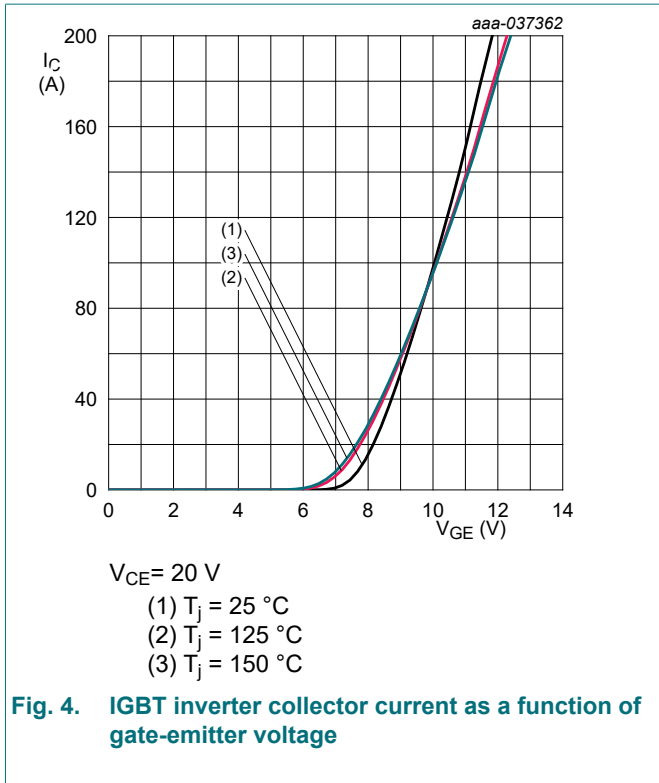
Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
<b>Inverter</b>							
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 100 A	T <sub>j</sub> = 25°C	-	1.7	2.1	V
			T <sub>j</sub> = 125°C	-	1.7	-	V
			T <sub>j</sub> = 150°C	-	1.7	-	V
I <sub>rr</sub>	peak reverse recovery current	I <sub>F</sub> = 100 A; V <sub>R</sub> = 600 V ; -dI <sub>F</sub> /dt = 2630 A/us; V <sub>GE</sub> = -15 V	T <sub>j</sub> = 25°C	-	126	-	A
			T <sub>j</sub> = 125°C	-	130	-	A
			T <sub>j</sub> = 150°C	-	132	-	A
Q <sub>rr</sub>	reverse recovery charge	I <sub>F</sub> = 100 A; V <sub>R</sub> = 600 V ; -dI <sub>F</sub> /dt = 2630 A/us; V <sub>GE</sub> = -15 V	T <sub>j</sub> = 25°C	-	6.48	-	μC
			T <sub>j</sub> = 125°C	-	12.8	-	μC
			T <sub>j</sub> = 150°C	-	15.4	-	μC
t <sub>rr</sub>	reverse recovery time	I <sub>F</sub> = 100 A; V <sub>R</sub> = 600 V ; -dI <sub>F</sub> /dt = 2630 A/us; V <sub>GE</sub> = -15 V	T <sub>j</sub> = 25°C	-	344	-	ns
			T <sub>j</sub> = 125°C	-	515	-	ns
			T <sub>j</sub> = 150°C	-	538	-	ns
E <sub>rec</sub>	reverse recovery energy	I <sub>F</sub> = 100 A; V <sub>R</sub> = 600 V ; -dI <sub>F</sub> /dt = 2630 A/us; V <sub>GE</sub> = -15 V	T <sub>j</sub> = 25°C	-	1.75	-	mJ
			T <sub>j</sub> = 125°C	-	4.3	-	mJ
			T <sub>j</sub> = 150°C	-	5.2	-	mJ
R <sub>th(j-c)</sub>	thermal resistance, junction to case	per diode	-	-	0.45	K/W	
T <sub>jop</sub>	operating junction temperature		-40	-	150	°C	
<b>Rectifier</b>							
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 100 A	T <sub>j</sub> = 150°C	-	0.99	-	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1600 V	T <sub>j</sub> = 150°C	-	1.5	-	A
R <sub>th(j-c)</sub>	Thermal resistance, junction to case	per diode		-	-	0.36	K/W
T <sub>jop</sub>	operating junction temperature			-40	-	150	°C

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Brake-chopper</b>							
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 50 A	T <sub>J</sub> = 25°C	-	1.74	2.1	V
			T <sub>J</sub> = 125°C	-	1.88	-	V
			T <sub>J</sub> = 150°C	-	1.86	-	V
I <sub>rr</sub>	peak reverse recovery current	I <sub>F</sub> = 50 A; V <sub>R</sub> = 600 V ; -di <sub>F</sub> /dt = 1510 A/us; V <sub>GE</sub> = -15 V	T <sub>J</sub> = 25°C	-	17	-	A
			T <sub>J</sub> = 125°C	-	20	-	A
			T <sub>J</sub> = 150°C	-	20	-	A
Q <sub>rr</sub>	reverse recovery charge	I <sub>F</sub> = 50 A; V <sub>R</sub> = 600 V ; -di <sub>F</sub> /dt = 1510 A/us; V <sub>GE</sub> = -15 V	T <sub>J</sub> = 25°C	-	3.04	-	μC
			T <sub>J</sub> = 125°C	-	5.52	-	μC
			T <sub>J</sub> = 150°C	-	6.29	-	μC
t <sub>rr</sub>	reverse recovery time	I <sub>F</sub> = 50 A; V <sub>R</sub> = 600 V ; -di <sub>F</sub> /dt = 1510 A/us; V <sub>GE</sub> = -15 V	T <sub>J</sub> = 25°C	-	363	-	ns
			T <sub>J</sub> = 125°C	-	536	-	ns
			T <sub>J</sub> = 150°C	-	616	-	ns
E <sub>rec</sub>	reverse recovery energy	I <sub>F</sub> = 50 A; V <sub>R</sub> = 600 V ; -di <sub>F</sub> /dt = 1510 A/us; V <sub>GE</sub> = -15 V	T <sub>J</sub> = 25°C	-	0.747	-	mJ
			T <sub>J</sub> = 125°C	-	1.65	-	mJ
			T <sub>J</sub> = 150°C	-	1.94	-	mJ
R <sub>th(j-c)</sub>	thermal resistance, junction to case	per diode		-	-	1.2	K/W
T <sub>Jop</sub>	operating junction temperature			-40	-	150	°C

### 7.1. Waveforms and output characteristics

Table 7.





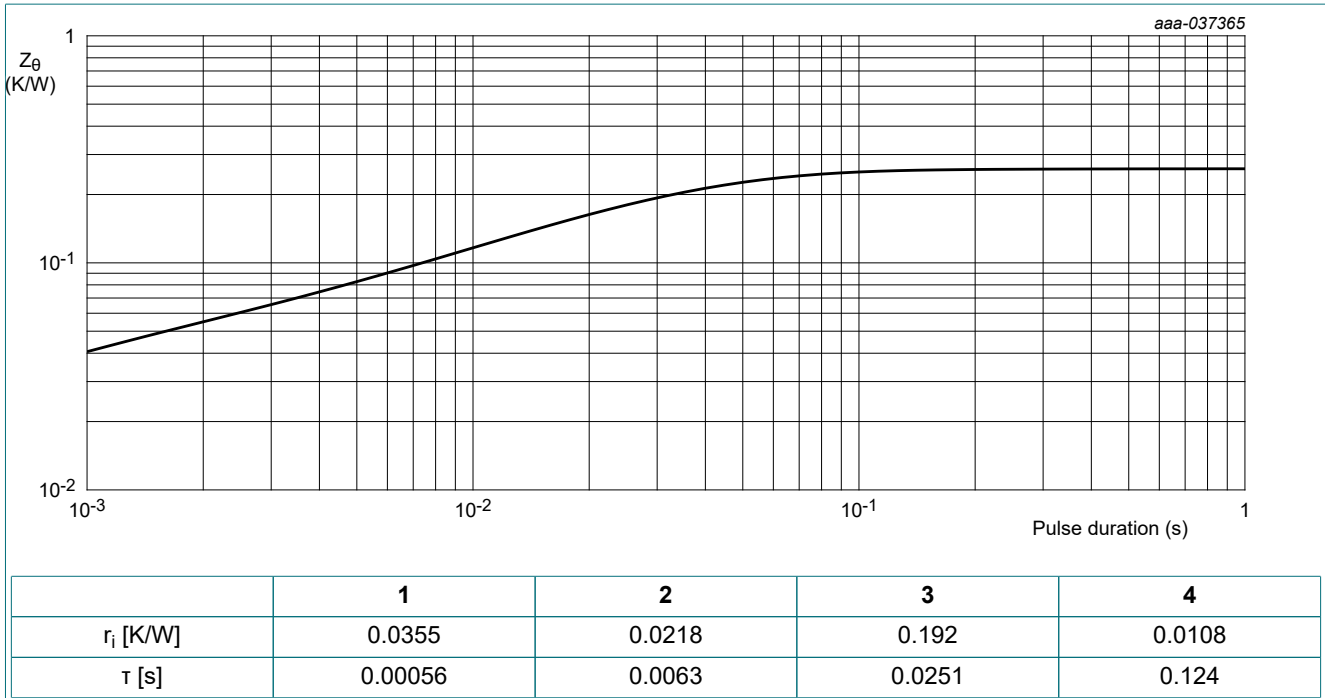


Fig. 7. Transient thermal impedance of IGBT inverter as a function of pulse duration

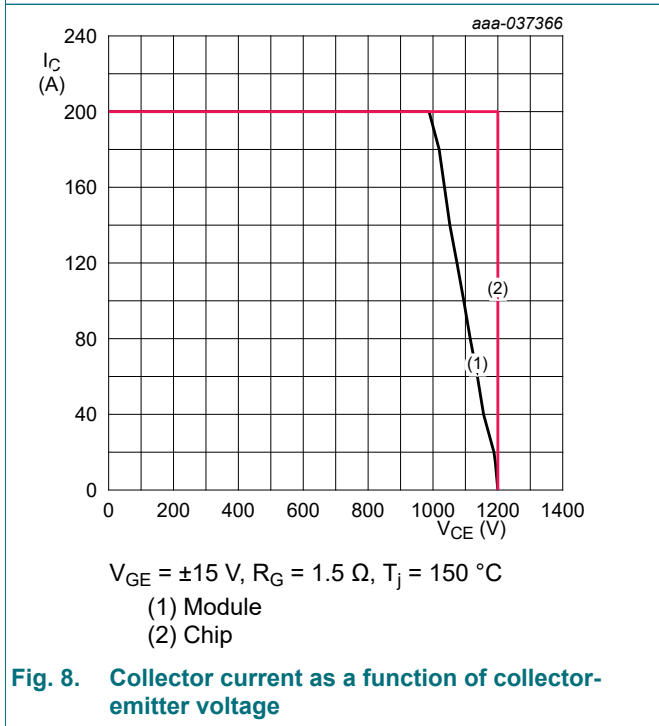


Fig. 8. Collector current as a function of collector-emitter voltage

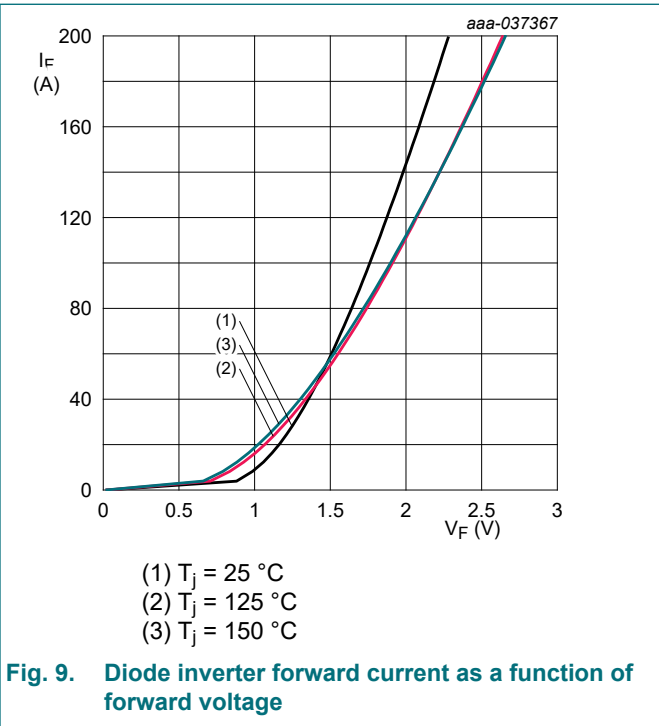


Fig. 9. Diode inverter forward current as a function of forward voltage



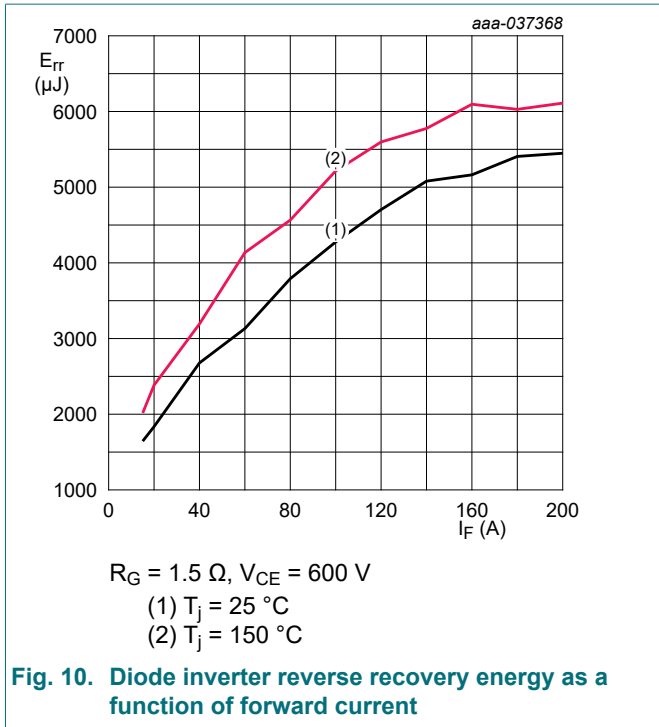


Fig. 10. Diode inverter reverse recovery energy as a function of forward current

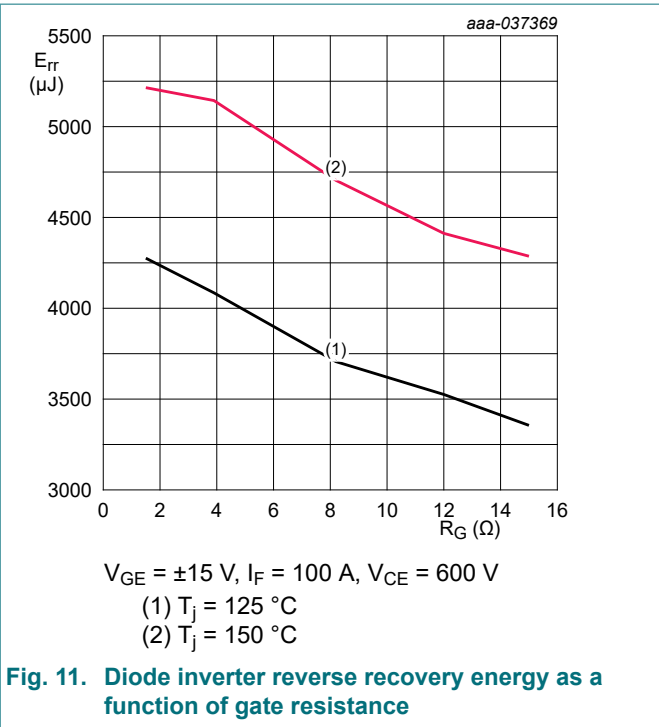


Fig. 11. Diode inverter reverse recovery energy as a function of gate resistance

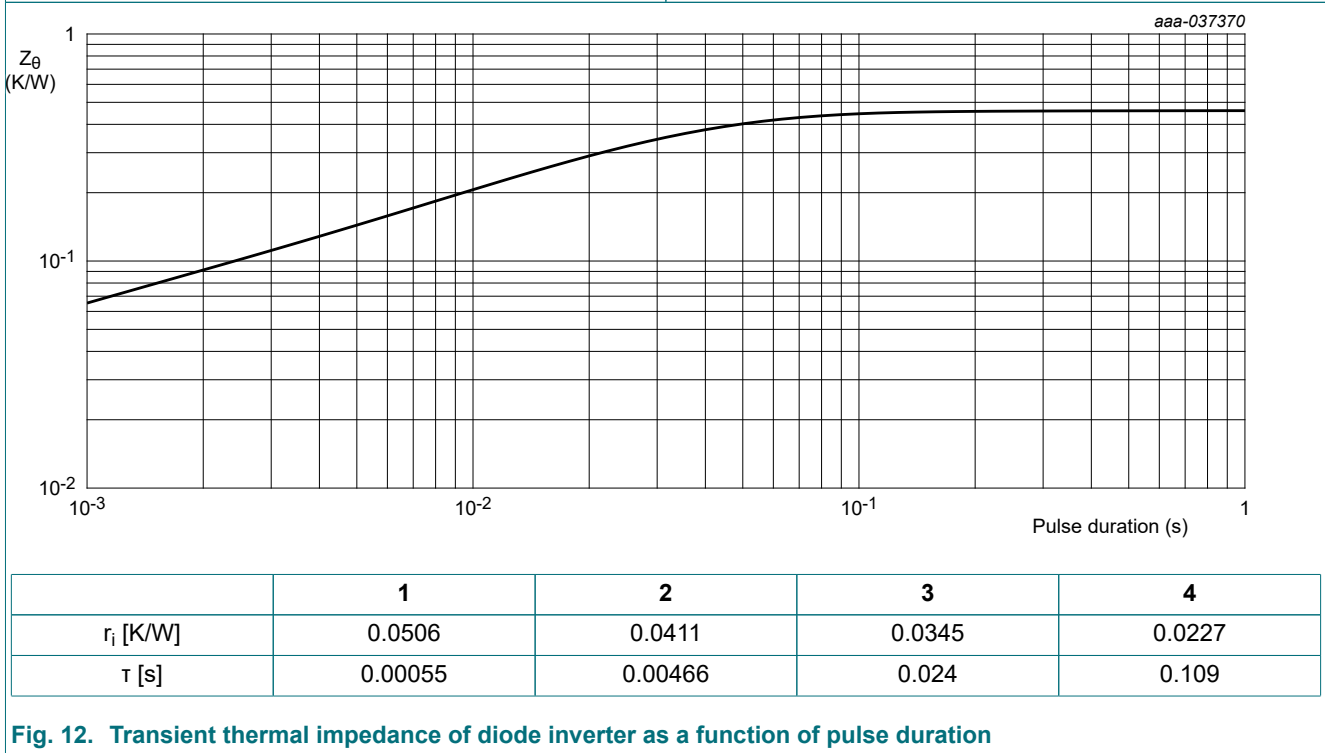


Fig. 12. Transient thermal impedance of diode inverter as a function of pulse duration

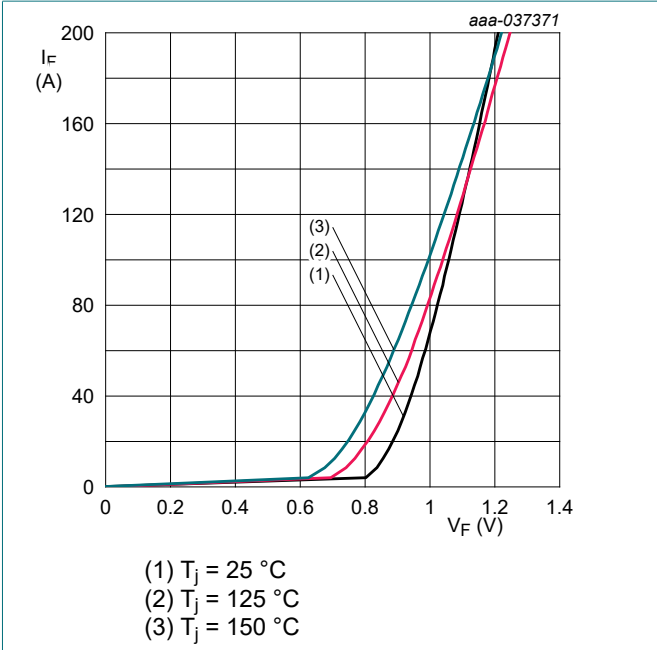


Fig. 13. Diode rectifier forward current as a function of forward voltage

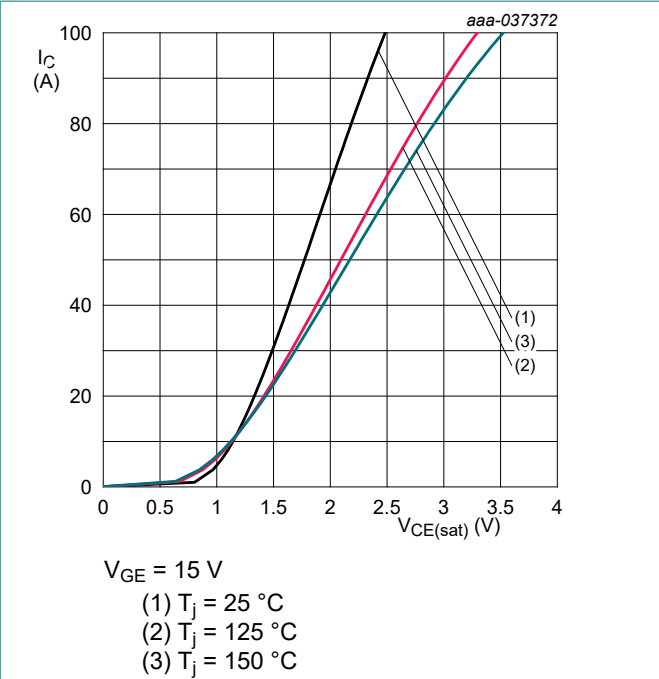


Fig. 14. IGBT brake collector current as a function of collector-emitter saturation voltage

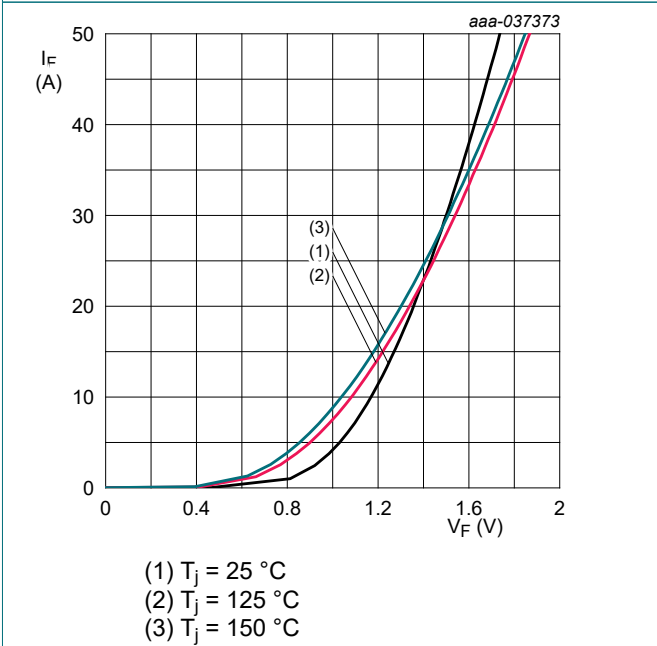


Fig. 15. Diode brake forward current as a function of forward voltage

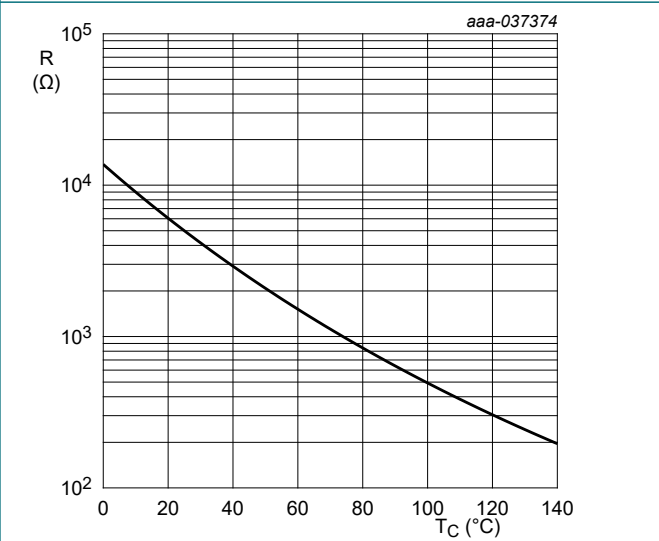


Fig. 16. NTC thermistor resistance as a function of temperature

## 8. NTC thermistor

Table 8. NTC thermistor

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
R <sub>25</sub>	rated resistance	T <sub>TNTC</sub> = 25 °C	-	5	-	kΩ
ΔR/R	deviation of R100	T <sub>TNTC</sub> = 100 °C; R <sub>100</sub> = 493 Ω	-10	-	10	%
P <sub>25</sub>	power dissipation	T <sub>TNTC</sub> = 25 °C	-	-	20	mW
B <sub>25/50</sub>	B-value		-	3375	-	K
B <sub>25/80</sub>	B-value		-	3414	-	K
B <sub>25/100</sub>	V-value		-	3436	-	K

## 9. Module characteristics

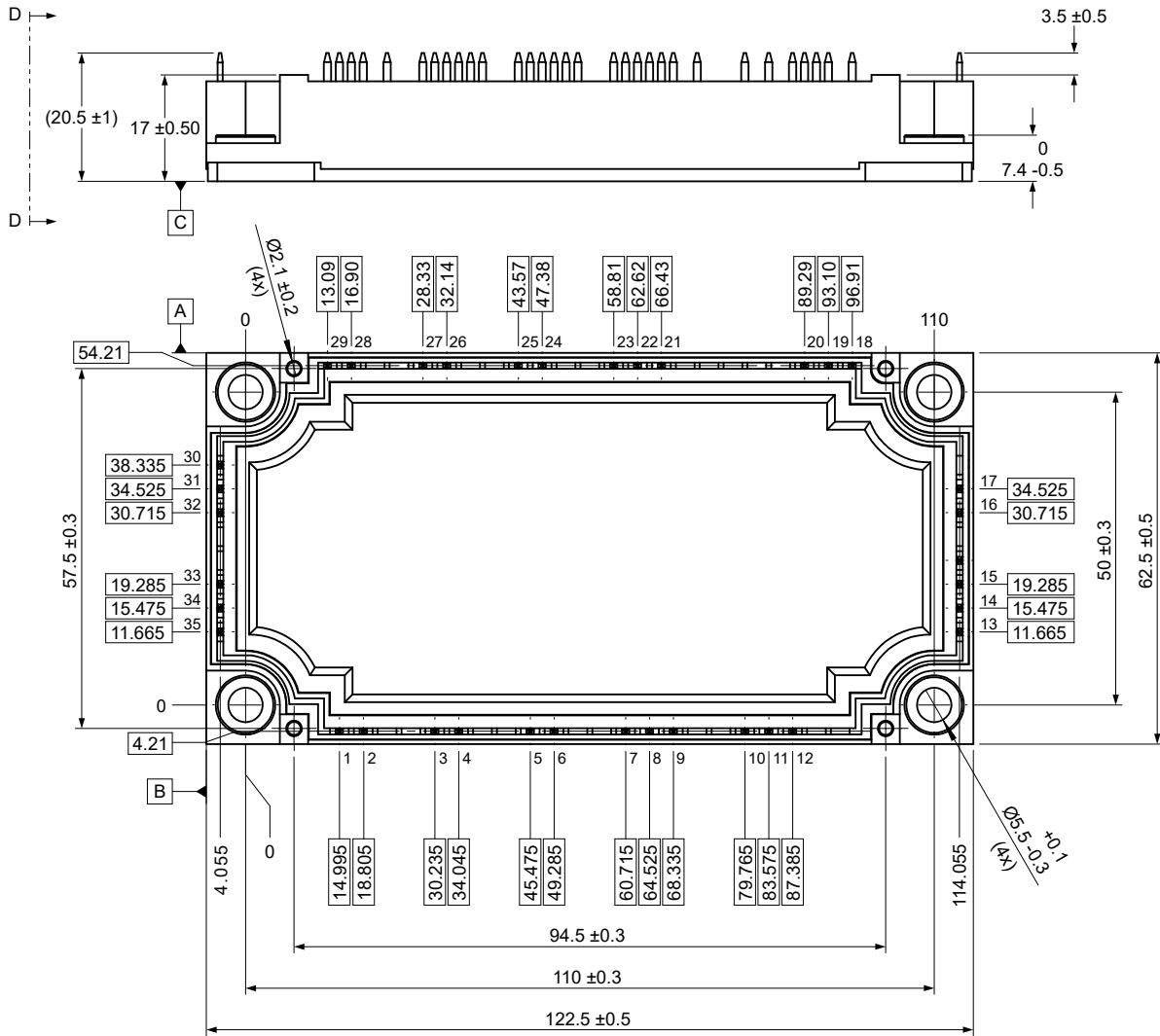
Table 9. Module characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>ISOL</sub>	isolation test voltage	RMS; f = 50 Hz, t = 1 min	2.5	-	-	kV
	creepage distance	terminal to heat sink	-	10	-	mm
		terminal to terminal				mm
	clearance	terminal to heat sink	-	7.5	-	mm
		terminal to terminal				mm
CTI	comparative tracking index		-	>200	-	
L <sub>sCE</sub>	stray inductance		-	35	-	nH
R <sub>CC'+EE'</sub>	module lead resistance, terminal-chip	T <sub>C</sub> = 25 °C per switch	-	1.2	-	mΩ
M	mounting torque for module mounting		-	-	-	Nm
G	weight		-	307	-	g
T <sub>stg</sub>	storage temperature		-40	-	125	°C

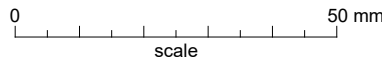
10. Package outline drawing

NP2-35P: plastic house, through hole solderable pin with copper baseplate, 35 pins; 62.5 mm x 122.5 mm x 17 mm body

SOT8053-1



Note: all pin position tolerance is  $\pm 0.5$  C A B



Dimensions (mm are the original dimensions)

sot8053-1\_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	EIAJ			
SOT8053-1						22-05-25 22-05-30

Fig. 17. Preliminary package outline drawing

# 11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NP100T12P2T3 v. 1	20240220	Product data sheet	-	-

## 12. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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