

30 V, 200 mA dual N-channel Trench MOSFET 29 October 2013

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Very fast switching
- Trench MOSFET technology
- ESD protection
- Low threshold voltage

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	-	200	mA
Static characteristics (per transistor)							
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 100 mA; T _j = 25 °C		-	2.7	4.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1		D1 D2
2	G1	gate TR1		
3	D2	drain TR2		$G1 \xrightarrow{f} G2$
4	S2	source TR2		
5	G2	gate TR2	SOT666	
6	D1	drain TR1	-	S1 S2 017aaa256

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
NX3020NAKV	SOT666	plastic surface-mounted package; 6 leads	SOT666				

7. Marking

Table 4. Marking codes	
Type number	Marking code
NX3020NAKV	GB

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	tor					
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-20	20	V
I _D dr	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C	[1]	-	200	mA
		V_{GS} = 4.5 V; T_{amb} = 100 °C	[1]	-	120	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	800	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	260	mW
			[1]	-	370	mW
		T _{sp} = 25 °C		-	1100	mW

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Symbol	Parameter	Conditions		Min	Мах	Unit
Source-dra	in diode	· · · · · · · · · · · · · · · · · · ·				
I _S	source current	T _{amb} = 25 °C		-	200	mA
Per device			I.			
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	375	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

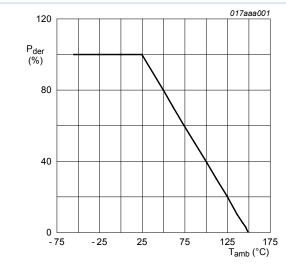


Fig. 1. Normalized total power dissipation as a function of ambient temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

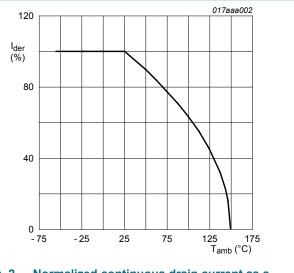
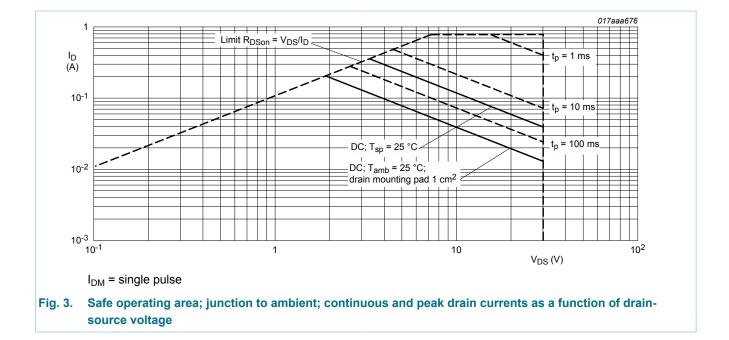


Fig. 2. Normalized continuous drain current as a function of ambient temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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9. Thermal characteristics

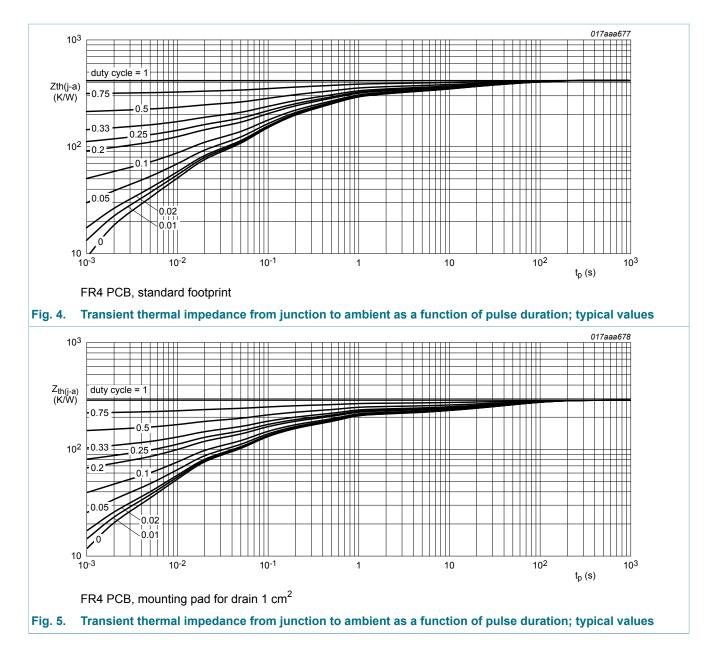
Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transis	tor	J.					
frc	thermal resistance	in free air	[1]	-	410	480	K/W
	from junction to ambient		[2]	-	290	340	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	105	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

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10. Characteristics

Table 7. C	haracteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics (per transistor)					
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	30	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS} ; T _j = 25 °C	0.8	1.2	1.5	V
I _{DSS}	drain leakage current	V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
		V_{DS} = 30 V; V_{GS} = 0 V; T_j = 150 °C	-	-	10	μA
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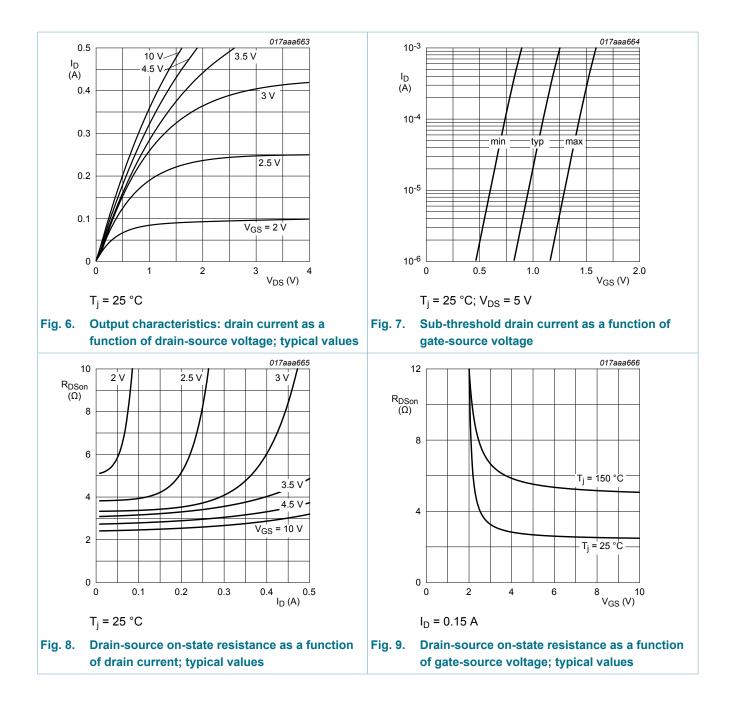
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	3.5	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	3.5	μA
		V_{GS} = 10 V; V_{DS} = 0 V; T_j = 25 °C	-	-	1	μA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C	-	-	1	μA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	0.5	μA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	0.5	μA
R _{DSon}	drain-source on-state	V_{GS} = 10 V; I _D = 100 mA; T _j = 25 °C	-	2.7	4.5	Ω
	resistance	V _{GS} = 10 V; I _D = 100 mA; T _j = 150 °C	-	5.5	9.2	Ω
		V _{GS} = 4.5 V; I _D = 100 mA; T _j = 25 °C	-	3	5.2	Ω
		V_{GS} = 2.5 V; I _D = 10 mA; T _j = 25 °C	-	4	13	Ω
9fs	forward transconductance	V _{DS} = 10 V; I _D = 150 mA; T _j = 25 °C	-	320	-	mS
Dynamic c	haracteristics (per transist	or)	1			
Q _{G(tot)}	total gate charge	V_{DS} = 15 V; I _D = 150 mA; V _{GS} = 4.5 V;	-	0.34	0.44	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.11	-	nC
Q _{GD}	gate-drain charge		-	0.06	-	nC
C _{iss}	input capacitance	V_{DS} = 10 V; f = 1 MHz; V_{GS} = 0 V;	-	13	20	pF
C _{oss}	output capacitance	T _j = 25 °C	-	2.6	-	pF
C _{rss}	reverse transfer capacitance		-	1.1	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 20 V; R _L = 250 Ω; V _{GS} = 10 V;	-	5	10	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	5	-	ns
t _{d(off)}	turn-off delay time		-	34	68	ns
t _f	fall time		-	17	-	ns
Source-dra	in diode (per transistor)	· · · · ·	I			
V _{SD}	source-drain voltage	I _S = 115 mA; V _{GS} = 0 V; T _i = 25 °C	0.47	0.7	1.2	V

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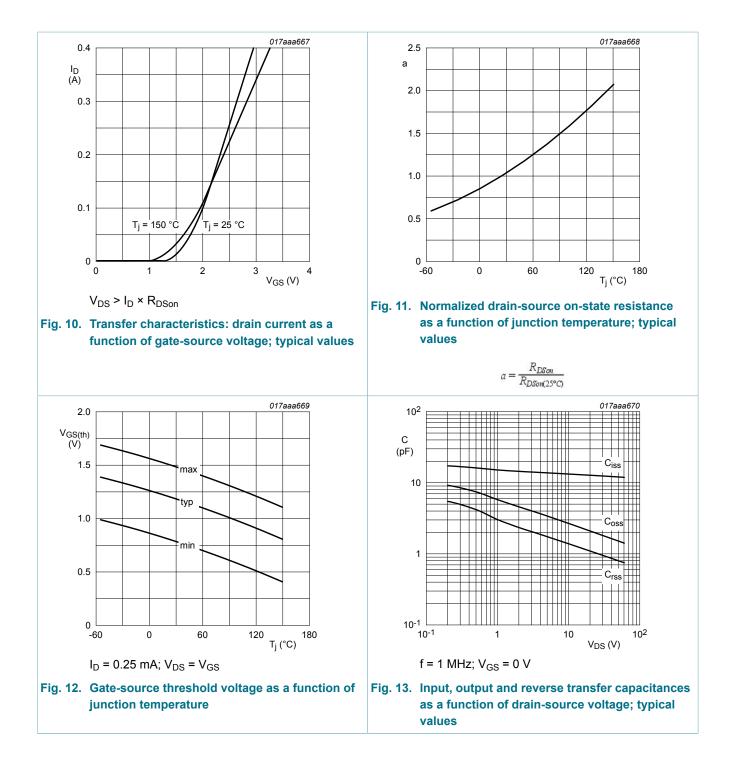
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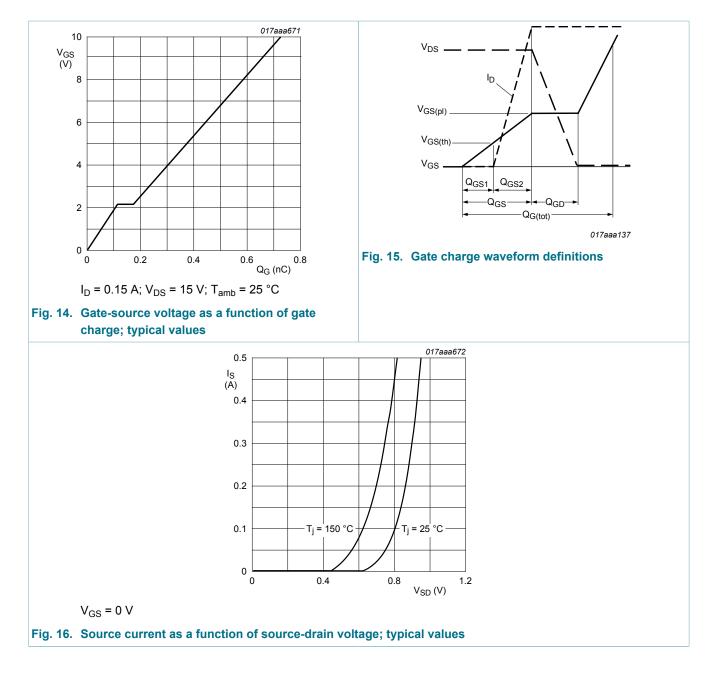
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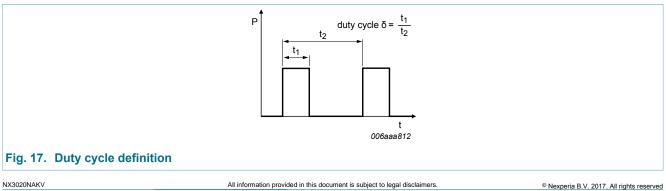


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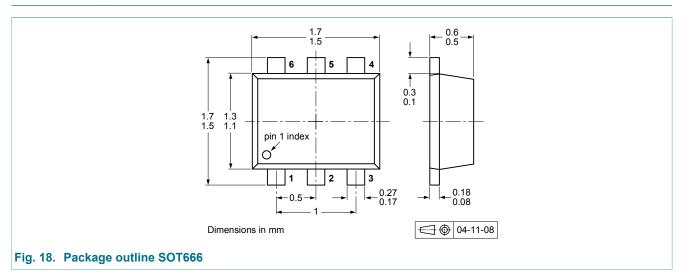


11. Test information

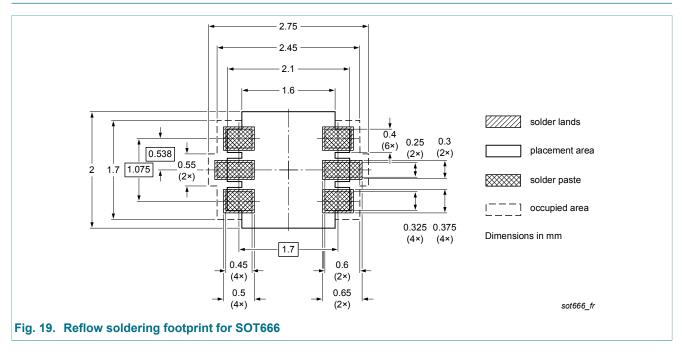


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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
NX3020NAKV v.2	20131029	Product data sheet	-	NX3020NAKV v.1			
Modifications:	 3D package outline added Table 7 values of capacitance parameters corrected Figure 13 corrected 						
NX3020NAKV v.1	20120706	Product data sheet	-	-			

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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