

# 15 V, 0.5 A NPN low VCEsat (BISS) transistor Rev. 1 — 26 January 2012

Product data sheet

#### 1. **Product profile**

#### **1.1 General description**

NPN low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a leadless ultra small SOT883B Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS3515MB.

#### 1.2 Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>

#### **1.3 Applications**

- DC-to-DC conversion
- Supply line switching
- Battery charger

#### 1.4 Quick reference data

- High efficiency due to less heat generation
- AEC-Q101 qualified
- Reduced Printed-Circuit Board (PCB) requirements
- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	15	V
I <sub>C</sub>	collector current		-	-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-	1	А
R <sub>CEsat</sub>	collector-emitter saturation resistance	$\begin{array}{l} I_{C} = 500 \text{ mA; } I_{B} = 50 \text{ mA; pulsed;} \\ t_{p} \leq 300  \mu\text{s; } \delta \leq 0.02 \text{ ; } T_{amb} = 25 ^{\circ}\text{C} \end{array}$	-	360	500	mΩ



#### 15 V, 0.5 A NPN low VCEsat (BISS) transistor

### 2. Pinning information

Table 2.	Pinning	j information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	Е	emitter		3
3	С	collector	2	1
			Transparent top view	2
			SOT883B	sym021

### 3. Ordering information

Table 3. Orderi	ng information		
Type number	Package		
	Name	Description	Version
PBSS2515MB	-	Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B

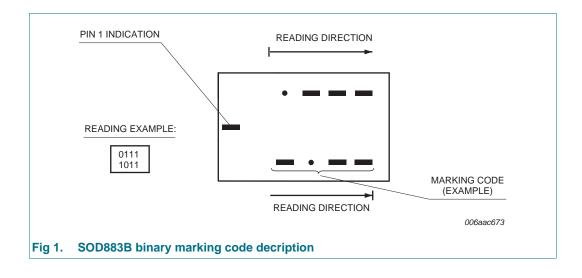
### 4. Marking

Table 4.	Marking	codes
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•	
Type number	Marking code <sup>[1]</sup>
PBSS2515MB	0001 0001

[1] For SOT883B binary marking code description, see Figure 1.

#### 4.1 Binary marking code description



15 V, 0.5 A NPN low VCEsat (BISS) transistor

### 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	15	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	15	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	<u>[1][2]</u>	-	250	mW
			[3][2]	-	590	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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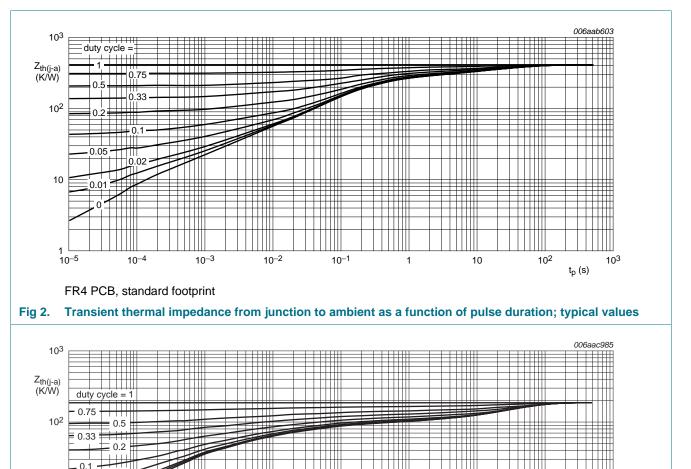
### 6. Thermal characteristics

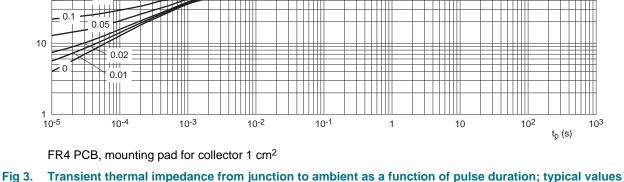
Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance	in free air	<u>[1][2]</u>	-	-	500	K/W
	from junction to ambient		<u>[3][2]</u>	-	-	212	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommented soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.





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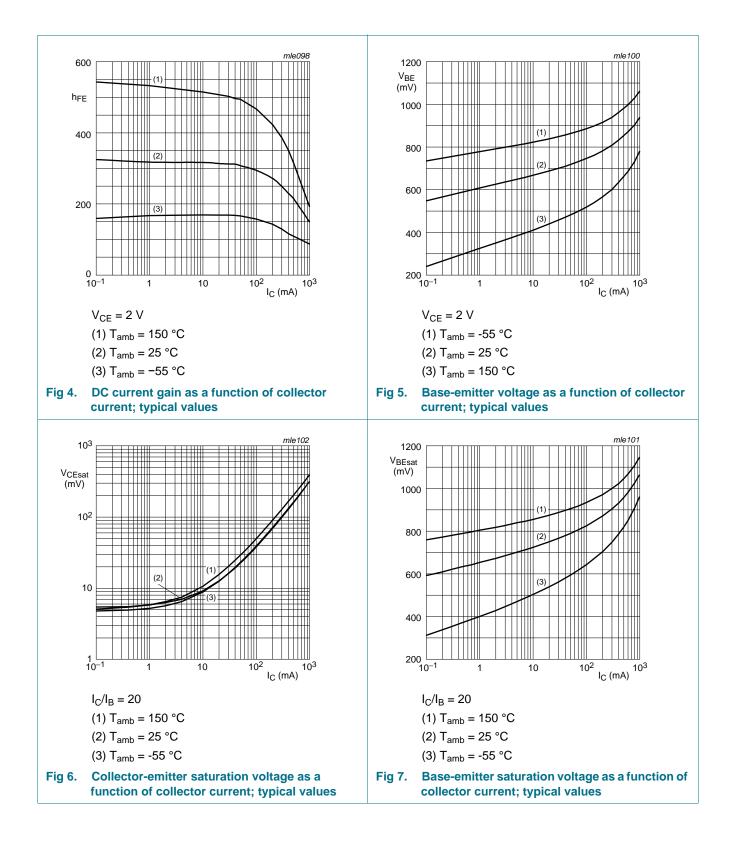
### 7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = 15 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	100	nA
	current	V <sub>CB</sub> = 15 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	50	μA
EBO	emitter-base cut-off current	$V_{EB}$ = 5 V; $I_{C}$ = 0 A; $T_{amb}$ = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \text{ T}_{amb} = 25 ^{\circ}\text{C}$	200	-	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	150	-	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 500 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	90	-	-	
V <sub>CEsat</sub>	collector-emitter	$I_C$ = 10 mA; $I_B$ = 0.5 mA; $T_{amb}$ = 25 °C	-	-	25	mV
	saturation voltage	$I_{C}$ = 200 mA; $I_{B}$ = 10 mA; pulsed; $t_{p} \le 300 \ \mu$ s; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	-	-	150	mV
		$\begin{split} I_{C} &= 500 \text{ mA; } I_{B} = 50 \text{ mA; pulsed;} \\ I_{p} &\leq 300  \mu\text{s; } \delta \leq 0.02 \text{ ; } T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	250	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C}$ = 500 mA; $I_{B}$ = 50 mA; pulsed; $t_{p}$ ≤ 300 µs; δ ≤ 0.02 ; $T_{amb}$ = 25 °C	-	360	500	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	$ \begin{array}{l} I_{C} = 500 \text{ mA; } I_{B} = 50 \text{ mA; pulsed;} \\ t_{p} \leq 300  \mu\text{s; } \delta \leq 0.02 \text{ ; } T_{amb} = 25 ^{\circ}\text{C} \end{array} $	-	-	1.1	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$V_{CE}$ = 2 V; I <sub>C</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	-	-	0.9	V
Т	transition frequency	$V_{CE}$ = 5 V; I <sub>C</sub> = 100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	250	420	-	MHz
Cc	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	4.4	6	pF

#### Nexperia

# PBSS2515MB

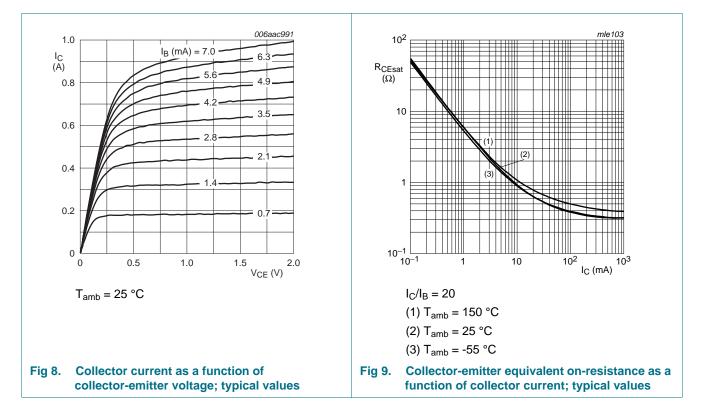
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#### Nexperia

# PBSS2515MB

#### 15 V, 0.5 A NPN low VCEsat (BISS) transistor



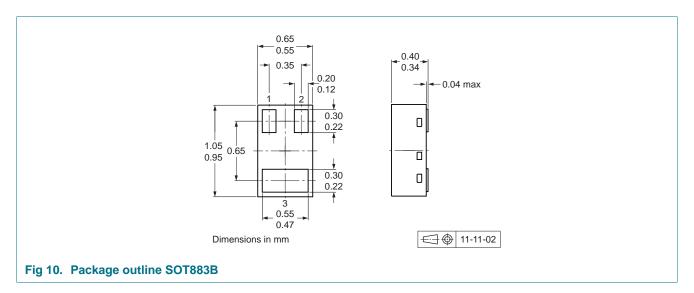
#### 8. Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors and is suitable for use in automotive applications.

15 V, 0.5 A NPN low VCEsat (BISS) transistor

#### **Package outline** 9.



### **10. Soldering**

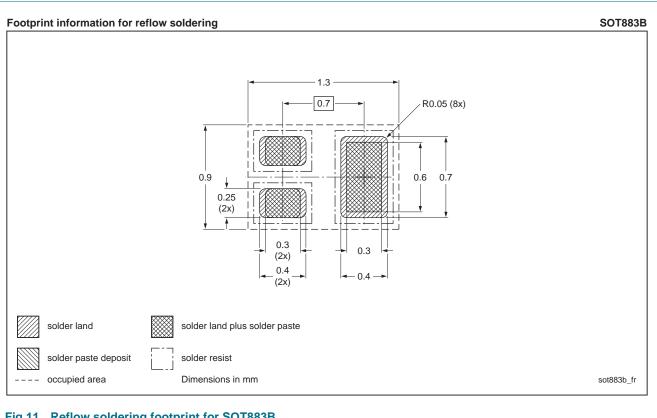


Fig 11. Reflow soldering footprint for SOT883B

PBSS2515MB **Product data sheet** 

#### 15 V, 0.5 A NPN low VCEsat (BISS) transistor

### **11. Revision history**

Table 8.	Revision history						
Document	ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS2515N	/IB v.1	20120126	Product data sheet	-	-		

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#### **12. Legal information**

#### **12.1 Data sheet status**

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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#### 15 V, 0.5 A NPN low VCEsat (BISS) transistor

#### 14. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Marking
5	Limiting values
6	Thermal characteristics4
7	Characteristics5
9	Package outline8
10	Soldering8
11	Revision history9
12	Legal information10
12.1	Data sheet status10
12.2	Definitions10
12.3	Disclaimers
12.4	Trademarks11
13	Contact information11

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