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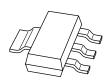
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PBSS303PZ 30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor Rev. 02 — 20 November 2009

Product data sheet

#### **Product profile** 1.

#### 1.1 General description

PNP low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS303NZ.

#### **1.2 Features**

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain (h<sub>FE</sub>) at high I<sub>C</sub>
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

#### 1.3 Applications

- DC-to-DC conversion
- MOSFET gate driving
- Motor control
- Charging circuits
- Power switches (e.g. motors, fans)

#### 1.4 Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-30	V
l <sub>C</sub>	collector current		-	-	-5.3	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	-	-10.6	A
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C} = -4 \text{ A};$ $I_{B} = -200 \text{ mA}$	<u>[1]</u> _	36	53	mΩ

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

## 2. Pinning information

Pin	Description	Simplified outline	Symbol		
1	base				
2	collector		2, 4		
3	emitter		1		
4	collector		3		
			sym028		

## 3. Ordering information

Table 3.         Ordering information						
Type number	Package					
	Name	Description	Version			
PBSS303PZ	SC-73	plastic surface-mounted package with increased heat sink; 4 leads	SOT223			

## 4. Marking

Table 4.         Marking codes	
Type number	Marking code
PBSS303PZ	S303PZ

30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

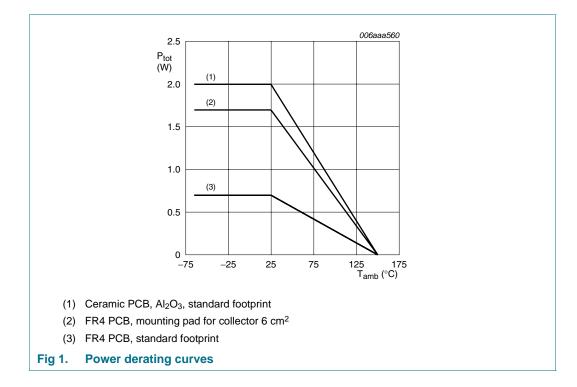
#### 5. Limiting values

Table 5. In accorda	Limiting values nce with the Absolute Maximu	um Rating System (I	EC 60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-30	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-30	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current		-	-5.3	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	-10.6	А
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	0.7	W
			[2] _	1.7	W
			[3] _	2	W
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

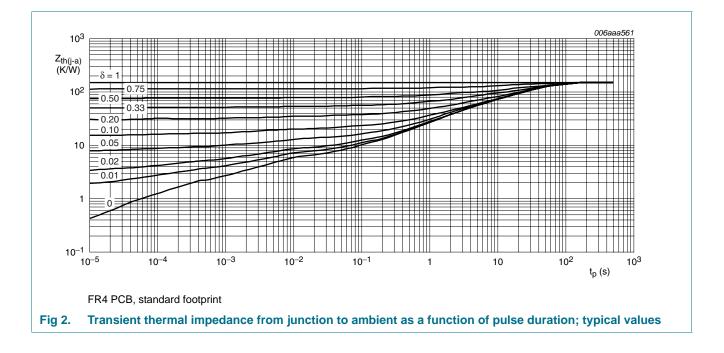
#### 6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from	in free air	<u>[1]</u> -	-	179	K/W
	junction to ambient		[2] _	-	74	K/W
			[3]	-	63	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	-	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

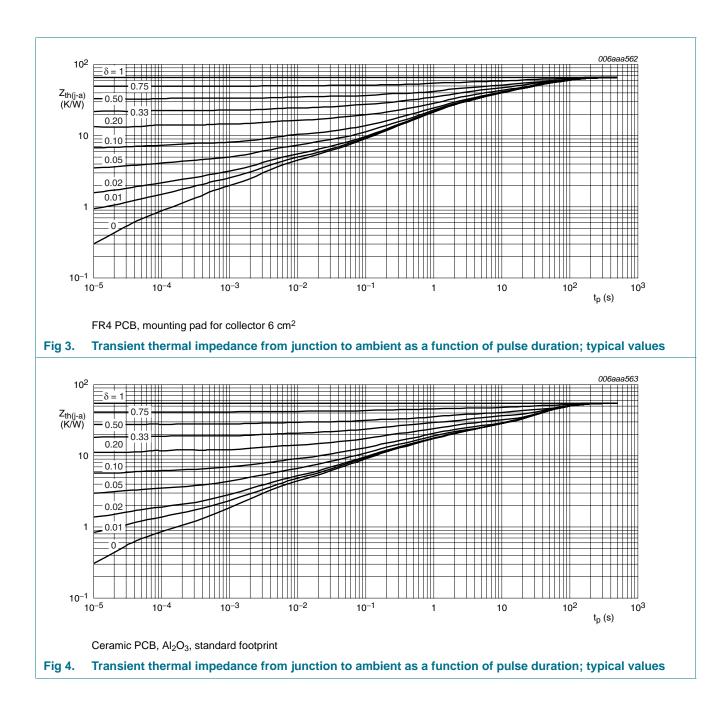
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



## PBSS303PZ

#### 30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor



30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

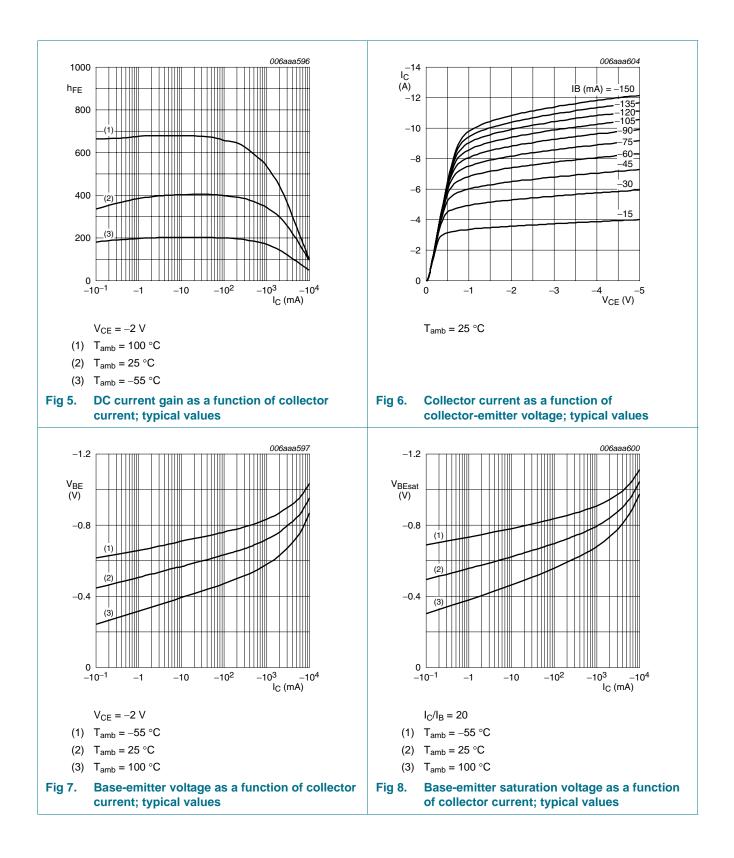
#### 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
	collector-base cut-off	$V_{CB} = -30 \text{ V}; \text{ I}_{E} = 0 \text{ A}$		-	-	-100	nA
	current	$V_{CB} = -30 \text{ V}; \text{ I}_{E} = 0 \text{ A};$ T <sub>j</sub> = 150 °C		-	-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -2 V; I <sub>C</sub> = -0.5 A	<u>[1]</u>	250	400	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -1 \text{ A}$	[1]	250	350	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -2 \text{ A}$	[1]	200	300	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -4 \text{ A}$	[1]	130	200	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -7 \text{ A}$	[1]	80	120	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C} = -0.5 \text{ A}; I_{B} = -50 \text{ mA}$	[1]	-	-25	-35	mV
		$I_{C} = -1 \text{ A}; I_{B} = -50 \text{ mA}$	[1]	-	-50	-70	mV
		$I_{\rm C} = -1$ A; $I_{\rm B} = -10$ mA	[1]	-	-75	-105	mV
		$I_{C} = -2 \text{ A}; I_{B} = -40 \text{ mA}$	[1]	-	-100	-140	mV
		$I_{C} = -4 \text{ A}; I_{B} = -200 \text{ mA}$	<u>[1]</u>	-	-145	-210	mV
		$I_{C} = -4 \text{ A}; I_{B} = -400 \text{ mA}$	[1]	-	-135	-190	mV
		$I_{C} = -4 \text{ A}; I_{B} = -40 \text{ mA}$	[1]	-	-245	-370	mV
		$I_{C} = -5.3 \text{ A}; I_{B} = -265 \text{ mA}$	[1]	-	-185	-265	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C} = -4 \text{ A}; I_{B} = -200 \text{ mA}$	[1]	-	36	53	mΩ
		$I_{C} = -4 \text{ A}; I_{B} = -40 \text{ mA}$	[1]	-	61	92	mΩ
V <sub>BEsat</sub>	base-emitter	$I_{C} = -1 \text{ A}; I_{B} = -100 \text{ mA}$	[1]	-	-0.82	-0.9	V
	saturation voltage	$I_{C} = -4 \text{ A}; I_{B} = -400 \text{ mA}$	[1]	-	-0.93	-1.05	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V}; I_C = -2 \text{ A}$	[1]	-	-0.76	-0.85	V
t <sub>d</sub>	delay time	$V_{CC}$ = -12.5 V; $I_{C}$ = -3 A;		-	15	-	ns
t <sub>r</sub>	rise time	I <sub>Bon</sub> = –0.15 A; I <sub>Boff</sub> = 0.15 A		-	55	-	ns
t <sub>on</sub>	turn-on time	BOIL - 0.10 V		-	70	-	ns
ts	storage time			-	215	-	ns
t <sub>f</sub>	fall time			-	105	-	ns
t <sub>off</sub>	turn-off time			-	320	-	ns
fT	transition frequency	$V_{CE} = -10 \text{ V}; I_C = -100 \text{ mA};$ f = 100 MHz		-	130	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz		-	110	160	pF

 $\label{eq:point} \begin{tabular}{ll} \mbox{Pulse test: } t_p \leq 300 \ \mu \mbox{s; } \delta \leq 0.02. \end{tabular}$ 

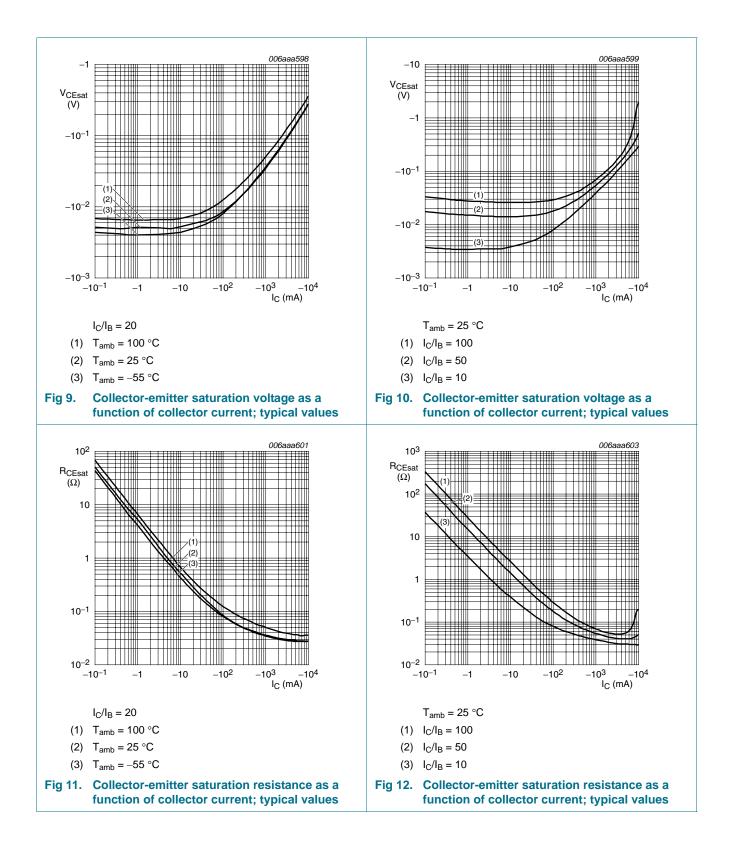
## PBSS303PZ

#### 30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor



## PBSS303PZ

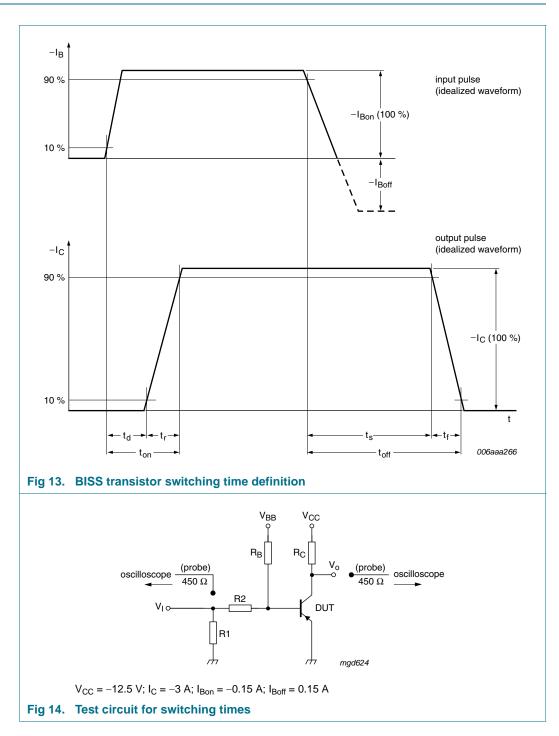
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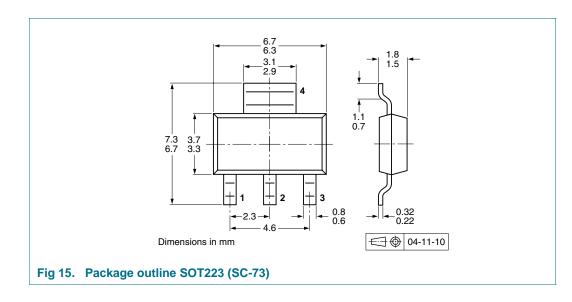
30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

#### 8. Test information



30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

## 9. Package outline



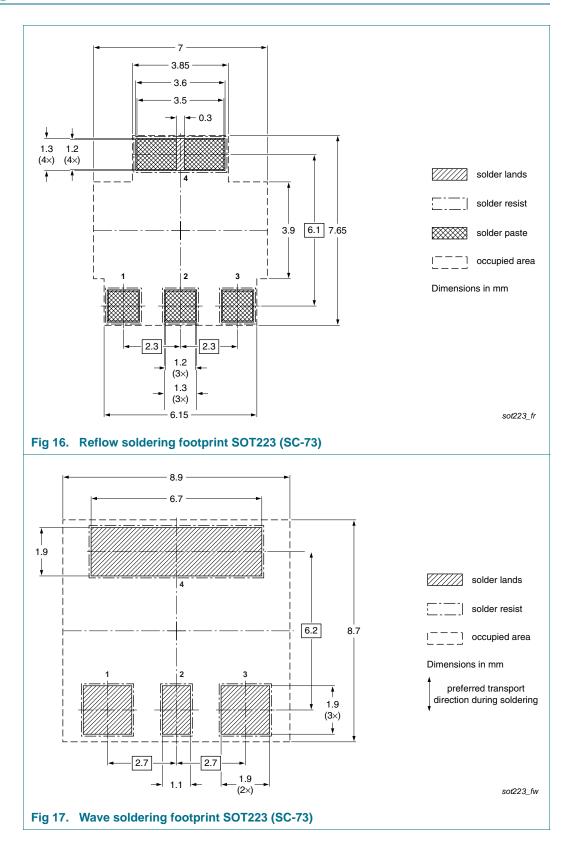
#### **10. Packing information**

# Table 8. Packing methodsThe indicated -xxx are the last three digits of the 12NC ordering code.[1]Type numberPackageDescriptionPacking quantity10004000PBSS303PZSOT2238 mm pitch, 12 mm tape and reel-115-135

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

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#### 11. Soldering



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## **12. Revision history**

Table 9. Revision	history					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS303PZ_2	20091120	Product data sheet	-	PBSS303PZ_1		
Modifications:		eet was changed to reflect w legal definitions and disc				
	<ul> <li>Figure 16 "Reflow soldering footprint SOT223 (SC-73)": updated</li> </ul>					
	<ul> <li>Figure 17 "W</li> </ul>	ave soldering footprint SO	T223 (SC-73)": update	d		
PBSS303PZ_1	20060914	Product data sheet	-	-		

30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

### 13. Legal information

#### Data sheet status 13.1

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

The term 'short data sheet' is explained in section "Definitions". [2]

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PBSS303PZ 2

## PBSS303PZ

30 V, 5.3 A PNP low V<sub>CEsat</sub> (BISS) transistor

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