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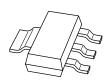
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PBSS304PZ 60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor Rev. 02 — 8 December 2009

Product data sheet

#### **Product profile** 1.

## 1.1 General description

PNP low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS304NZ.

## **1.2 Features**

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain (h<sub>FE</sub>) at high I<sub>C</sub>
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

## 1.3 Applications

- High-voltage DC-to-DC conversion
- High-voltage MOSFET gate driving
- High-voltage motor control
- High-voltage power switches (e.g. motors, fans)
- Automotive applications

## 1.4 Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-60	V
I <sub>C</sub>	collector current		-	-	-4.5	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	-	-9	A
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C} = -4 \text{ A};$ $I_{B} = -200 \text{ mA}$	[1] -	53	75	mΩ

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# 2. Pinning information

Pin	Description	Simplified outline	Symbol	
1	base			
2	collector		2, 4	
3	emitter		1	
4	collector		3	
			sym028	

# 3. Ordering information

Table 3. Ordering information						
Type number	Package	ge				
	Name	Description	Version			
PBSS304PZ	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223			

# 4. Marking

Table 4.   Marking codes	
Type number	Marking code
PBSS304PZ	S304PZ

60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

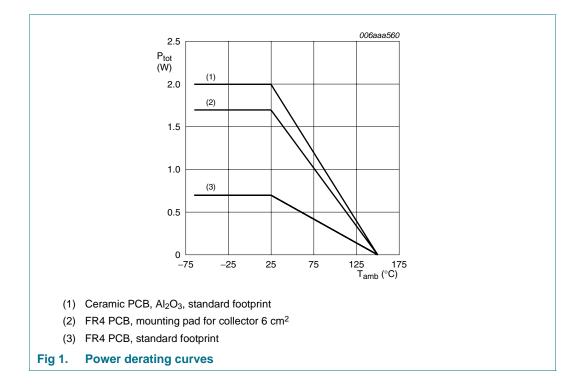
# 5. Limiting values

Table 5. In accorda	Limiting values nce with the Absolute Maximum F	Rating System (IEC 6	0134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current		-	-4.5	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	-9	A
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	0.7	W
			[2] _	1.7	W
			[3] _	2.0	W
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

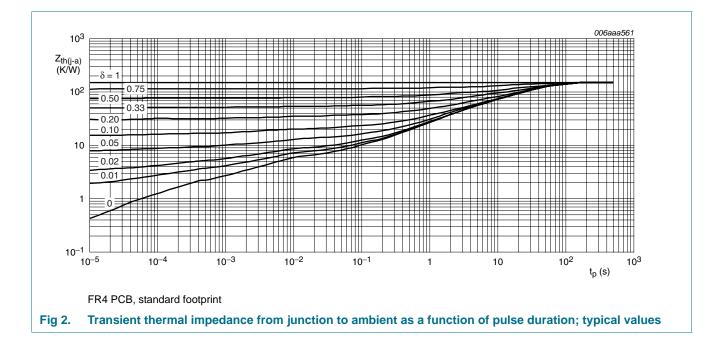
# 6. Thermal characteristics

Table 6.	Thermal characteristics	5				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	179	K/W
			[2] _	-	74	K/W
			[3]	-	63	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	-	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

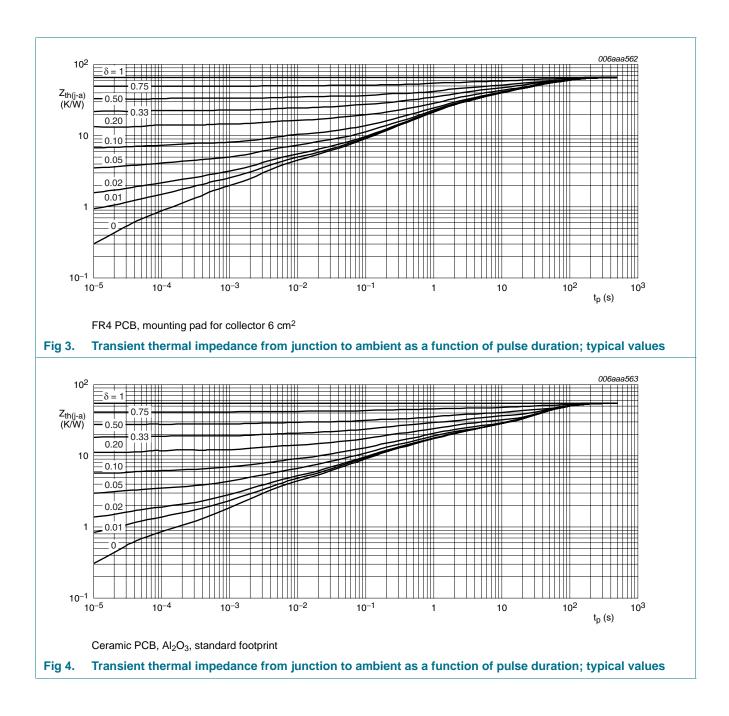
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



# PBSS304PZ

## 60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor



60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

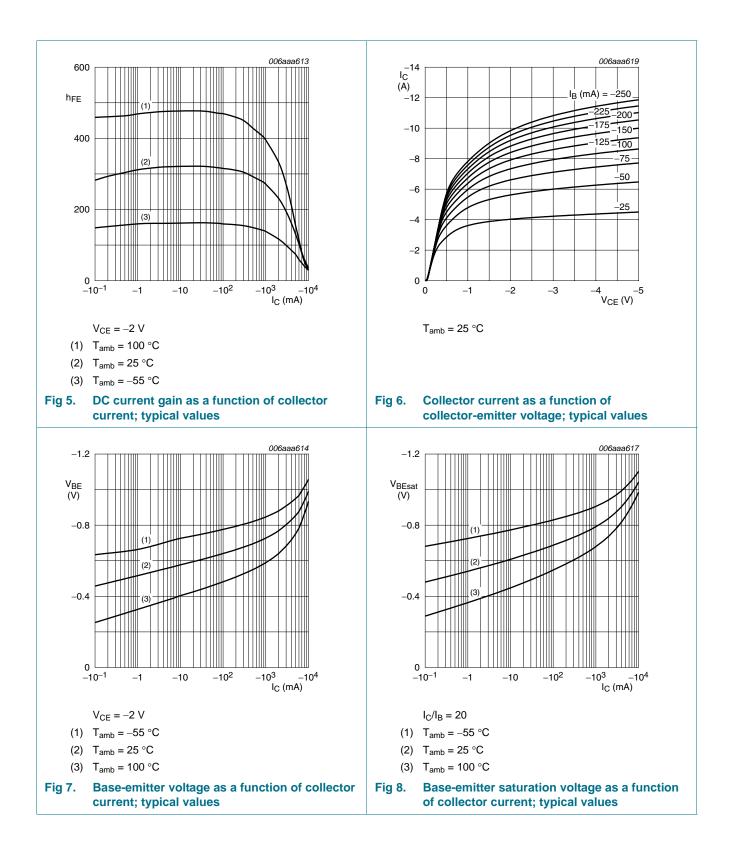
# 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
000	collector-base cut-off current	$V_{CB} = -60 \text{ V}; I_E = 0 \text{ A}$		-	-	-100	nA
		$V_{CB} = -60 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 ^{\circ}\text{C}$		-	-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -2 V; I <sub>C</sub> = -0.5 A	[1]	200	295	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -1 \text{ A}$	[1]	200	270	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -2 \text{ A}$	[1]	150	230	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -4 \text{ A}$	[1]	120	170	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -6 \text{ A}$	<u>[1]</u>	60	100	-	
OLOUI	collector-emitter saturation voltage	$I_{C} = -0.5 \text{ A}; I_{B} = -50 \text{ mA}$	<u>[1]</u>	-	-35	-50	mV
		$I_{C} = -1$ A; $I_{B} = -50$ mA	[1]	-	-65	-90	mV
		$I_{C} = -1 \text{ A}; I_{B} = -10 \text{ mA}$	[1]	-	-130	-190	mV
		$I_C = -2$ A; $I_B = -40$ mA	[1]	-	-165	-230	mV
		$I_{C} = -4 \text{ A}; I_{B} = -200 \text{ mA}$	<u>[1]</u>	-	-210	-300	mV
		$I_{C} = -4 \text{ A}; I_{B} = -400 \text{ mA}$	<u>[1]</u>	-	-160	-230	mV
		$I_{C} = -4.5 \text{ A}; I_{B} = -225 \text{ mA}$	<u>[1]</u>	-	-265	-375	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C} = -4 \text{ A}; I_{B} = -200 \text{ mA}$	[1]	-	53	75	mΩ
		$I_C = -2 \text{ A}; I_B = -40 \text{ mA}$	[1]	-	82	115	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C} = -1 \text{ A}; I_{B} = -100 \text{ mA}$	[1]	-	-0.81	-0.9	V
		$I_{C} = -4 \text{ A}; I_{B} = -400 \text{ mA}$	[1]	-	-0.93	-1.05	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$V_{CE} = -2 V; I_C = -2 A$	<u>[1]</u>	-	-0.77	-0.85	V
t <sub>d</sub>	delay time	$V_{CC} = -12.5 \text{ V}; I_C = -3 \text{ A};$		-	15	-	ns
t <sub>r</sub>	rise time	I <sub>Bon</sub> = -0.15 A; -I <sub>Boff</sub> = 0.15 A		-	65	-	ns
t <sub>on</sub>	turn-on time	Bolt - 0.13 A		-	80	-	ns
t <sub>s</sub>	storage time			-	225	-	ns
t <sub>f</sub>	fall time			-	95	-	ns
t <sub>off</sub>	turn-off time			-	320	-	ns
f <sub>T</sub>	transition frequency	$V_{CE} = -10 \text{ V}; I_C = -100 \text{ mA};$ f = 100 MHz		-	130	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz		-	90	120	pF

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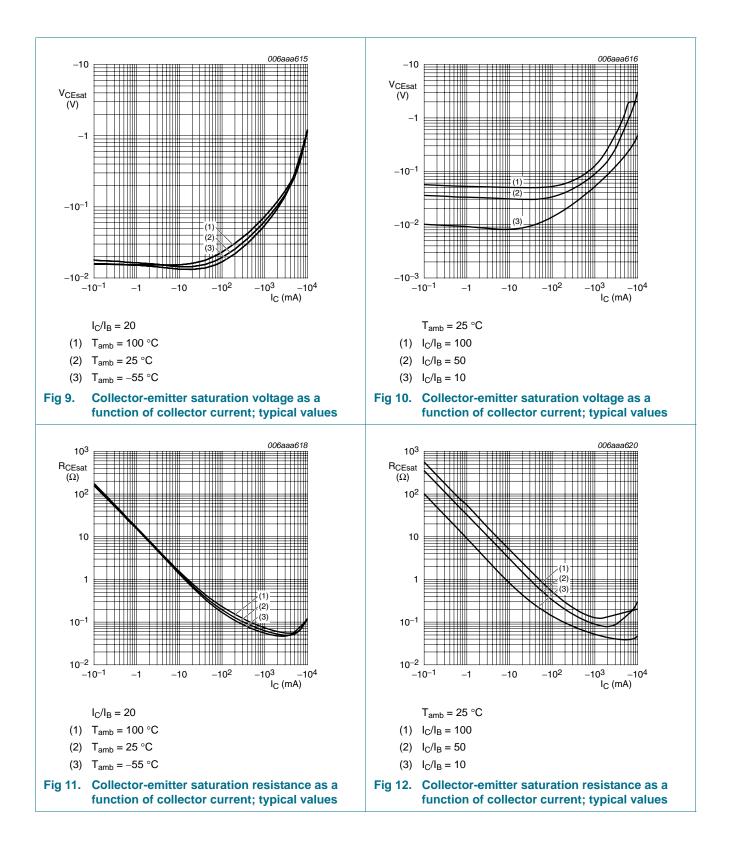
# PBSS304PZ

## 60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor



# PBSS304PZ

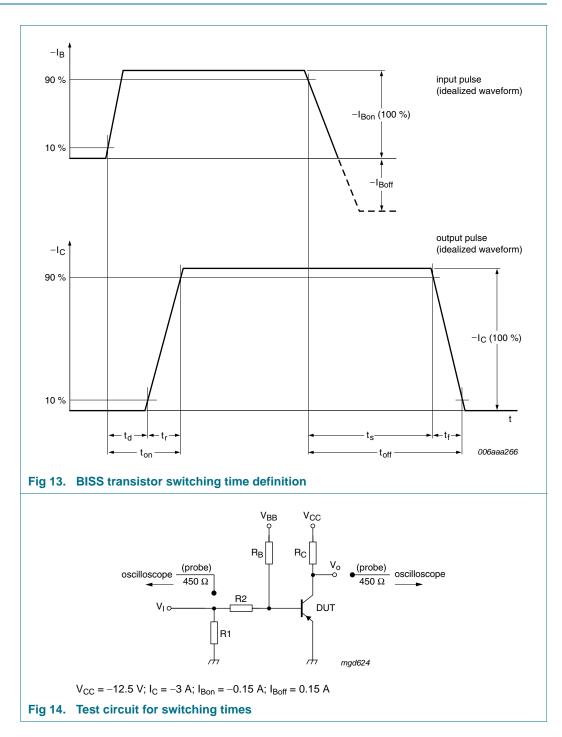
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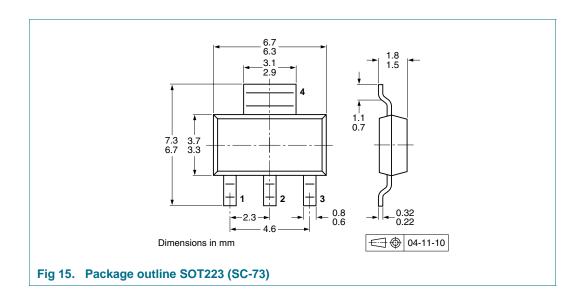
60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# 8. Test information



60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# 9. Package outline



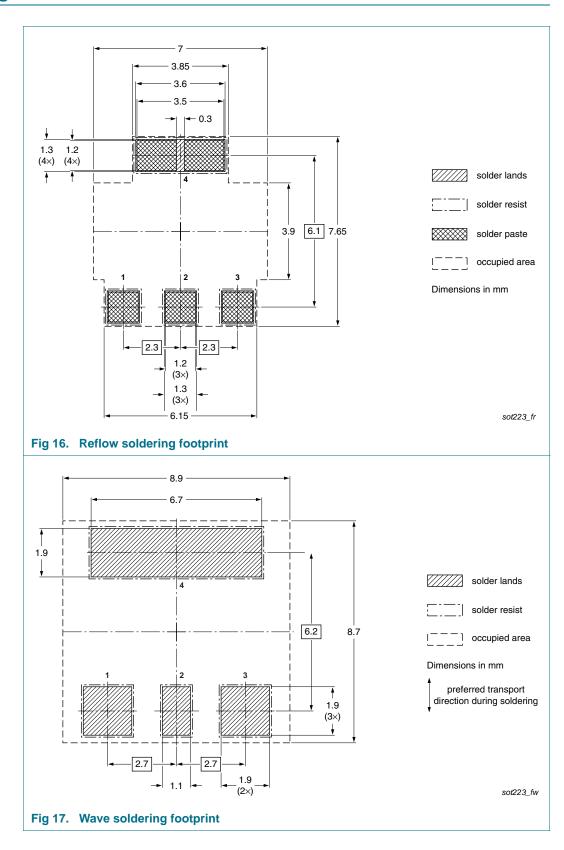
# **10. Packing information**

# Table 8. Packing methods The indicated -xxx are the last three digits of the 12NC ordering code.[1] Type number Package Description Packing quantity 1000 4000 PBSS304PZ SOT223 8 mm pitch, 12 mm tape and reel -115 -135

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# 11. Soldering





60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# **12. Revision history**

Table 9. Revision	history					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS304PZ_2	20091208	Product data sheet	-	PBSS304PZ_1		
Modifications:		eet was changed to reflect w legal definitions and disc				
	<ul> <li>Figure 16 "Reflow soldering footprint": updated</li> </ul>					
	<ul> <li>Figure 17 "W</li> </ul>	lave soldering footprint": up	odated			
PBSS304PZ_1	20060919	Product data sheet	-	-		

60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

# 13. Legal information

#### Data sheet status 13.1

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

The term 'short data sheet' is explained in section "Definitions". [2]

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://w

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PBSS304PZ 2

# PBSS304PZ

60 V, 4.5 A PNP low V<sub>CEsat</sub> (BISS) transistor

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