40 V, 2 A NPN low VCEsat (BISS) transistor

15 October 2012

Product data sheet

### 1. Product profile

#### 1.1 General description

NPN low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a medium power and flat lead SOT89 Surface-Mounted Device (SMD) plastic package. PNP complement: PBSS5240X.

#### **1.2 Features and benefits**

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High efficiency due to less heat generation

#### **1.3 Applications**

- DC-to-DC conversion
- Supply line switching
- Battery charger
- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- Inductive load driver (e.g. relays, buzzers and motors)

#### 1.4 Quick reference data

Table 1. Qui	Fable 1. Quick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	40	V
I <sub>C</sub>	collector current			-	-	2	А
I <sub>CM</sub>	peak collector current			-	-	3	А
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C}$ = 1 A; $I_{B}$ = 100 mA; pulsed; $t_{p} \le 300 \ \mu$ s; δ ≤ 0.02 ; $T_{amb}$ = 25 °C		-	-	260	mΩ
I <sub>CRM</sub>	repetitive peak collector current	$t_p \leq 20$ ms; $\delta \leq 0.33$ ; pulsed		-	-	2.5	A

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### 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		c
2	С	collector		в
3	В	base		
			SOT89	sym123

### 3. Ordering information

Table 3. Ordering information							
Type number	Package	lackage					
	Name	Description	Version				
PBSS4240X	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89				

### 4. Marking

Table 4. Marking codes	Marking codes					
Type number	Marking code					
PBSS4240X	S47					

### 5. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	40	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	2	А
I <sub>CRM</sub>	repetitive peak collector current	$\delta \leq 0.33$ ; $t_p \leq 20$ ms; pulsed		-	2.5	А
I <sub>CM</sub>	peak collector current			-	3	А
I <sub>B</sub>	base current			-	300	mA
I <sub>BM</sub>	peak base current			-	1	А
P <sub>tot</sub>	total power dissipation		[1]	-	0.5	W
			[2]	-	0.95	W

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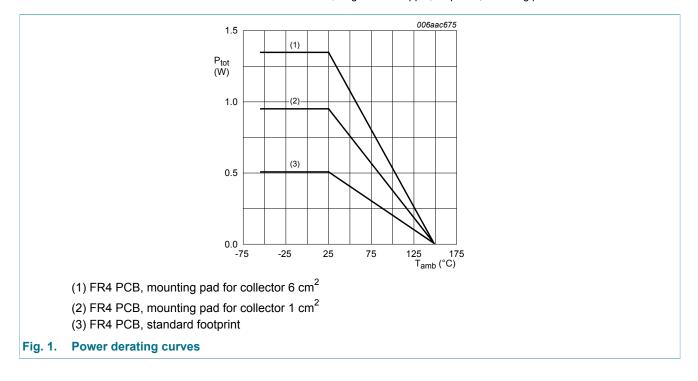
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Symbol	Parameter	Conditions		Min	Max	Unit
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



### 6. Thermal characteristics

Table 6. The	ermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient		[2	[1]	-	-	250	K/W
			[2]	-	-	132	K/W
	ampient		[3]	-	-	93	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	16	K/W

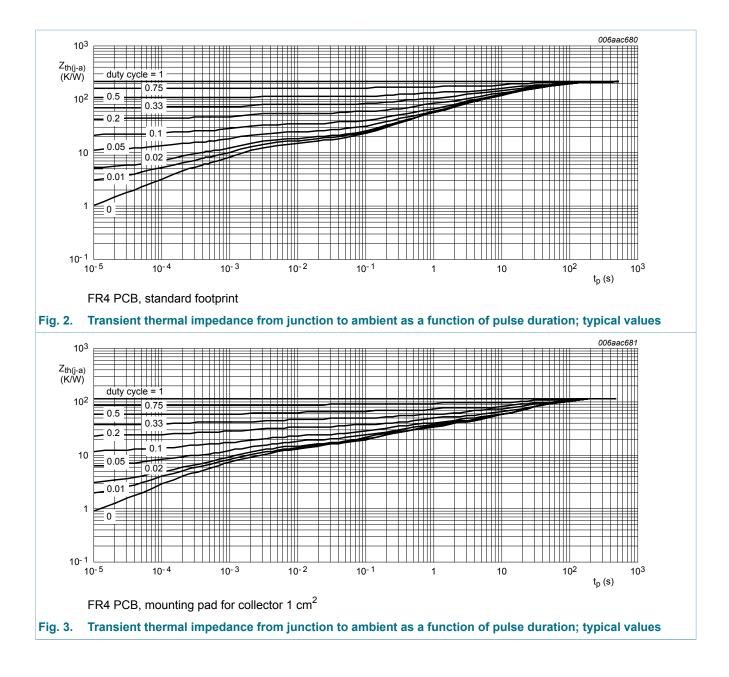
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



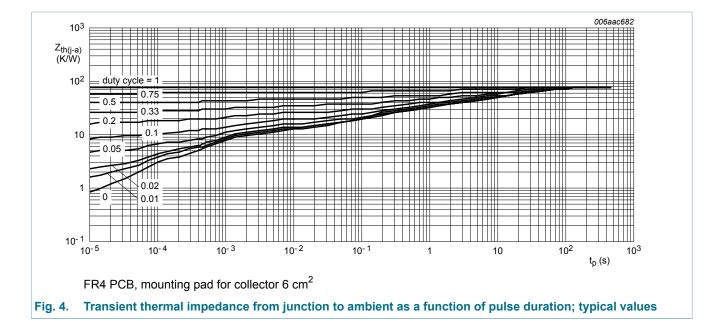
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### PBSS4240X

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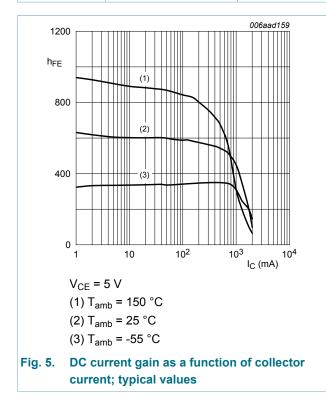
### 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB}$ = 40 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 40 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	50	μA
I <sub>CEO</sub>	collector-emitter cut-off current	$V_{CE}$ = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB}$ = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	300	-	-	
		$V_{CE}$ = 5 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	300	-	900	
		$V_{CE}$ = 5 V; I <sub>C</sub> = 1 A; T <sub>amb</sub> = 25 °C	200	-	-	
		$\label{eq:VCE} \begin{array}{l} V_{CE} \texttt{= 5 V; } I_{C} \texttt{= 2 A; pulsed; } t_{p} \texttt{\leq 300 } \mu s; \\ \delta \texttt{\leq 0.02 ; } T_{amb} \texttt{= 25 °C} \end{array}$	75	-	-	
V <sub>CEsat</sub>	collector-emitter	$I_{C}$ = 100 mA; $I_{B}$ = 1 mA; $T_{amb}$ = 25 °C	-	-	80	mV
	saturation voltage	$I_{C}$ = 500 mA; $I_{B}$ = 50 mA; $T_{amb}$ = 25 °C	-	-	140	mV
		$\begin{split} I_{C} &= 1 \text{ A};  I_{B} = 100 \text{ mA}; \text{ pulsed}; \\ t_{p} &\leq 300  \mu\text{s};  \delta \leq 0.02 ;  T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	260	mV
		$\begin{split} I_{C} &= 2 \text{ A};  I_{B} = 200 \text{ mA}; \text{ pulsed}; \\ t_{p} &\leq 300  \mu\text{s};  \delta \leq 0.02 ;  T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	510	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C}$ = 1 A; $I_{B}$ = 100 mA; pulsed; $t_{p} \le 300 \ \mu$ s; δ ≤ 0.02 ; $T_{amb}$ = 25 °C	-	-	260	mΩ

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C}$ = 1 A; $I_{B}$ = 100 mA; pulsed; $t_{p} \le 300 \ \mu$ s; δ $\le 0.02$ ; $T_{amb}$ = 25 °C	-	-	1.2	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$\label{eq:Vce} \begin{split} V_{CE} &= 5 \text{ V}; \text{ I}_{C} = 1 \text{ A}; \text{ pulsed};  \text{t}_{p} \leq 300  \mu\text{s}; \\ \delta \leq 0.02 ;   \text{T}_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	1.1	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = 10 V; I <sub>C</sub> = 50 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	150	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	10	pF



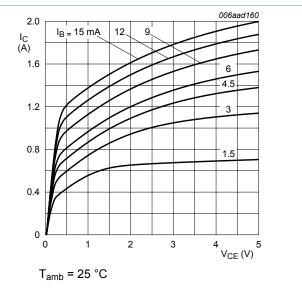
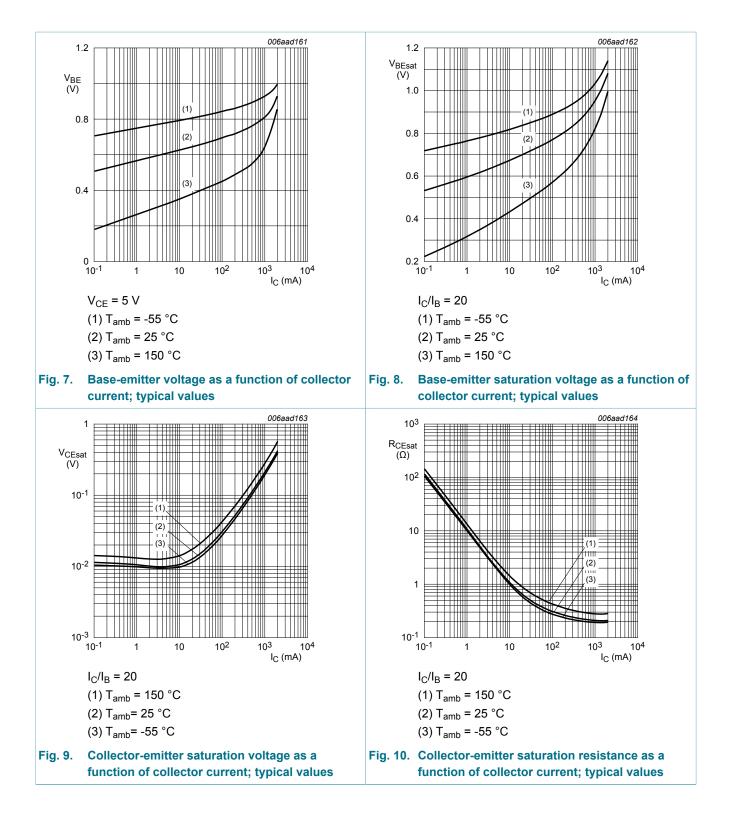


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

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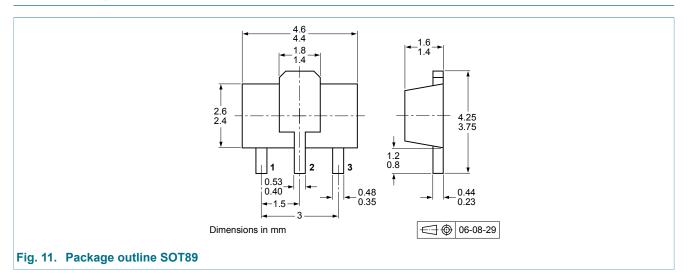


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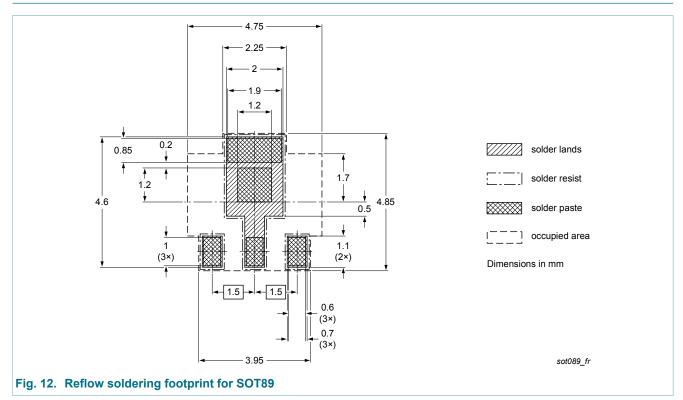
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### 8. Package outline



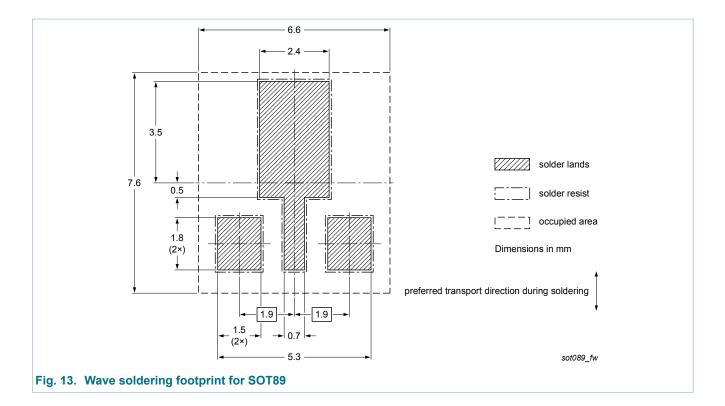
### 9. Soldering



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### 10. Revision history

Table 8.	<b>Revision his</b>	story	
Data sheet	t ID	Release date	Data sheet status

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4240X v.1	20121015	Product data sheet	-	-

#### 40 V, 2 A NPN low VCEsat (BISS) transistor

#### 11. Legal information

#### 11.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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