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Team Nexperia

NPN 500 mA, 50 V resistor-equipped transistors; **R1 = 2.2 k**Ω, **R2 = open** 

Rev. 03 — 16 November 2009

**Product data sheet** 

#### **Product profile** 1.

### **1.1 General description**

500 mA NPN Resistor-Equipped Transistors (RET) family.

| Table 1. | Product | overview |
|----------|---------|----------|
|          | 1100000 | 01011010 |

| Type number              | Package |        |          | PNP complement |
|--------------------------|---------|--------|----------|----------------|
|                          | NXP     | JEITA  | JEDEC    |                |
| PDTD123TK                | SOT346  | SC-59A | TO-236   | PDTB123TK      |
| PDTD123TS <sup>[1]</sup> | SOT54   | SC-43A | TO-92    | PDTB123TS      |
| PDTD123TT                | SOT23   | -      | TO-236AB | PDTB123TT      |

[1] Also available in SOT54A and SOT54 variant packages (see Section 2).

### 1.2 Features

- Built-in bias resistors
- Simplifies circuit design
- 500 mA output current capability

### 1.3 Applications

- Digital application in automotive and industrial segments
- Controlling IC inputs

### 1.4 Quick reference data

### Reduces component count

- Reduces pick and place costs
- Cost saving alternative for BC817 series in digital applications
- Switching loads

#### Table 2. Quick reference data

| Symbol    | Parameter                 | Conditions | Min  | Тур | Max  | Unit |
|-----------|---------------------------|------------|------|-----|------|------|
| $V_{CEO}$ | collector-emitter voltage | open base  | -    | -   | 50   | V    |
| lo        | output current            |            | -    | -   | 500  | mA   |
| R1        | bias resistor 1 (input)   |            | 1.54 | 2.2 | 2.86 | kΩ   |



### NPN 500 mA resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

### 2. Pinning information

| Table 3. | Pinning            |                           |                     |
|----------|--------------------|---------------------------|---------------------|
| Pin      | Description        | Simplified outline        | Symbol              |
| SOT54    |                    |                           |                     |
| 1        | input (base)       |                           |                     |
| 2        | output (collector) |                           | 2                   |
| 3        | GND (emitter)      |                           | 1 R1 3<br>006aaa218 |
| SOT54A   |                    |                           |                     |
| 1        | input (base)       |                           |                     |
| 2        | output (collector) |                           | 2                   |
| 3        | GND (emitter)      | 001aab348                 | 1 R1 3<br>006aaa218 |
| SOT54 va | riant              |                           |                     |
| 1        | input (base)       |                           |                     |
| 2        | output (collector) |                           | 2                   |
| 3        | GND (emitter)      | 1<br>Cm<br>n<br>001aab447 | 1 R1 3<br>006aaa218 |
| SOT23, S | OT346              |                           |                     |
| 1        | input (base)       |                           |                     |
| 2        | GND (emitter)      |                           | 3                   |
| 3        | output (collector) | [1] [2]<br>006aaa144      | 1 R1 2<br>sym012    |

NPN 500 mA resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

### 3. Ordering information

| Type number              | Package | ackage                                                         |         |  |  |  |
|--------------------------|---------|----------------------------------------------------------------|---------|--|--|--|
|                          | Name    | Description                                                    | Version |  |  |  |
| PDTD123TK                | SC-59A  | plastic surface mounted package; 3 leads                       | SOT346  |  |  |  |
| PDTD123TS <sup>[1]</sup> | SC-43A  | plastic single-ended leaded (through hole) package;<br>3 leads | SOT54   |  |  |  |
| PDTD123TT                | -       | plastic surface mounted package; 3 leads                       | SOT23   |  |  |  |

[1] Also available in SOT54A and SOT54 variant packages (see <u>Section 2</u> and <u>Section 9</u>).

### 4. Marking

| Table 5.   Marking codes |                             |
|--------------------------|-----------------------------|
| Type number              | Marking code <sup>[1]</sup> |
| PDTD123TK                | E9                          |
| PDTD123TS                | TD123TS                     |
| PDTD123TT                | *1T                         |

[1] \* = -: made in Hong Kong

- \* = p: made in Hong Kong
- \* = t: made in Malaysia

\* = W: made in China

### 5. Limiting values

### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                 | Conditions                   | Min      | Max  | Unit |
|------------------|---------------------------|------------------------------|----------|------|------|
| V <sub>CBO</sub> | collector-base voltage    | open emitter                 | -        | 50   | V    |
| V <sub>CEO</sub> | collector-emitter voltage | open base                    | -        | 50   | V    |
| V <sub>EBO</sub> | emitter-base voltage      | open collector               | -        | 5    | V    |
| VI               | input voltage             |                              |          |      |      |
|                  | positive                  |                              | -        | +12  | V    |
|                  | negative                  |                              | -        | -5   | V    |
| lo               | output current            |                              | -        | 500  | mA   |
| P <sub>tot</sub> | total power dissipation   | $T_{amb} \le 25 \ ^{\circ}C$ | <u>1</u> |      |      |
|                  | SOT346                    |                              | -        | 250  | mW   |
|                  | SOT54                     |                              | -        | 500  | mW   |
|                  | SOT23                     |                              | -        | 250  | mW   |
| T <sub>stg</sub> | storage temperature       |                              | -65      | +150 | °C   |
| Tj               | junction temperature      |                              | -        | 150  | °C   |
| T <sub>amb</sub> | ambient temperature       |                              | -65      | +150 | °C   |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

NPN 500 mA resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

### 6. Thermal characteristics

| Table 7.             | Thermal characteristics                     | 6           |            |     |     |      |
|----------------------|---------------------------------------------|-------------|------------|-----|-----|------|
| Symbol               | Parameter                                   | Conditions  | Min        | Тур | Max | Unit |
| R <sub>th(j-a)</sub> | thermal resistance from junction to ambient | in free air | <u>[1]</u> |     |     |      |
|                      | SOT346                                      |             | -          | -   | 500 | K/W  |
|                      | SOT54                                       |             | -          | -   | 250 | K/W  |
|                      | SOT23                                       |             | -          | -   | 500 | K/W  |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 7. Characteristics

| <b>Table 8.</b> $T_{amb} = 25$ | <b>Characteristics</b><br><i>°C unless otherwise spec</i> | cified.                                                                                                    |      |     |      |      |
|--------------------------------|-----------------------------------------------------------|------------------------------------------------------------------------------------------------------------|------|-----|------|------|
| Symbol                         | Parameter                                                 | Conditions                                                                                                 | Min  | Тур | Max  | Unit |
| I <sub>CBO</sub>               | collector-base cut-off                                    | $V_{CB} = 40 \text{ V}; I_E = 0 \text{ A}$                                                                 | -    | -   | 100  | nA   |
|                                | current                                                   | $V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}$                                                                 | -    | -   | 100  | nA   |
| I <sub>CEO</sub>               | collector-emitter<br>cut-off current                      | $V_{CE} = 50 \text{ V}; \text{ I}_{B} = 0 \text{ A}$                                                       | -    | -   | 0.5  | μΑ   |
| I <sub>EBO</sub>               | emitter-base cut-off<br>current                           | $V_{EB} = 5 V; I_{C} = 0 A$                                                                                | -    | -   | 100  | nA   |
| h <sub>FE</sub>                | DC current gain                                           | $V_{CE} = 5 \text{ V}; I_{C} = 50 \text{ mA}$                                                              | 100  | 300 | -    |      |
| V <sub>CEsat</sub>             | collector-emitter<br>saturation voltage                   | $I_{\rm C}$ = 50 mA; $I_{\rm B}$ = 2.5 mA                                                                  | -    | -   | 0.3  | V    |
| R1                             | bias resistor 1 (input)                                   |                                                                                                            | 1.54 | 2.2 | 2.86 | kΩ   |
| C <sub>c</sub>                 | collector capacitance                                     | $\label{eq:VCB} \begin{array}{l} V_{CB} = 10 \; V; \; I_{E} = i_{e} = 0 \; A; \\ f = 1 \; MHz \end{array}$ | -    | 7   | -    | pF   |

### **NXP Semiconductors**

# **PDTD123T series**

NPN 500 mA resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open



### NPN 500 mA resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open

### 8. Package outline



NPN 500 mA resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open

### 9. Packing information

| Table 9.         Packing methods           The indicated -xxx are the last three digits of the 12NC ordering code.[1] |               |                                |         |                  |       |  |
|-----------------------------------------------------------------------------------------------------------------------|---------------|--------------------------------|---------|------------------|-------|--|
| Type number                                                                                                           | Package       | Description                    | Packing | Packing quantity |       |  |
|                                                                                                                       |               |                                | 3000    | 5000             | 10000 |  |
| PDTD123TK                                                                                                             | SOT346        | 4 mm pitch, 8 mm tape and reel | -115    | -                | -135  |  |
| PDTD123TS                                                                                                             | SOT54         | bulk, straight leads           | -       | -412             | -     |  |
|                                                                                                                       | SOT54A        | tape and reel, wide pitch      | -       | -                | -116  |  |
|                                                                                                                       |               | tape ammopack, wide pitch      | -       | -                | -126  |  |
|                                                                                                                       | SOT54 variant | bulk, delta pinning            | -       | -112             | -     |  |
| PDTD123TT                                                                                                             | SOT23         | 4 mm pitch, 8 mm tape and reel | -215    | -                | -235  |  |

[1] For further information and the availability of packing methods, see <u>Section 12</u>.

### NPN 500 mA resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open

### **10. Revision history**

| Table 10. Revision his | tory                                                            |                                                            |                                              |                                                 |
|------------------------|-----------------------------------------------------------------|------------------------------------------------------------|----------------------------------------------|-------------------------------------------------|
| Document ID            | Release date                                                    | Data sheet status                                          | Change notice                                | Supersedes                                      |
| PDTD123T_SER_3         | 20091116                                                        | Product data sheet                                         | -                                            | PDTD123T_SER_2                                  |
| Modifications:         | <ul> <li>This data she<br/>including new<br/>content</li> </ul> | eet was changed to reflect<br>v legal definitions and disc | the new company nam<br>laimers. No changes w | e NXP Semiconductors, ere made to the technical |
| PDTD123T_SER_2         | 20050721                                                        | Product data sheet                                         | -                                            | PDTD123T_SER_1                                  |
| PDTD123T_SER_1         | 20050603                                                        | Product data sheet                                         | -                                            | -                                               |

### NPN 500 mA resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

### 11. Legal information

### 11.1 Data sheet status

| Document status[1][2]          | Product status <sup>[3]</sup> | Definition                                                                            |
|--------------------------------|-------------------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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NPN 500 mA resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open

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