

ESD protection diode in SOD523 package

29 November 2018

**Product data sheet** 

### 1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

### 2. Features and benefits

- Unidirectional ESD protection of one line
- Low clamping voltage: V<sub>CL</sub> = 20 V at I<sub>PPM</sub> = 18 A
- ESD protection > 30 kV
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 18 A at t<sub>p</sub> = 8/20 μs

### 3. Application information

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines
- CAN bus protection

### 4. Quick reference data

#### Table 1. Quick reference data

| Symbol           | Parameter                   | Conditions  | Min | Тур | Мах | Unit |
|------------------|-----------------------------|---|-----|-----|-----|------|
| V <sub>RWM</sub> | reverse standoff<br>voltage | T <sub>amb</sub> = 25 °C                                  | -   | -   | 3.3 | V    |
| C <sub>d</sub>   | diode capacitance           | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C | -   | 207 | 300 | pF   |



### 5. Pinning information

| Table 2. | Pinning inf | formation   |                    |                |
|----------|-------------|-------------|--------------------|----------------|
| Pin      | Symbol      | Description | Simplified outline | Graphic symbol |
| 1        | К           | cathode[1]  |                    | 1 🛃 2          |
| 2        | A           | anode       | 1                  | sym035         |
|          |             |             | SOD523             |                |

[1] The marking bar indicates pin 1.

### 6. Ordering information

#### Table 3. Ordering information

| Type number | Package |  |         |  |  |
|-------------|---------|--|---------|--|--|
|             | Name    | Description  | Version |  |  |
| PESD3V3S1UB |         | plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body | SOD523  |  |  |

### 7. Marking

#### Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PESD3V3S1UB | N1           |

### 8. Limiting values

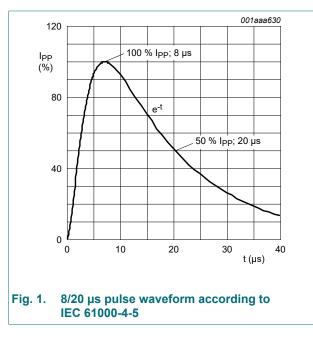
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                | Conditions                        |     | Min | Max | Unit |
|------------------|--------------------------|-----------------------------------|-----|-----|-----|------|
| P <sub>PPM</sub> | rated peak pulse power   | t <sub>p</sub> = 8/20 μs          | [1] | -   | 330 | W    |
| I <sub>PPM</sub> | rated peak pulse current | t <sub>p</sub> = 8/20 μs          | [1] | -   | 18  | А    |
| Tj               | junction temperature     |                                   |     | -   | 150 | °C   |
| T <sub>amb</sub> | ambient temperature      |                                   |     | -55 | 150 | °C   |
| T <sub>stg</sub> | storage temperature      |                                   |     | -65 | 150 | °C   |
| ESD maximun      | n ratings                |                                   |     |     |     |      |
| V <sub>ESD</sub> | electrostatic discharge  | IEC 61000-4-2 (contact discharge) | [2] | -   | 30  | kV   |
|                  | voltage                  | HBM MIL-STD883                    |     | -   | 10  | kV   |

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.

[2] Device stressed with ten non-repetitive ESD pulses.



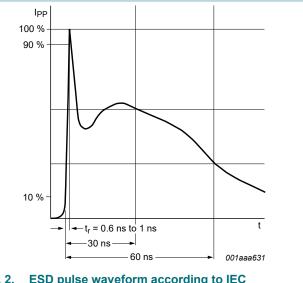
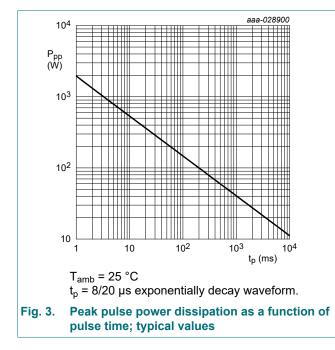


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

| Symbol           | Parameter                   | Conditions  |     | Min | Тур | Max | Unit |
|------------------|-----------------------------|---|-----|-----|-----|-----|------|
| V <sub>RWM</sub> | reverse standoff<br>voltage | T <sub>amb</sub> = 25 °C                                  |     | -   | -   | 3.3 | V    |
| V <sub>BR</sub>  | breakdown voltage           | I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C           |     | 5.2 | 5.6 | 6   | V    |
| I <sub>RM</sub>  | reverse leakage current     | V <sub>RWM</sub> = 3.3 V; T <sub>amb</sub> = 25 °C        |     | -   | 0.7 | 2   | μA   |
| C <sub>d</sub>   | diode capacitance           | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C |     | -   | 207 | 300 | pF   |
| V <sub>CL</sub>  | clamping voltage            | I <sub>PPM</sub> = 1 A; T <sub>amb</sub> = 25 °C          | [1] | -   | -   | 7   | V    |
|                  |                             | I <sub>PPM</sub> = 18 A; T <sub>amb</sub> = 25 °C         | [1] | -   | -   | 20  | V    |
| r <sub>dif</sub> | differential resistance     | I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C           |     | -   | -   | 400 | Ω    |

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.



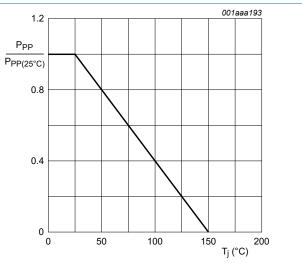
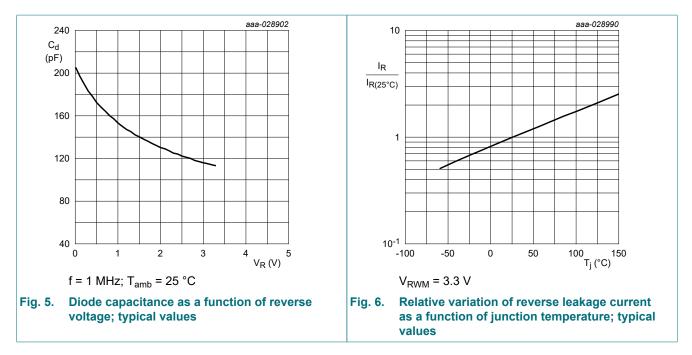
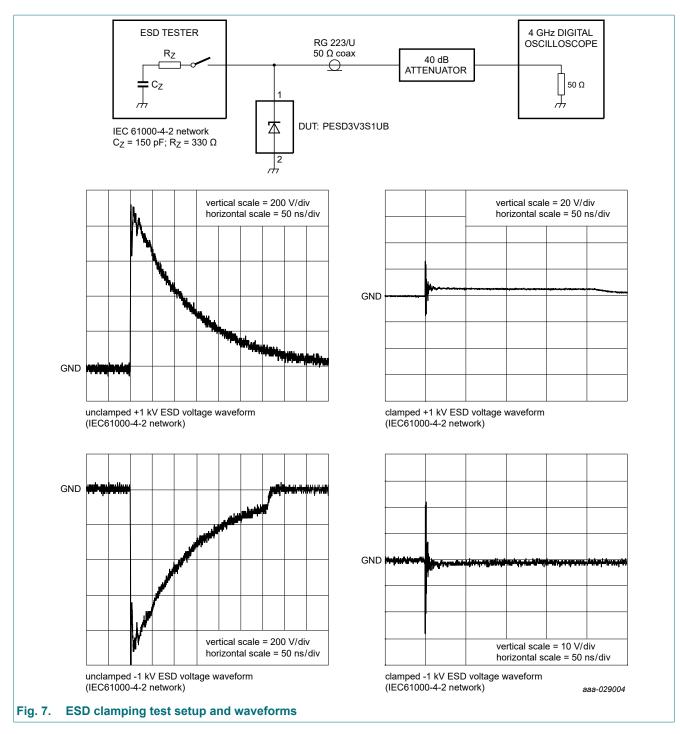


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

#### ESD protection diode in SOD523 package



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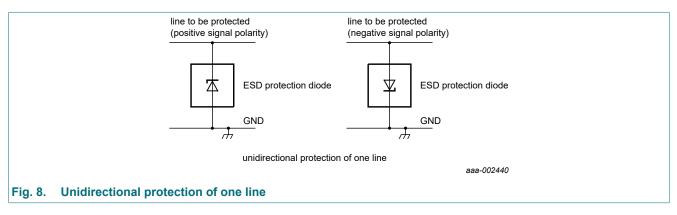


PESD3V3S1UB

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### **10.** Application information

The device is designed for unidirectional protection of one single data line from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarity is above or below ground. It provides a surge capability of up to 330 W per line for a 8/20  $\mu$ s waveform.

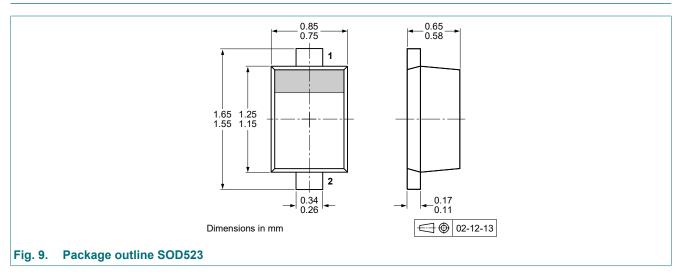


#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

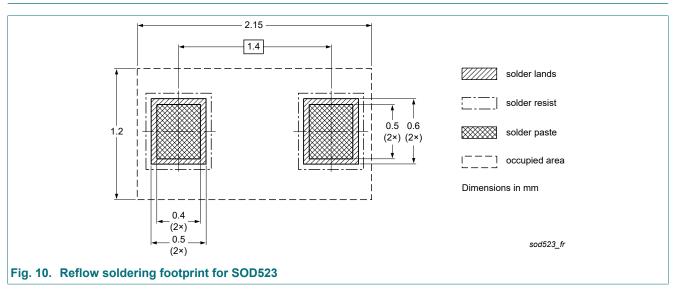
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### 11. Package outline



#### ESD protection diode in SOD523 package

### 12. Soldering



### 13. Revision history

| Table 7. Revision history |   |                    |               |                    |
|---------------------------|---|--------------------|---------------|--------------------|
| Data sheet ID             | Release date  | Data sheet status  | Change notice | Supersedes         |
| PESD3V3S1UB v.1           | 20181129  | Product data sheet | -             | PESDXS1UB_SERIES_2 |
| Modifications:            | <ul> <li>Nexperia.</li> <li>Legal texts have</li> <li>Soldering section</li> <li>Application inform</li> <li>Figure 9: updated</li> </ul> | nation: updated.   |               |                    |
| PESDXS1UB_SERIES_2        | 20090824  | Product data sheet | -             | PESDXS1UB_SERIES_1 |
| PESDXS1UB_SERIES_1        | 20040614  | Product data sheet | -             | -                  |

### 14. Legal information

#### **Data sheet status**

| Document status<br>[1][2]         | Product<br>status [3] | Definition  |
|-----------------------------------|-----------------------|---|
| Objective [short]<br>data sheet   | Development           | This document contains data from<br>the objective specification for<br>product development. |
| Preliminary [short]<br>data sheet | Qualification         | This document contains data from the preliminary specification.                             |
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