

# **PESD1IVN-U**

# In-vehicle network ESD protection diode

15 July 2015

**Product data sheet** 

## 1. General description

ElectroStatic Discharge (ESD) protection diode in a small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package designed to protect one automotive in-vehicle network line from the damage caused by ESD and other transients.

### 2. Features and benefits

- One small SOT323 package to protect one in-vehicle network line
- Low clamping voltage: V<sub>CL</sub> = 38 V at I<sub>PP</sub> = 1 A
- ESD protection up to 18 kV; IEC 61000-4-2, level 4
- IEC 61000-4-5 (surge);  $I_{PP}$  = 3 A at  $t_p$  = 8/20  $\mu$ s
- AEC-Q101 qualified

### 3. Applications

- In-vehicle network ESD protection for CAN, LIN, FlexRay and Single Edge Nibble Transmission (SENT) interfaces
- Generic automotive applications

### 4. Quick reference data

Table 1. Quick reference data

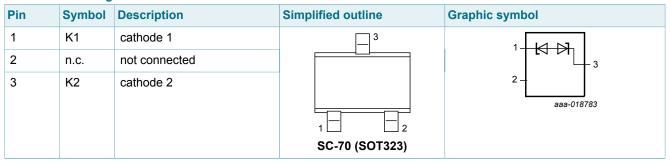
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	26.5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	8.5	11	pF



In-vehicle network ESD protection diode

# 5. Pinning information

Table 2. Pinning information



# 6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PESD1IVN-U	SC-70	plastic surface-mounted package; 3 leads	SOT323		

# 7. Marking

Table 4. Marking codes

Type number	Marking code
	[1]
PESD1IVN-U	3X%

[1] % = placeholder for manufacturing site code

In-vehicle network ESD protection diode

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>PPM</sub>	rated peak pulse power	t <sub>p</sub> = 8/20 μs	[1][2]	-	150	W
I <sub>PPM</sub>	rated peak pulse current		[1][2]	-	3	Α
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximun	n ratings				•	
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2][3]	-	18	kV
		MIL-STD-883 (human body model)	[2][3]	-	10	kV

<sup>[1]</sup> Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

- [2] Measured from pin 1 to 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

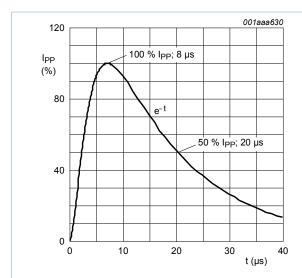


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5 and IEC 61643-321

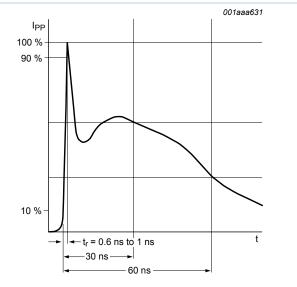
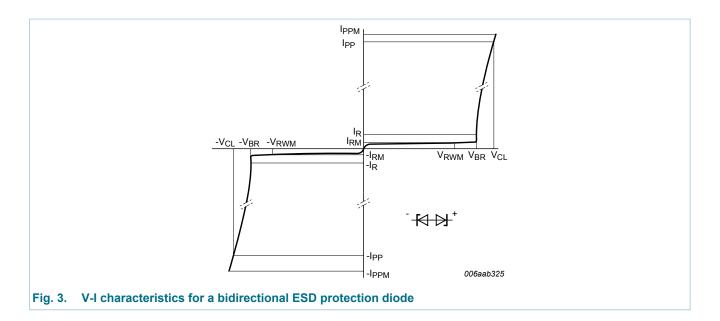


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### In-vehicle network ESD protection diode



### 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	26.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 26.5 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		28	30	32	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	8.5	11	pF
		$f = 1 \text{ MHz}; V_R = 2.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	6.6	-	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1][2]	-	-	38	V
		I <sub>PPM</sub> = 3 A; T <sub>amb</sub> = 25 °C	[1][2]	-	-	53	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 20 A; T <sub>amb</sub> = 25 °C	[3]	-	2	-	Ω

Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

<sup>[2]</sup> Measured from pin 1 to 3.

<sup>3</sup> Non-repetitive current pulse, Transmission line Pulse (TLP), square pulse, ANSI/ESD STM5.5.1-2008.

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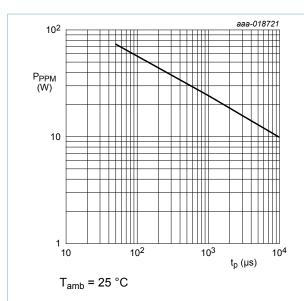


Fig. 4. Rated peak pulse power as a function of square pulse duration; typical values

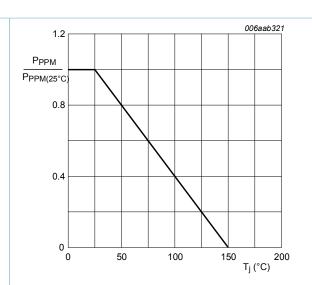
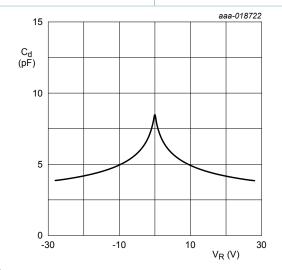


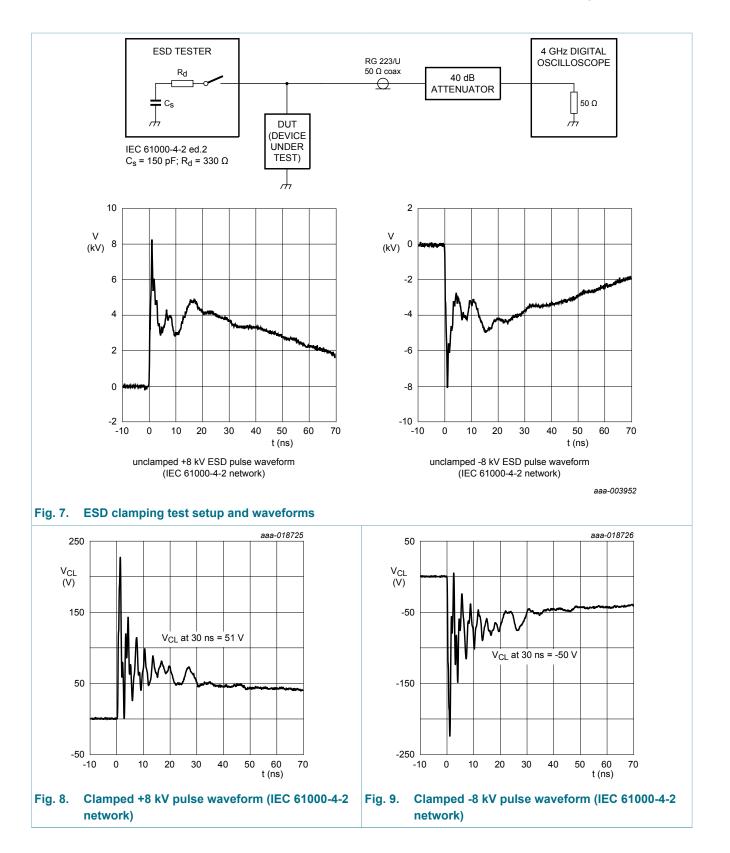
Fig. 5. Relative variation of rated peak pulse power as a function of junction temperature; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$ 

Fig. 6. Diode capacitance as a function of reverse voltage; typical values

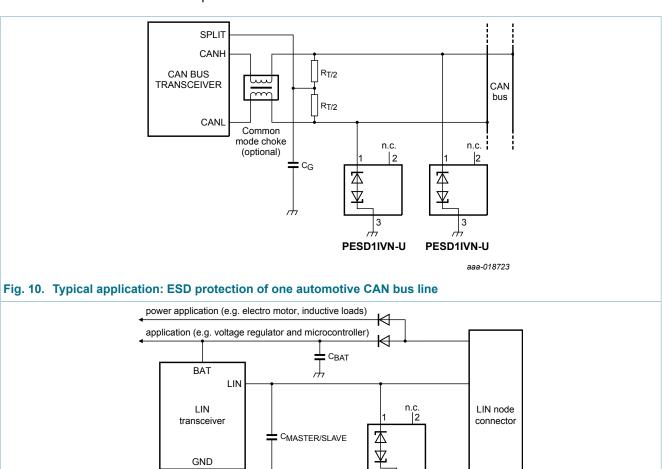
#### In-vehicle network ESD protection diode



In-vehicle network ESD protection diode

### 10. Application information

The device is designed for the protection of one automotive in-vehicle network bus line from surge pulses and ESD damage. The device provides a surge capability of up to 3 A for an  $8/20~\mu s$  waveform.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

PESD1IVN-U

aaa-018724

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.

Fig. 11. Typical application: ESD protection of one automotive LIN bus line

- 4. Avoid running protected conductors in parallel with unprotected conductors.
- Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.

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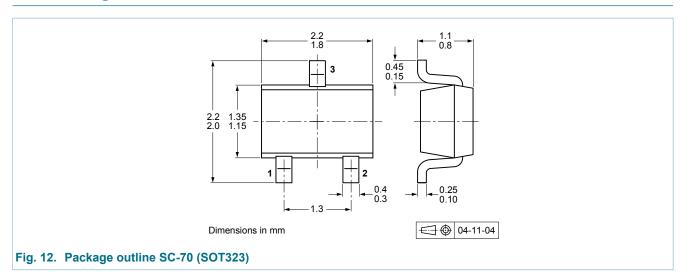
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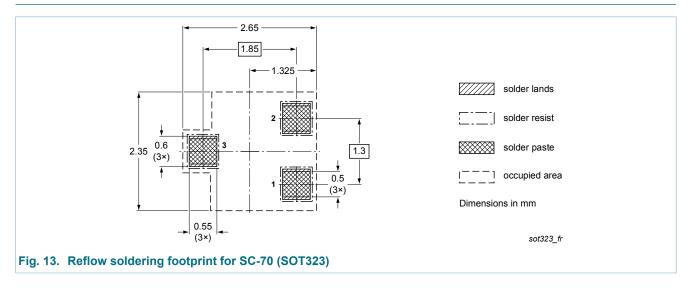
In-vehicle network ESD protection diode

- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

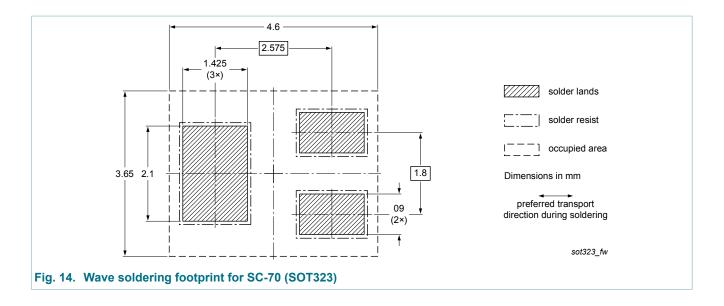
## 11. Package outline



### 12. Soldering



### In-vehicle network ESD protection diode



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# 13. Revision history

### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD1IVN-U v.1	20150715	Product data sheet	-	-

#### In-vehicle network ESD protection diode

### 14. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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### In-vehicle network ESD protection diode

### 15. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	
7	Marking	2
8	Limiting values	3
9	Characteristics	
10	Application information	7
11	Package outline	8
12	Soldering	8
13	Revision history	10
14	Legal information	11
14.1	Data sheet status	
14.2	Definitions	11
14.3	Disclaimers	11
14.4	Trademarks	

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