

ESD protection for In-vehicle networks

Product data sheet

1. General description

ESD protection device in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package designed to protect two automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 24 V
- Low clamping voltage: V_{CL} = 33 V at I_{PP} = 3.5 A
- Typ. diode capacitance matching: $\Delta C_d/C_d = 0.1 \%$ •
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605; C = 330 pF, R = 330 Ω)
- ISO 7637-3: Pulse a: V_S = -150 V / Pulse b: V_S= +100 V •
- Ultra low leakage current: I_{RM} < 1 nA
- Qualified according to AEC-Q101 / Automotive grade

3. Applications

ESD protection for In-vehicle network lines in automotive enviroments

- CAN
- LIN •
- FlexRay
- SENT

4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	-	3.5	A
V _{CL}	clamping voltage	I_{PPM} = 3.5 A; t_p = 8/20 $\mu s;$ T_{amb} = 25 $^\circ C$	[<u>3] [2]</u>	-	33	42	V

According to IEC 61000-4-5. [1]

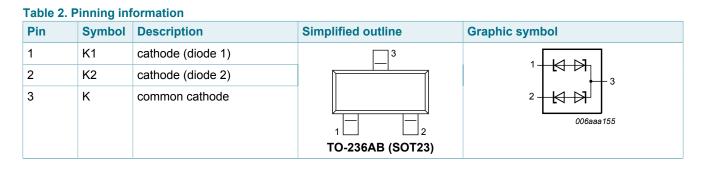
[2] Measured from pin 1 or 2 to pin 3.

[3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

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5. Pinning information



6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PESD2IVN24-T	TO-236AB	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

7. Marking

Table 4.	Marking	codes
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Type number	Marking code[1]
PESD2IVN24-T	BV%

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

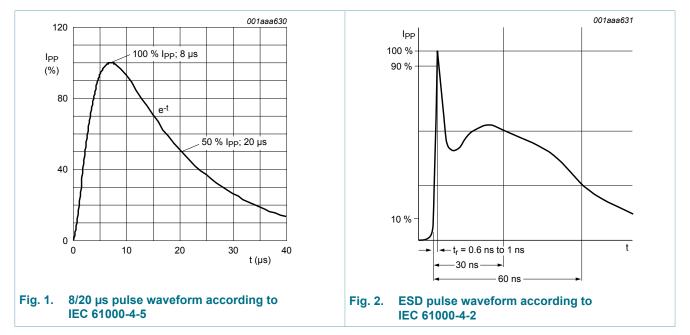
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	3.5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum	ratings					
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
		ISO 10605; contact discharge; C = 330 pF, R = 330 Ω	[2] [3]	-	30	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 Ω	[2] [3]	-	30	kV

[1] According to IEC 61000-4-5.

[2] Measured from pin 1 or 2 to pin 3.

[3] Device stressed with ten non-repetitive ESD pulses.



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9. Characteristics

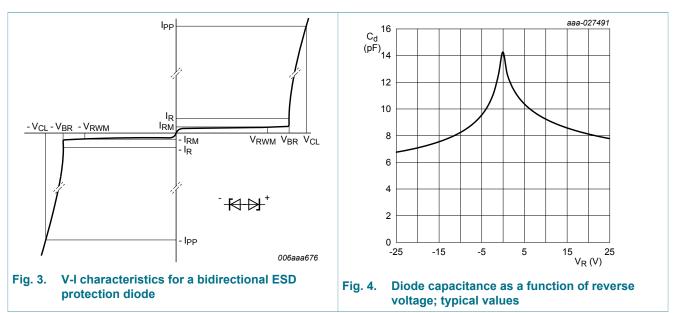
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
V _{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	25.5	30.5	35.5	V
I _{RM}	reverse leakage current	V _{RWM} = 24 V; T _{amb} = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C	[1]	-	14	17	pF
$\Delta C_d/C_d$	diode capacitance matching		[2]	-	0.1	-	%
		f = 1 MHz; V_R = 2.5 V; T_{amb} = 25 °C	[2]	-	0.1	-	%
V _{CL}	clamping voltage	I _{PPM} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[3] [1]	-	31	40	V
		I_{PPM} = 3.5 A; t_p = 8/20 µs; T_{amb} = 25 °C	[3] [1]	-	33	42	V
		I_{PP} = 16 A; t_p = TLP; T_{amb} = 25 °C	[4] [1]	-	32	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[4] [1]	-	0.2	-	Ω

[1] Measured from pin 1 or 2 to pin 3.

[2] ΔC_d is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.

[3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

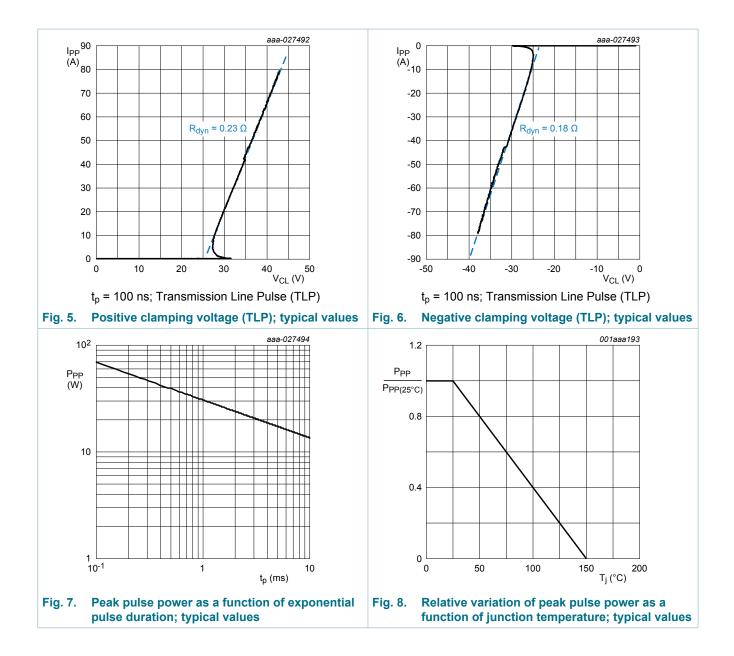
[4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008



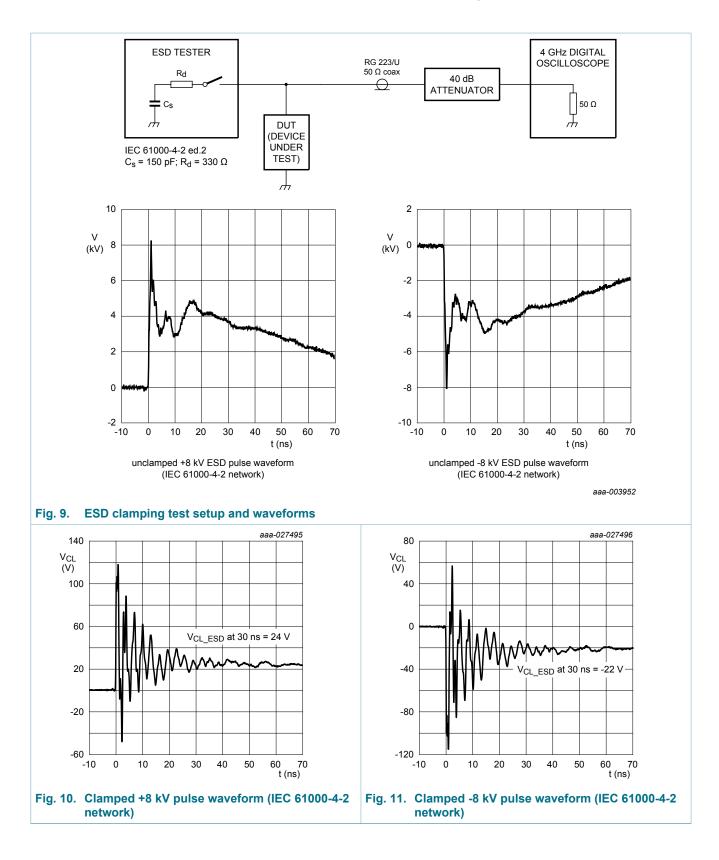
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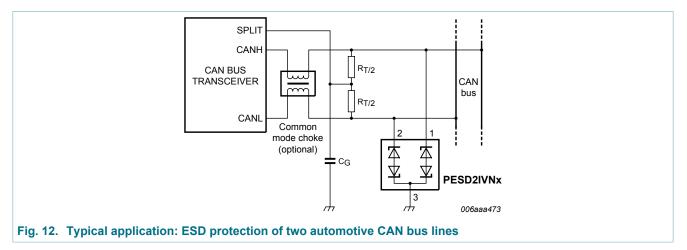
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10. Application information

The PESD2IVN24-T is designed for the protection of two automotive IVN bus line from the damage caused by ESD and surge pulses.



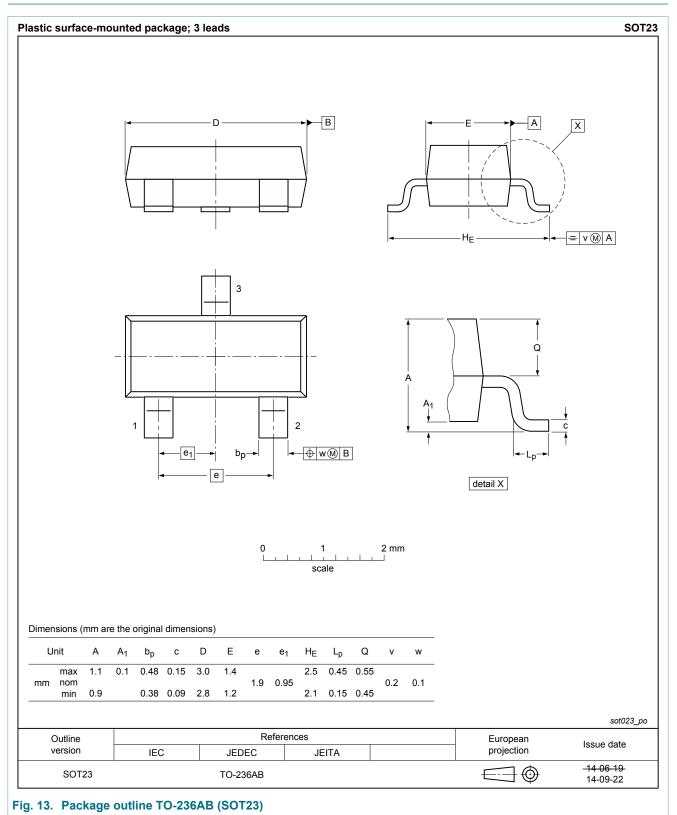
Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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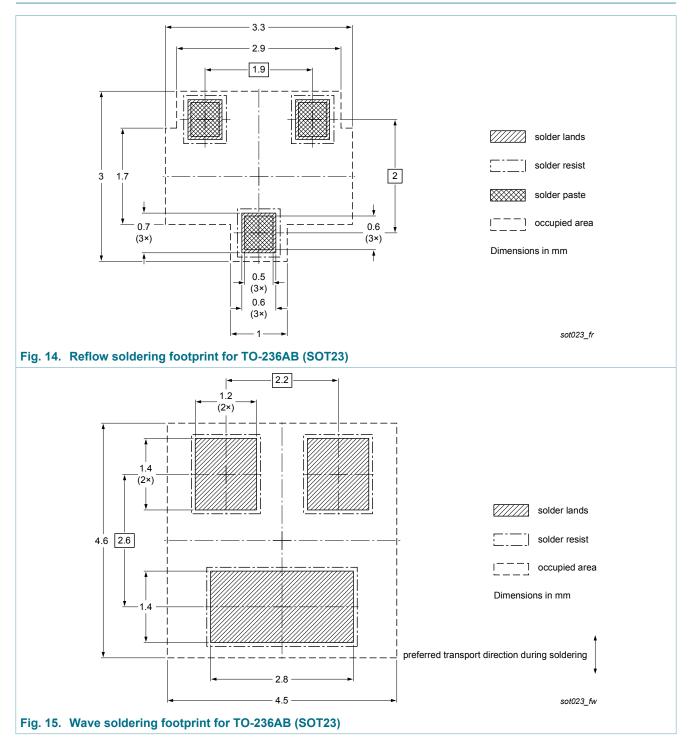
11. Package outline



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12. Soldering



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13. Revision history

Table 7. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD2IVN24-T v.2	20180201	Product data sheet	-	PESD2IVN24-T v.1			
Modifications:	Marking code: corrected						
PESD2IVN24-T v.1	20171012	Product data sheet	-	-			

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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