### 1. General description

Ultra low clamping bidirectional ElectroStatic Discharge (ESD) protection diode, part of the TrEOS protection family . The device is housed in a DSN0603-2 (SOD962-2) leadless ultra small Surface-Mounted Device (SMD) package designed to protect one signal line from the damage caused by ESD and other transients.

### 2. Features and benefits

- · Bidirectional ESD protection of one line
- Surge robustness I<sub>PPM</sub> = 18 A according to IEC 61000-4-5
- Low diode capacitance C<sub>d</sub> = 5 pF
- · Ultra low clamping to protect sensitive I/Os
- · Extremely low-inductance protection path to ground
- ESD protection up to ± 30 kV according to IEC 61000-4-2
- · Ultra small SMD package

# 3. Applications

- Cellular handsets and accessories
- Portable electronics
- Communication systems
- · Computers and peripherals

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage		-	-	3.3	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	5	pF



# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		K1   K2
2	K2	cathode (diode 2)		sym045
			Transparent top view	
			DSN0603-2 (SOD962-2)	

# 6. Ordering information

### **Table 3. Ordering information**

Type number Package					
	Name	Description	Version		
PESD3V3MS-SF		silicon, leadless ultra small package; 2 terminals; 0.4 mm pitch; 0.6 mm x 0.3 mm x 0.3 mm body	SOD962-2		

# 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD3V3MS-SF	g

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{RWM}$	reverse standoff voltage			-	3.3	V
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	18	Α
T <sub>amb</sub>	ambient temperature			-40	125	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings				•	
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[3]	-30	30	kV
	voltage	IEC 61000-4-2; air discharge	[3]	-30	30	kV

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] In positive and negative direction.
- [3] Device stressed with ten non-repetitive ESD pulses.

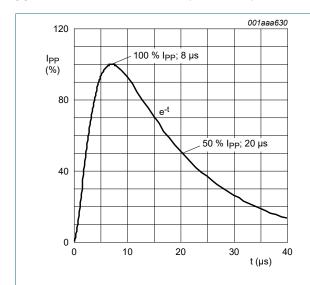


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5 and IEC 61643-321

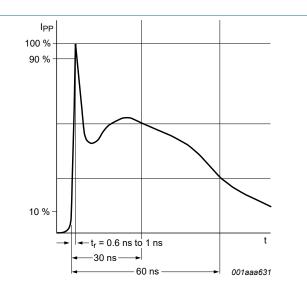


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

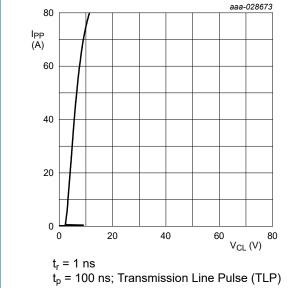
## 9. Characteristics

#### **Table 6. Characteristics**

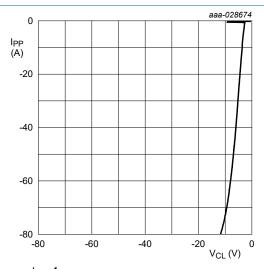
 $t_{amb}$  = 25°C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 1 mA		-	7	-	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 3.3 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	=	5	pF
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 18 A	[1]	-	5	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A	[2]	-	0.11	-	Ω
		I <sub>R</sub> = -10 A	[2]	-	0.11	-	Ω

- Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.







 $t_r = 1 \text{ ns}$ t<sub>p</sub> = 100 ns; Transmission Line Pulse (TLP)

Fig. 4. Dynamic resistance with negative clamping; typical values

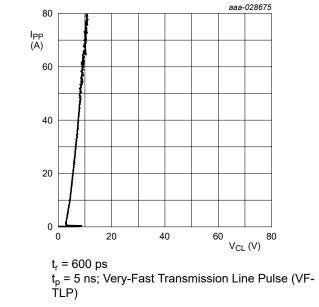


Fig. 5. Dynamic resistance with positive clamping; typical values

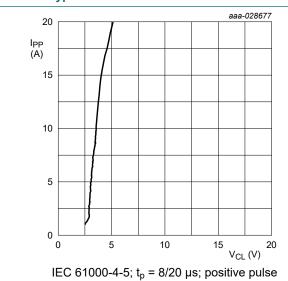


Fig. 7. Dynamic resistance with positive clamping; typical values

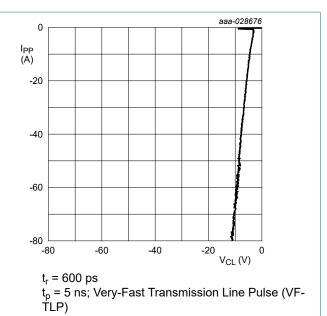
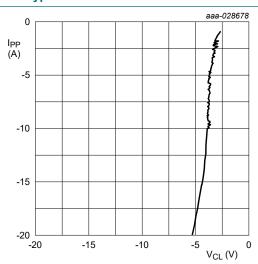


Fig. 6. Dynamic resistance with negative clamping; typical values

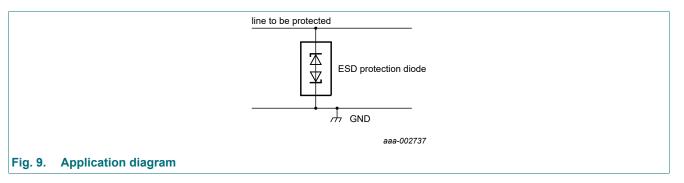


IEC 61000-4-5;  $t_p = 8/20 \mu s$ ; negative pulse

## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

The device uses an advanced clamping structure showing a negative dynamic resistance. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device in snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

# 11. Package outline

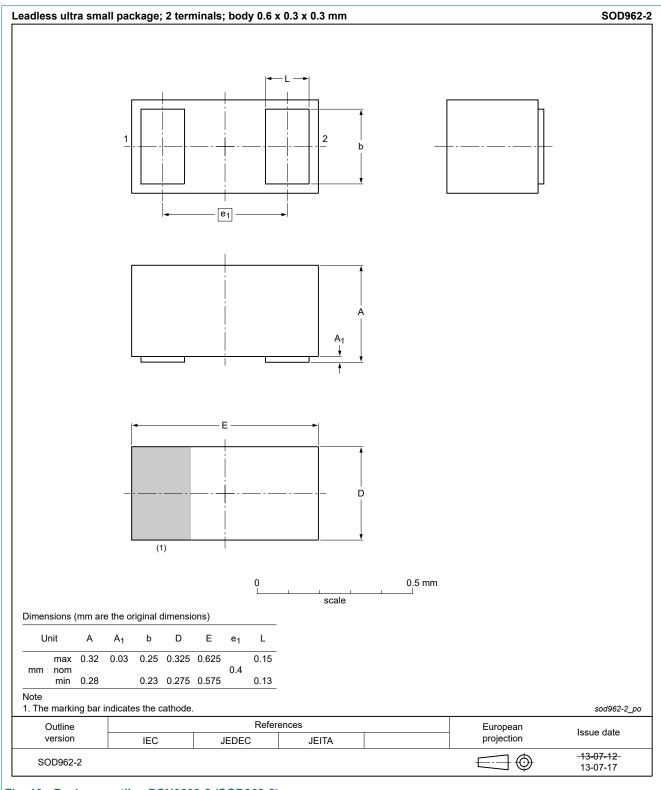


Fig. 10. Package outline DSN0603-2 (SOD962-2)

# 12. Soldering

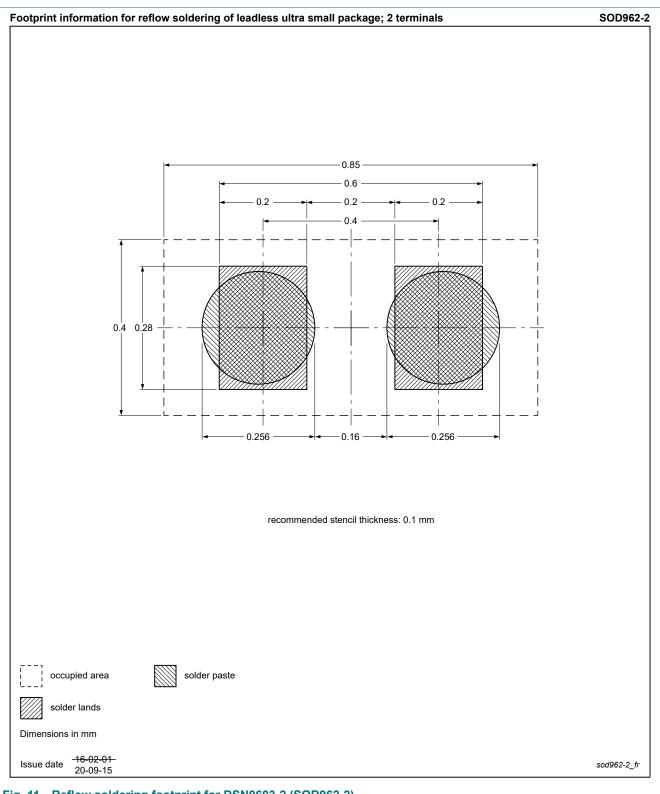


Fig. 11. Reflow soldering footprint for DSN0603-2 (SOD962-2)

# 13. Revision history

### Table 7. Revision history

able 1. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD3V3MS-SF v.3	20210303	Product data sheet	-	PESD3V3MS-SF v.2			
Modifications:	Figure "Reflow solde	Figure "Reflow soldering footprint" updated					
PESD3V3MS-SF v.2	20200427	Product data sheet	-	PESD3V3MS-SF v.1			
PESD3V3MS-SF v.1	20181129	Product data sheet	-	-			

### 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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