# 1. General description

4-fold unidirectional ElectroStatic Discharge (ESD) protection array designed to protect up to four lines from the damage caused by ESD and other transients.

The device is housed in a leadless extremely thin small DFN1308-6 (SOT8006B) Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Unidirectional ESD protection of up to 4 lines
- Very high surge robustness; I<sub>PP</sub> = 14.2 A (average measured) for 8/20 μs pulse
- Very low clamping voltage: V<sub>CL</sub> = 3.7 V typ. for 11 A 8/20 μs pulse
- · ESD protection up to 25 kV
- Very low dynamic resistance  $R_{dyn}$  = 0.15  $\Omega$  (TLP)

# 3. Applications

ESD protection for SD-card in portable communication, consumer devices and computing devices.

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	3.3	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s; T_{amb} = 25 \ ^{\circ}C$	[1]	-	-	11	А
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 11 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	3.7	4.5	V

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.



# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	1 2 3	K1 - F4 - K4
2	CA	common anode		CA CA
3	K2	cathode (diode 2)	6 5 4	
4	K3	cathode (diode 3)	DFN1308-6 (SOT8006B)	K2 K3
5	CA	common anode		006aaa156
6	K4	cathode (diode 4)		

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package						
	Name	Description	Version				
PESD3V3X4UHM		plastic, leadless extremely thin small package; 6 terminals; body 1.3 x 0.8 x 0.38 mm	SOT8006B				

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PESD3V3X4UHM	X4

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1]	-	11	А
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximum	ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[2]	-	25	kV
	voltage	IEC 61000-4-2 (air discharge)	[2]	-	25	kV

<sup>1]</sup> Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.

<sup>[2]</sup> Device stressed with ten non-repetitive ESD pulses.

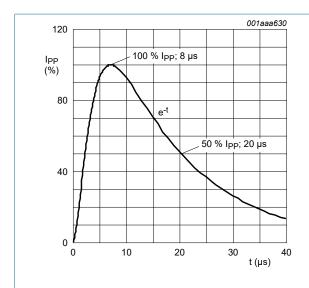


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

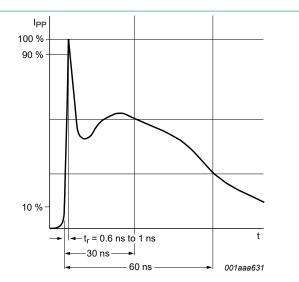


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	3.3	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C		5	6.2	7	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 3.3 V; T <sub>amb</sub> = 25 °C		-	1	100	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	0.8	0.9	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1]	-	2	-	V
		$I_{PPM}$ = 11 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	3.7	4.5	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = 100 ns; T <sub>amb</sub> = 25 °C	[2]	-	3.8	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2]	-	0.15	-	Ω
V <sub>t1</sub>	trigger voltage	T <sub>amb</sub> = 25 °C		-	8.1	-	V

<sup>[1]</sup> Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

<sup>[2]</sup> Non-repetitive current pulse, Transmission Line Pulse (TLP)  $t_p = 100$  ns; square pulse; ANSI/ESD STM5.5.1-2008.

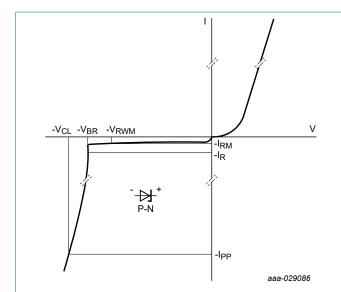


Fig. 3. V-I characteristics

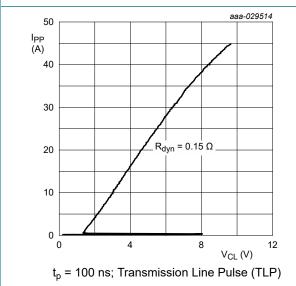


Fig. 5. Positive clamping voltage (TLP); typical values

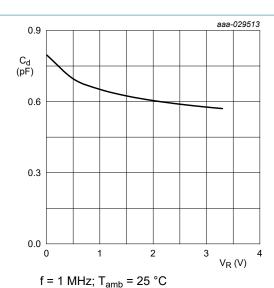


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

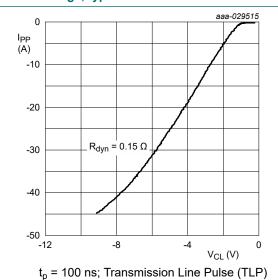
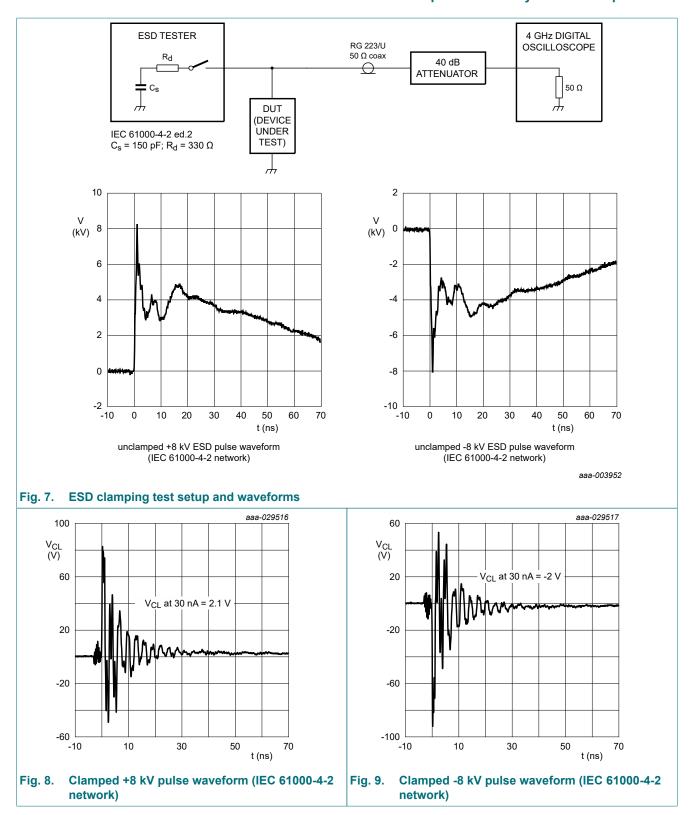
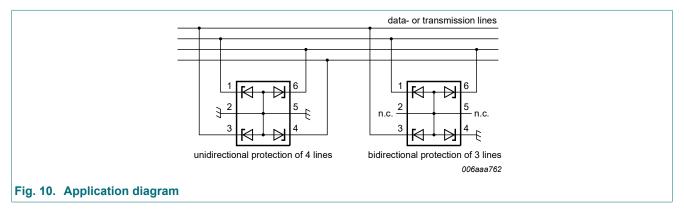


Fig. 6. Negative clamping voltage (TLP); typical values



# 10. Application information

The device is designed for protection of up to 4 unidirectional data lines from the damage caused by ESD and surge pulses. The device is suitable on lines where the signal polarities are above or below ground.

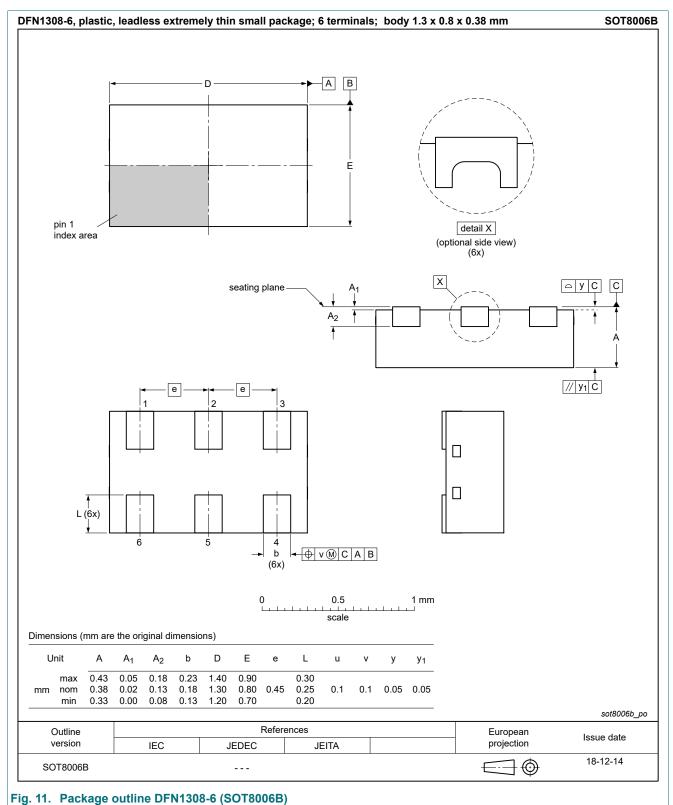


### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

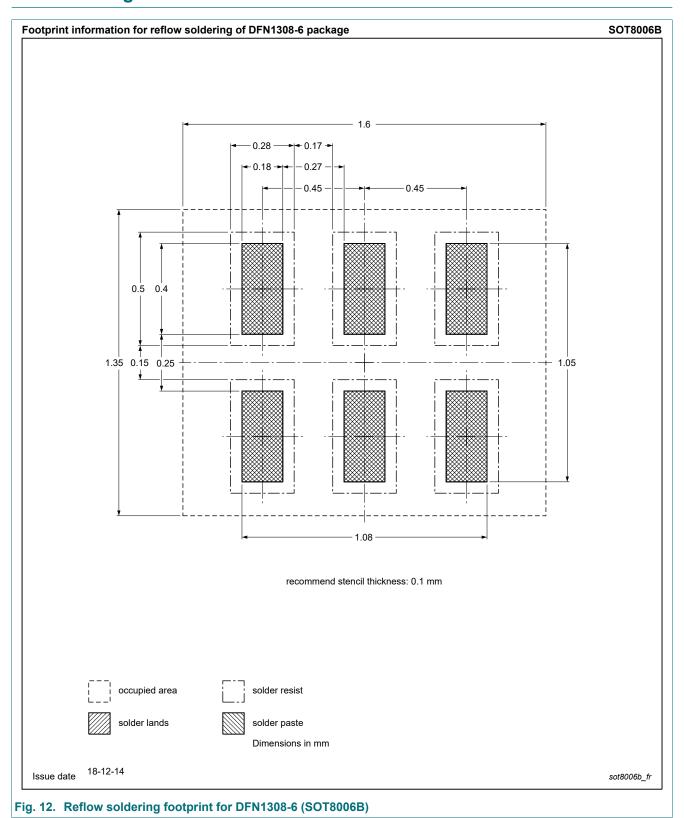
- Place the device as close to the input terminal or connector as possible
- · Minimize the path length between the device and the protected line.
- · Keep parallel signal paths to a minimum.
- · Avoid running protected conductors in parallel with unprotected conductors.
- Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- · Minimize the length of the transient return path to ground.
- · Avoid using shared transient return paths to a common ground point.
- · Use ground planes whenever possible. For multilayer PCBs, use ground vias.

# 11. Package outline



Tig. 11. Tackage outline bi 141300-0 (0010000b

# 12. Soldering



# 13. Revision history

## Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD3V3X4UHM v.1	20190212	Product data sheet	-	-

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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### 4-fold ESD protection array for SD-card protection

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# **Contents**

1.	General description	1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	. 2
9.	Characteristics	3
10.	Application information	. 6
11.	Package outline	. 7
12.	Soldering	. 8
13.	Revision history	9
14.	Legal information	10

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