

Ultra low clamping bidirectional ESD protection diode

2 March 2021

**Product data sheet** 

### 1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a DSN0603-2 (SOD962) leadless ultra small Surface-Mounted Device (SMD) package. The device is designed to protect one signal line from the damage caused by ESD and other transients.

### 2. Features and benefits

- Bidirectional ESD protection of one line
- Ultra small leadless package with a height of 0.3 mm
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 6.4 A (average measured)
- Very low clamping voltage: V<sub>CL</sub> = 9 V max for 5.4 A, 8/20 µs pulse
- Ultra low leakage current: I<sub>RM</sub> < 1 nA</li>
- ESD protection up to 20 kV

### 3. Applications

ESD and surge protection for:

- very sensitive interface lines
- generic interface lines

in portable electronics, communication, consumer and computing devices.

### 4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	5.5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	5.3	6	pF
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	-	5.4	A

[1] According to IEC 61000-4-5 .

[2] Average measured  $I_{PPM} = 6.4 A$ .



# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	1 2	sym045
			Transparent top view	
			DSN0603-2 (SOD962-2)	

# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD5V5U1BCSF	DSN0603-2	silicon, leadless ultra small package; 2 terminals; 0.4 mm pitch; 0.6 mm x 0.3 mm x 0.3 mm body	SOD962-2			

### 7. Marking

Table 4. Marking codes					
Type number	Marking code				
PESD5V5U1BCSF	F3				

### 8. Limiting values

#### Table 5. Limiting values

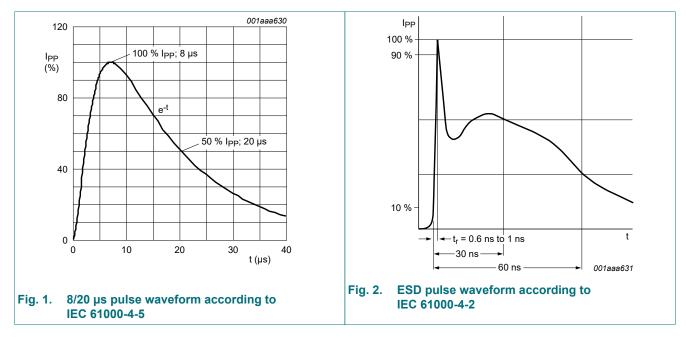
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	5.4	А
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-40	125	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	um ratings	·				
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[3]	-	20	kV
	voltage	IEC 61000-4-2; air discharge	[3]	-	20	kV

[1] [2] According to IEC 61000-4-5.

Average measured  $I_{PPM} = 6.4 \text{ A}.$ 

[3] Device stressed with ten non-repetitive ESD pulses.

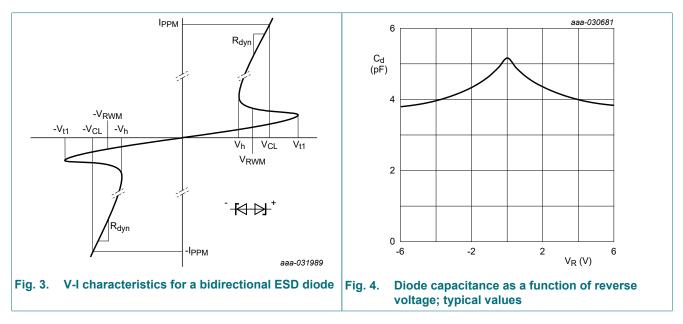


### 9. Characteristics

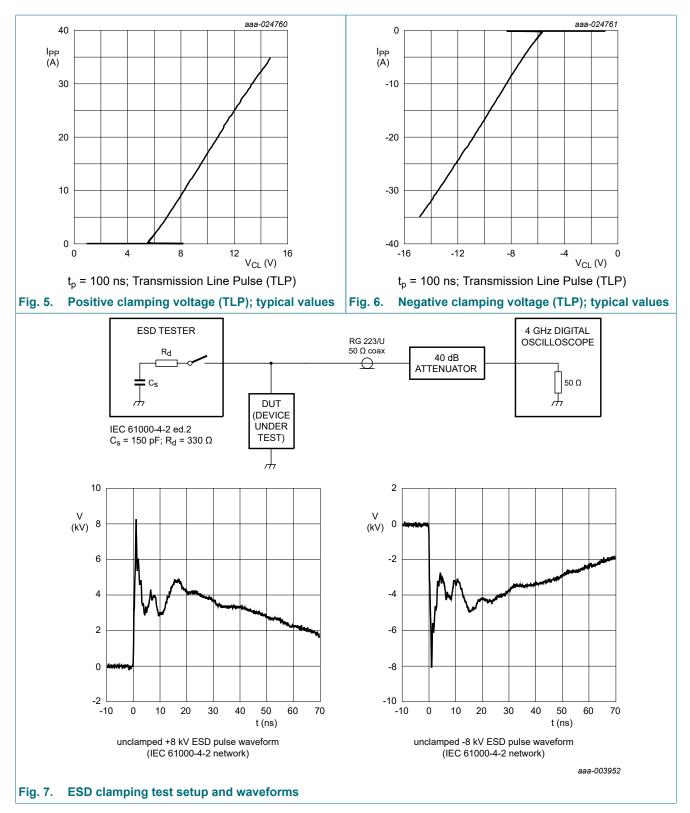
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	5.5	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		4.5	5.5	8	V
V <sub>h</sub>	holding voltage	TLP; 100 ns; T <sub>amb</sub> = 25 °C		-	5.5	-	V
V <sub>t1</sub>	trigger voltage			-	8.2	-	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 5.5 V; T <sub>amb</sub> = 25 °C		-	0.1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	5.3	6	pF
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 5.4 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1]	-	-	9	V
		I <sub>PP</sub> = 8 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C	[2]	-	7.8	9	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C	[2]	-	9.7	12	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2]	-	0.24	-	Ω

[1]

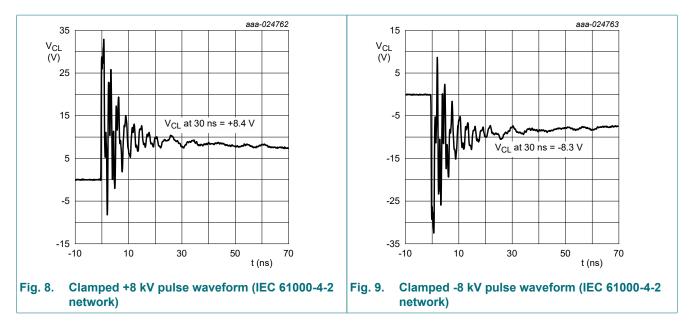
Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5. Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008. [2]



### Ultra low clamping bidirectional ESD protection diode

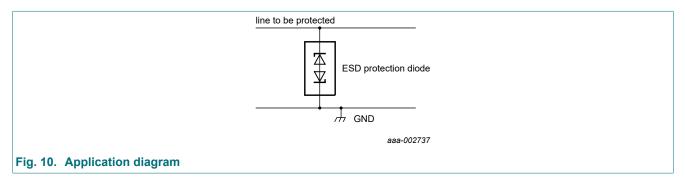


### Ultra low clamping bidirectional ESD protection diode



### **10.** Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground. The device is not designed to be used on lines connected to a DC supply.

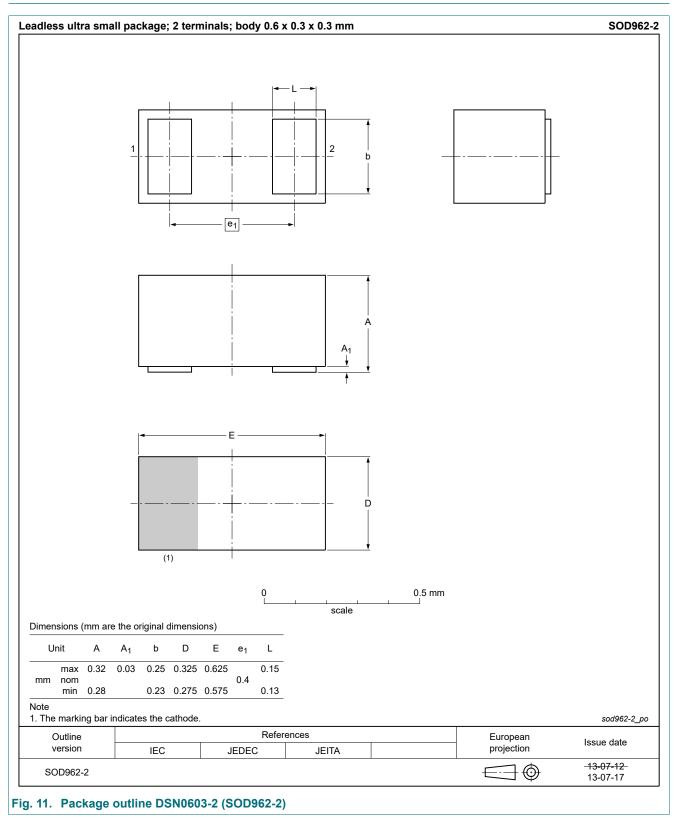


#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

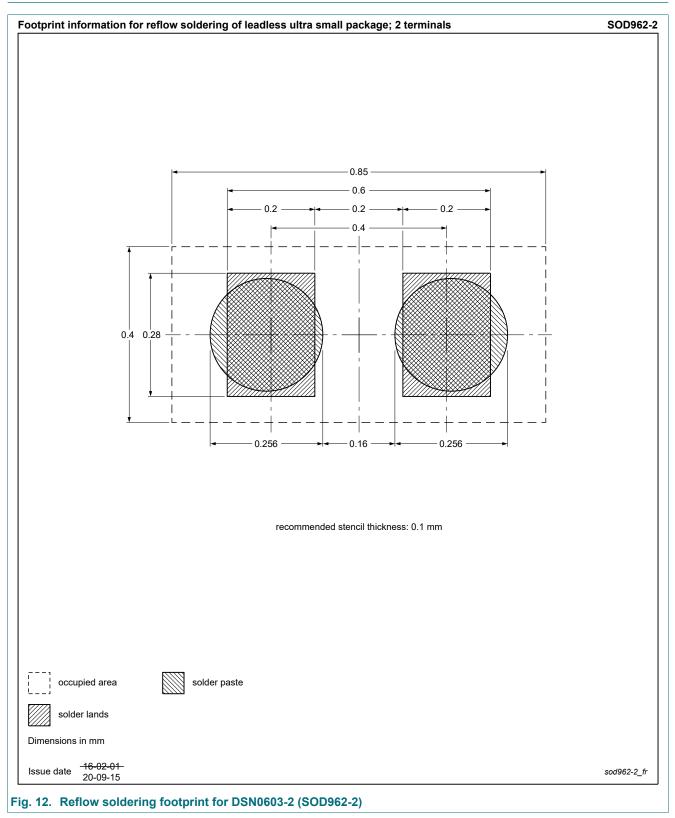
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### **11. Package outline**



### Ultra low clamping bidirectional ESD protection diode

# 12. Soldering



# **13. Revision history**

Table 7. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD5V5U1BCSF v.2	20210302	Product data sheet	-	PESD5V5U1BCSF v.1			
Modifications:	Figure "Reflow soldering footprint" updated						
PESD5V5U1BCSF v.1	20200716	Product data sheet	-	-			

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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