**Product data sheet** 

## 1. General description

NPN high-voltage transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Low current (max. 300 mA)
- High voltage (max. 140 V)
- AEC-Q101 qualified

## 3. Applications

· General purpose

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	140	V
I <sub>C</sub>	collector current		-	-	300	mA

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	Е	emitter		
3	С	collector		B—(
				Ė
			1	sym123
			SOT23	

# 6. Ordering information

**Table 3. Ordering information** 

Type number Package					
	Name	Description	Version		
PMBT5550		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		



NPN high-voltage transistor

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PMBT5550	%1F

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	160	V
$V_{CEO}$	collector-emitter voltage	open base		-	140	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	300	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	600	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 9. Thermal characteristics

### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uily-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

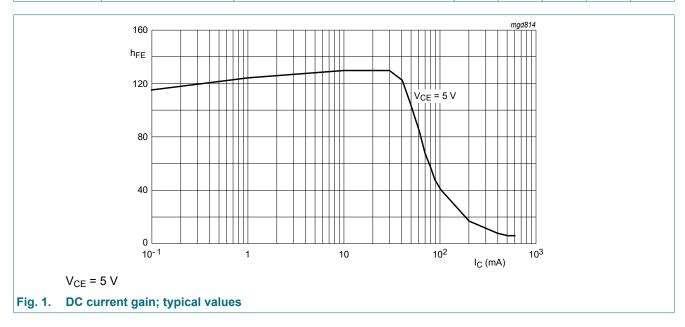
NPN high-voltage transistor

## 10. Characteristics

#### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$I_{CBO}$	collector-base cut-off	$V_{CB} = 100 \text{ V}; I_{E} = 0 \text{ A}; T_{j} = 25 \text{ °C}$	-	-	50	nA
	current	V <sub>CB</sub> = 100 V; T <sub>amb</sub> = 100 °C	-	-	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA	60	-	-	
		$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; T_{j} = 25 \text{ °C}$	60	250	-	
		$V_{CE} = 5 \text{ V}; I_{C} = 50 \text{ mA}; T_{j} = 25 \text{ °C}$	20	-	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	-	-	150	mV
	saturation voltage	I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA	-	-	250	mV
V <sub>BEsat</sub>	base-emitter saturation	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	-	-	1	V
	voltage	I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA	-	-	1.2	V
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz	-	-	6	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = 0 \text{ A}; i_c = 0 \text{ A}; f = 1 \text{ MHz}$	-	-	30	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; f = 100 MHz	100	300	-	MHz
NF	noise figure	$V_{CE}$ = 5 V; $I_{C}$ = 200 μA; $R_{S}$ = 2 kΩ; 10 Hz ≤ f ≤ 15700 Hz	-	-	10	dB



## 11. Test information

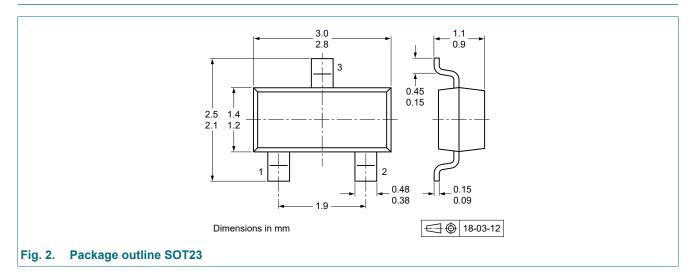
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

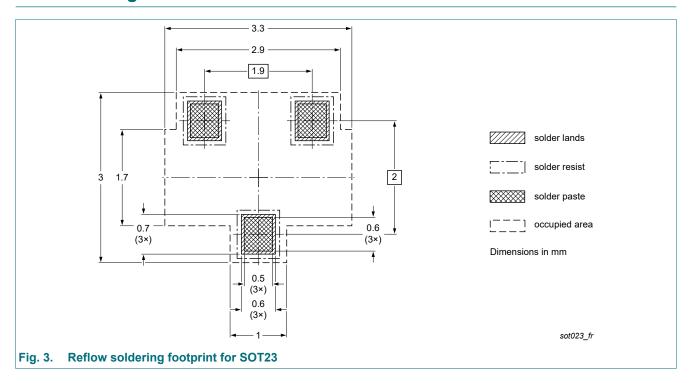
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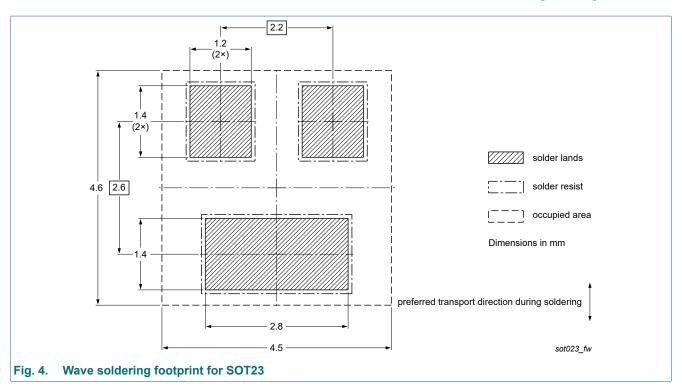
# 12. Package outline



# 13. Soldering



### NPN high-voltage transistor



## NPN high-voltage transistor

# 14. Revision history

### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBT5550 v.3	20200831	Product data sheet	-	PMBT5550 v.2			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>						
PMBT5550 v.2	20040121	Product data sheet	-	PMBT5550 v.1			
PMBT5550 v.1	19990415	Product data sheet	-	-			

#### NPN high-voltage transistor

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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PMBT5550

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