

# PMCM4401UNE 20 V, N-channel Trench MOSFET 29 May 2017

Product data sheet

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a 4 bumps Wafer Level Chip-Size Package (WLCSP) using Trench MOSFET technology.

#### 2. Features and benefits

- Low threshold voltage
- Ultra small package: 0.78 x 0.78 x 0.35 mm
- Trench MOSFET technology •
- ElectroStatic Discharge (ESD) protection > 2 kV HBM •

#### 3. Applications

- Battery switch
- High-speed line driver
- Low-side loadswitch
- Switching circuits

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>GS</sub>	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	$V_{GS}$ = 4.5 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	-	5.4	А
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 4.5 V; I <sub>D</sub> = 3 A; T <sub>j</sub> = 25 °C		-	43	52	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>

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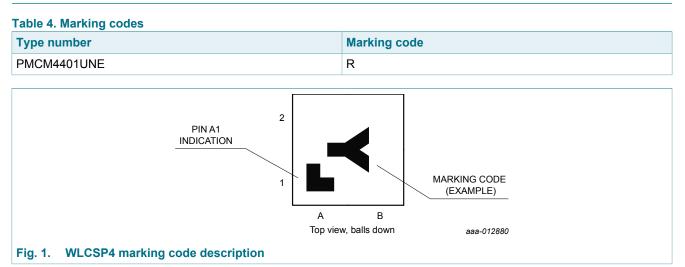
### 5. Pinning information

Table 2. Pinning information								
Pin	Symbol	Description	Simplified outline	Graphic symbol				
A1	G	gate	1 2	D				
A2	S	source						
B1	D	drain		G ← → 🛱 本 \				
B2	S	source	в					
			Transparent top view WLCSP4 (WLCSP4_2-2)	S 017aaa255				

# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMCM4401UNE	WLCSP4	wafer level chip-size package; 4 bumps (2 x 2)	WLCSP4_2-2			

### 7. Marking



20 V, N-channel Trench MOSFET

#### 8. Limiting values

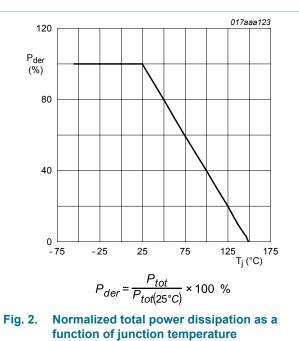
#### Table 5. Limiting values

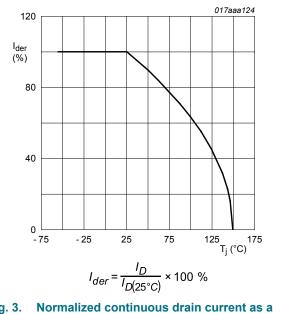
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	5.4	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	4.2	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	2.7	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	17	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	400	mW
			[1]	-	1.3	W
		T <sub>sp</sub> = 25 °C		-	12.5	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode					
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.2	А

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.

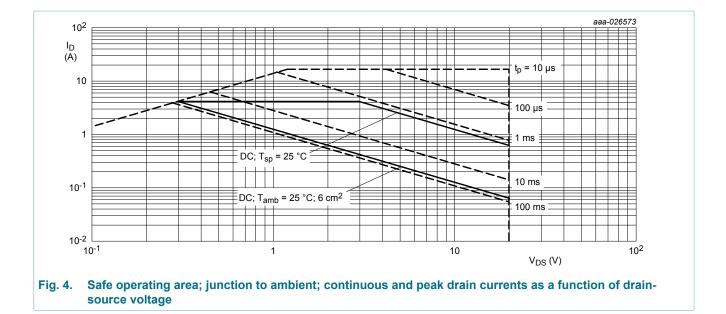






# PMCM4401UNE

#### 20 V, N-channel Trench MOSFET



# 9. Thermal characteristics

#### Table 6. Thermal characteristics

	iai characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	250	300	K/W
			[2]	-	70	85	K/W
			[3]	-	85	100	K/W
		t ≤ 5 s	[3]	-	50	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	5	10	K/W

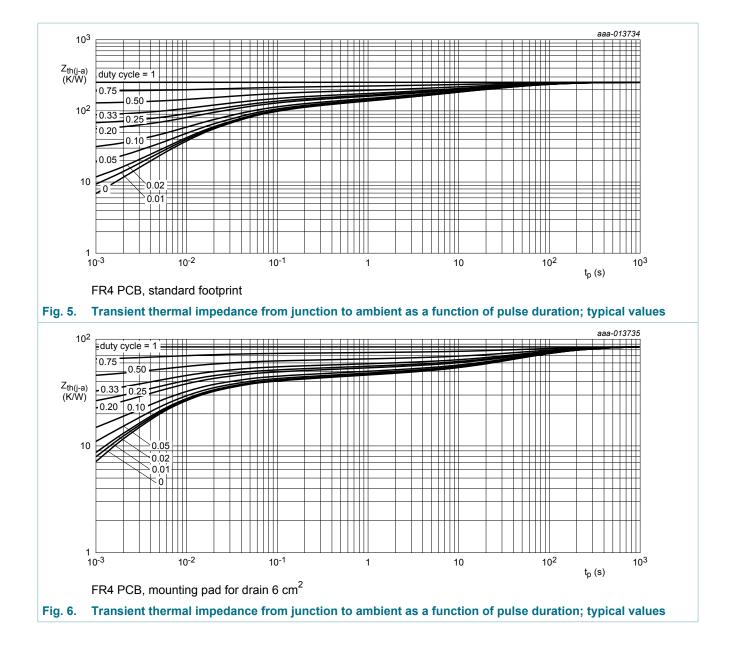
Device mounted on an FR4 Printed-Circuit Board (PCB), single sided-copper; tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain, 4 layer, 1 cm<sup>2</sup> [1]

[2]

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

# PMCM4401UNE

#### 20 V, N-channel Trench MOSFET



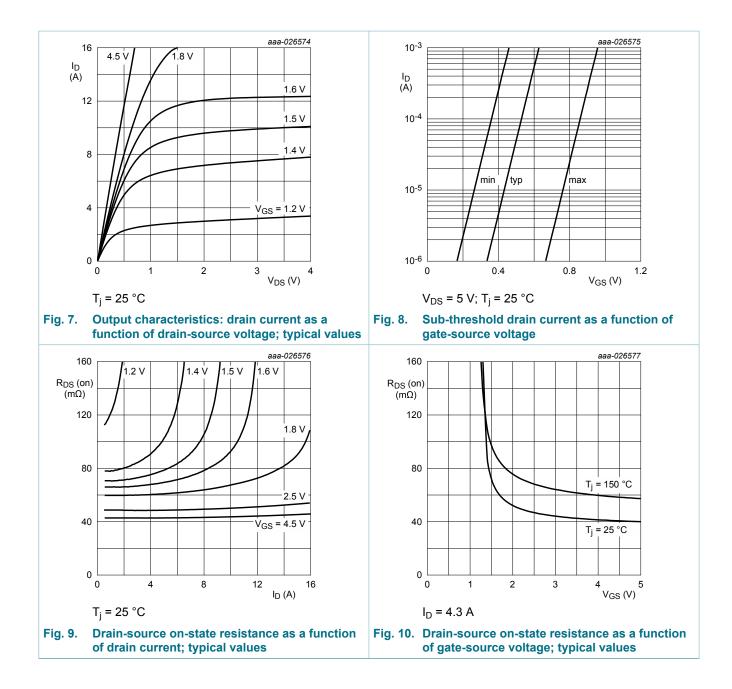
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# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		1 1	-		
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_D$ = 250 µA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = 25 °C	0.4	0.6	0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 20 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		$V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-10	μA
		$V_{GS}$ = 4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
		$V_{GS}$ = -4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-1	μA
		$V_{GS}$ = 2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	200	nA
		$V_{GS}$ = -2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-200	nA
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 3 A; T <sub>j</sub> = 25 °C	-	43	52	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 3 A; T <sub>j</sub> = 150 °C	-	60	71	mΩ
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 3 A; T <sub>j</sub> = 25 °C	-	55	64	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 25 °C	-	65	85	mΩ
		V <sub>GS</sub> = 1.5 V; I <sub>D</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	75	120	mΩ
9fs	forward transconductance	V <sub>DS</sub> = 5 V; I <sub>D</sub> = 3 A; T <sub>j</sub> = 25 °C	-	22	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C	-	4.6	-	Ω
Dynamic ch	aracteristics			1	1	
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 4 A; V <sub>GS</sub> = 4.5 V;	-	6.2	9	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.4	-	nC
Q <sub>GD</sub>	gate-drain charge	_	-	2	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 10 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	315	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	70	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	65	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 10 V; I <sub>D</sub> = 4 A; V <sub>GS</sub> = 4.5 V;	-	4.4	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	23	-	ns
t <sub>d(off)</sub>	turn-off delay time	1	-	27	-	ns
t <sub>f</sub>	fall time	1	-	10	-	ns
Source-drai	n diode		· · ·			
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 1.2 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.7	1.2	V

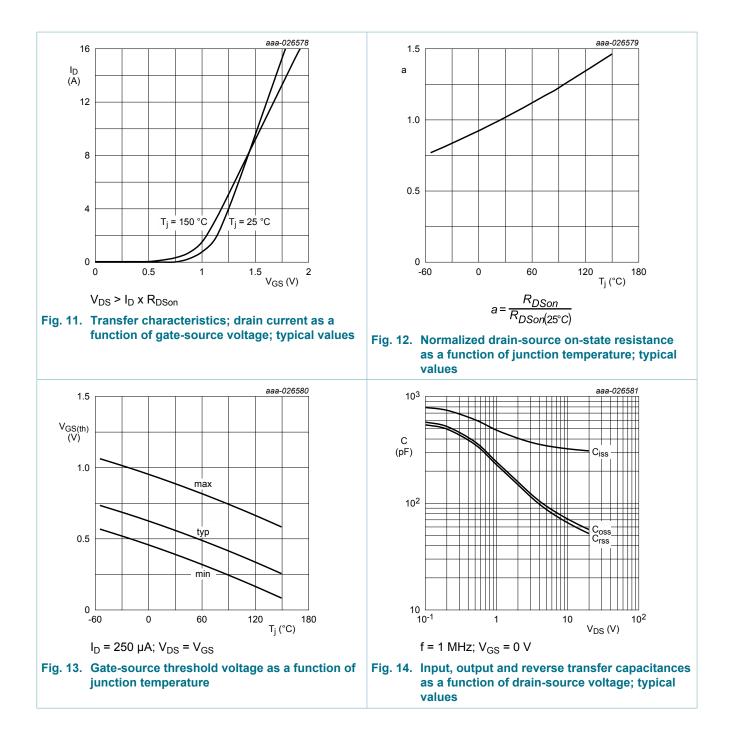
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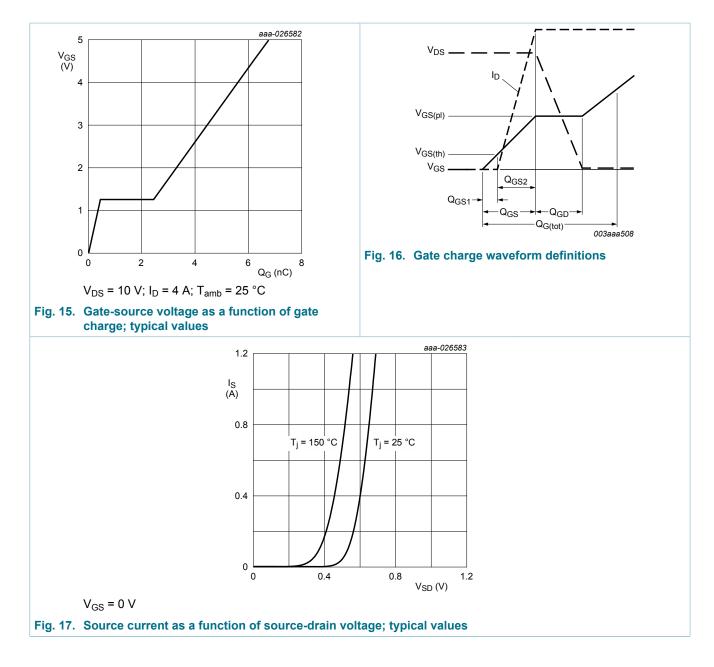
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#### 20 V, N-channel Trench MOSFET

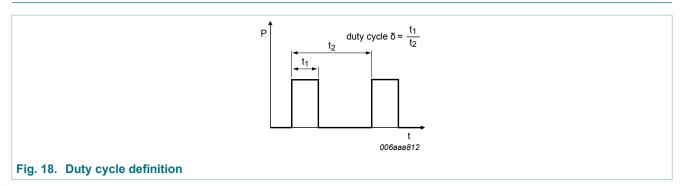


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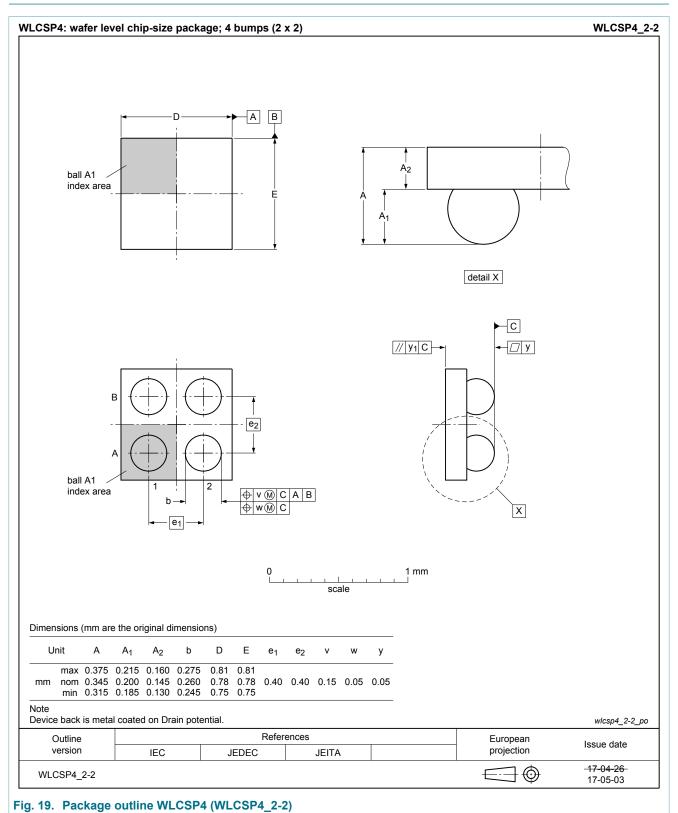


# **11. Test information**



#### 20 V, N-channel Trench MOSFET

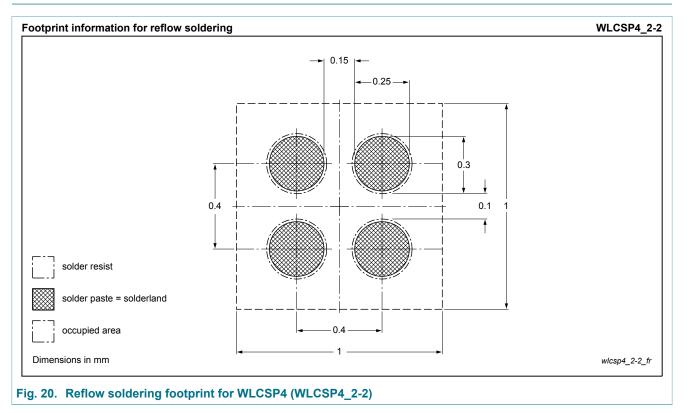
#### 12. Package outline



PMCM4401UNE

20 V, N-channel Trench MOSFET

# 13. Soldering



#### 20 V, N-channel Trench MOSFET

# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMCM4401UNE v.1	20170529	Product data sheet	-	-		

#### 20 V, N-channel Trench MOSFET

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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#### 20 V, N-channel Trench MOSFET

# 16. Contents

1.	General description	.1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	.2
8.	Limiting values	. 3
9.	Thermal characteristics	. 4
10.	Characteristics	6
11.	. Test information	. 9
12.	Package outline	10
13.	Soldering	11
	Revision history	
15.	Legal information	13

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