Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a 4 bumps Wafer Level Chip-Size Package (WLCSP) using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Ultra small package: 0.78 × 0.78 × 0.35 mm
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

3. Applications

- · Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V_{DS}	drain-source voltage	T _j = 25 °C		-	-	12	V	
V _{GS}	gate-source voltage			-8	-	8	V	
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	6	Α	
Static characte	Static characteristics							
R _{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 3 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	36	42	mΩ	

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
A1	G	gate	1 2	D I
A2	S	source	A () ()	
B1	D	drain		G T
B2	S	source	В	T N
			Transparent top view WLCSP4 (OL- PMCM4401VNE)	S 017aaa255

6. Ordering information

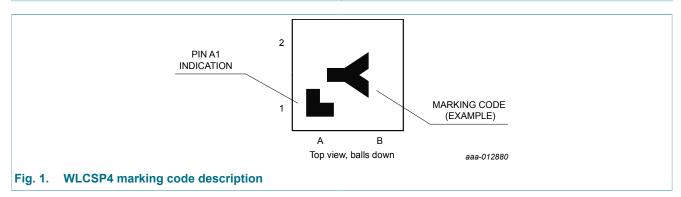
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMCM4401VNE	WLCSP4	WLCSP4: wafer level chip-size package; 4 bumps (2 x 2)	OL-PMCM4401VNE			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMCM4401VNE	P



8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	12	V
V_{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	6	Α
		V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	4.7	Α
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	3	Α
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10$ μs		-	19	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	400	mW
			[1]	-	1300	mW
		T _{sp} = 25 °C		-	12500	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain o	diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	1.1	Α

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

^[2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

Nexperia PMCM4401VNE

12V, N-channel Trench MOSFET

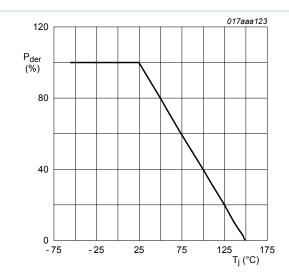


Fig. 2. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

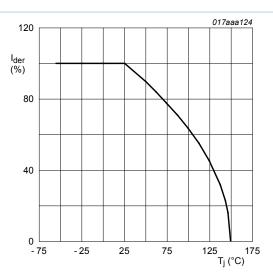
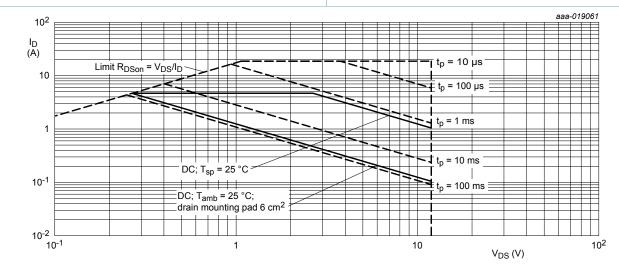


Fig. 3. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$



I_{DM} = single pulse

Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uig a)	thermal resistance		[1]	-	250	300	K/W
	from junction to		[2]	-	70	85	K/W
	anbient		[3]	-	85	100	K/W
		in free air; t ≤ 5 s	[<u>3]</u>	-	50	60	K/W

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		-	5	10	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain, 4-layer, 1 cm².
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

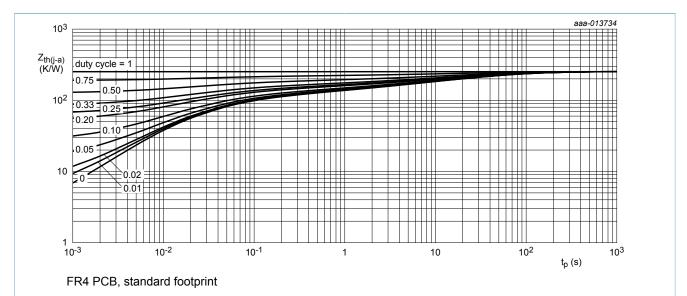


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

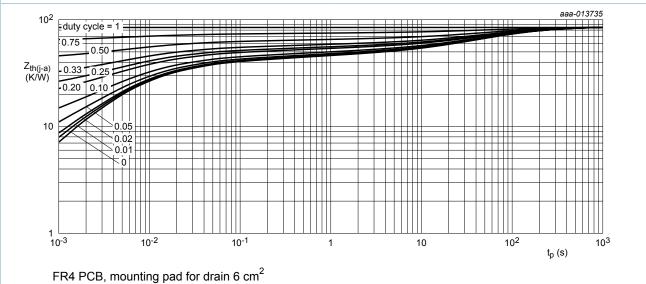


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					_
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	12	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.4	0.6	0.9	V
I _{DSS}	drain leakage current	V _{DS} = 12 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μΑ
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	1	μA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-1	μA
		V _{GS} = 2.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	200	nA
		V _{GS} = -2.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-200	nA
R _{DSon}		V_{GS} = 4.5 V; I_D = 3 A; T_j = 25 °C	-	36	42	mΩ
	resistance	V _{GS} = 4.5 V; I _D = 3 A; T _j = 150 °C	-	50	57	mΩ
		V _{GS} = 2.5 V; I _D = 3 A; T _j = 25 °C	-	46	54	mΩ
		V _{GS} = 1.8 V; I _D = 1 A; T _j = 25 °C	-	60	77	mΩ
		V _{GS} = 1.5 V; I _D = 0.1 A; T _j = 25 °C	-	86	120	mΩ
9 _{fs}	forward transconductance	$V_{DS} = 5 \text{ V}; I_D = 3 \text{ A}; T_j = 25 \text{ °C}$	-	16	-	S
R_G	gate resistance	f = 1 MHz; T _j = 25 °C	-	4.7	-	Ω
Dynamic c	characteristics		'			
Q _{G(tot)}	total gate charge	$V_{DS} = 6 \text{ V}; I_D = 5 \text{ A}; V_{GS} = 4.5 \text{ V};$	-	6	9	nC
Q_{GS}	gate-source charge	T _j = 25 °C	-	0.4	-	nC
Q_{GD}	gate-drain charge		-	1.8	-	nC
C _{iss}	input capacitance	V _{DS} = 6 V; f = 1 MHz; V _{GS} = 0 V;	-	335	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	130	-	pF
C _{rss}	reverse transfer capacitance		-	120	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 6 \text{ V}; I_D = 4 \text{ A}; V_{GS} = 4.5 \text{ V};$	-	6.3	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	35.5	-	ns
t _{d(off)}	turn-off delay time		-	30	-	ns
t _f	fall time		-	18	-	ns

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Source-drain diode							
V_{SD}	source-drain voltage	$I_S = 1.1 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	0.7	1.2	V

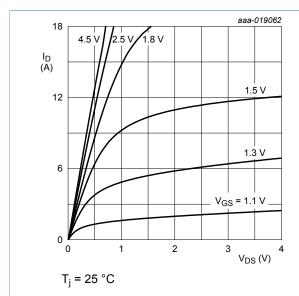
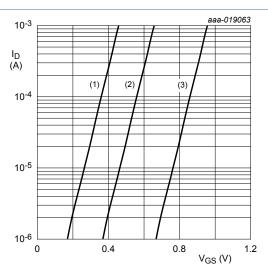


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values



$$T_i = 25 \,^{\circ}C; \, V_{DS} = 5 \,^{\circ}V$$

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 8. Sub-threshold drain current as a function of gate-source voltage

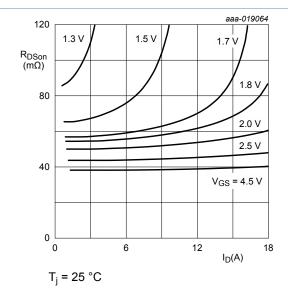


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

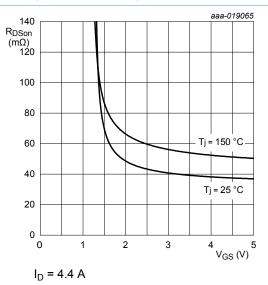
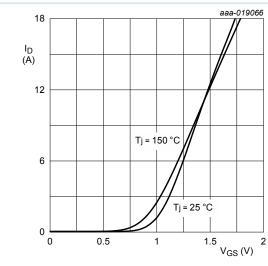


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values



 $V_{DS} > I_D \times R_{DSon}$

Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

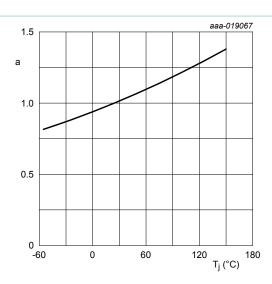
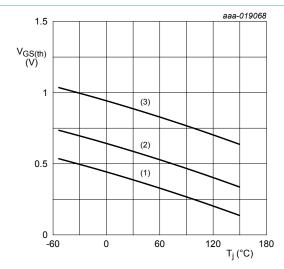


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

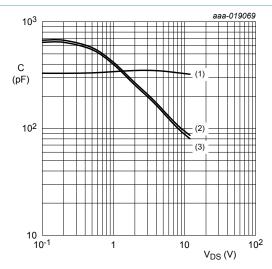
$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$



 $I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 13. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$

- (1) C_{iss}
- (2) C_{oss}
- (3) C_{rss}

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

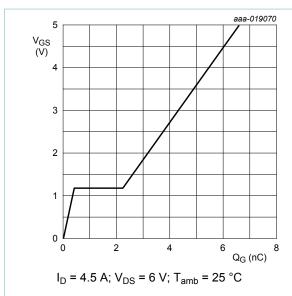


Fig. 15. Gate-source voltage as a function of gate charge; typical values

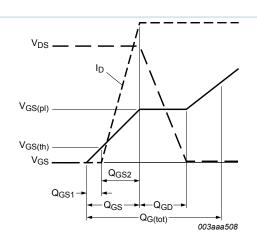


Fig. 16. MOSFET transistor: Gate charge waveform definitions

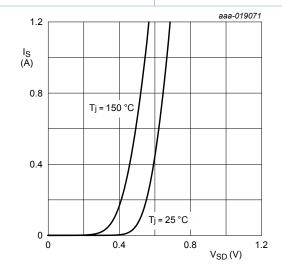
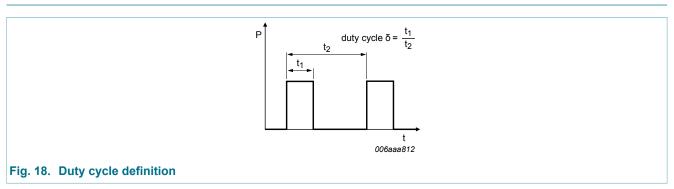


Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

 $V_{GS} = 0 V$

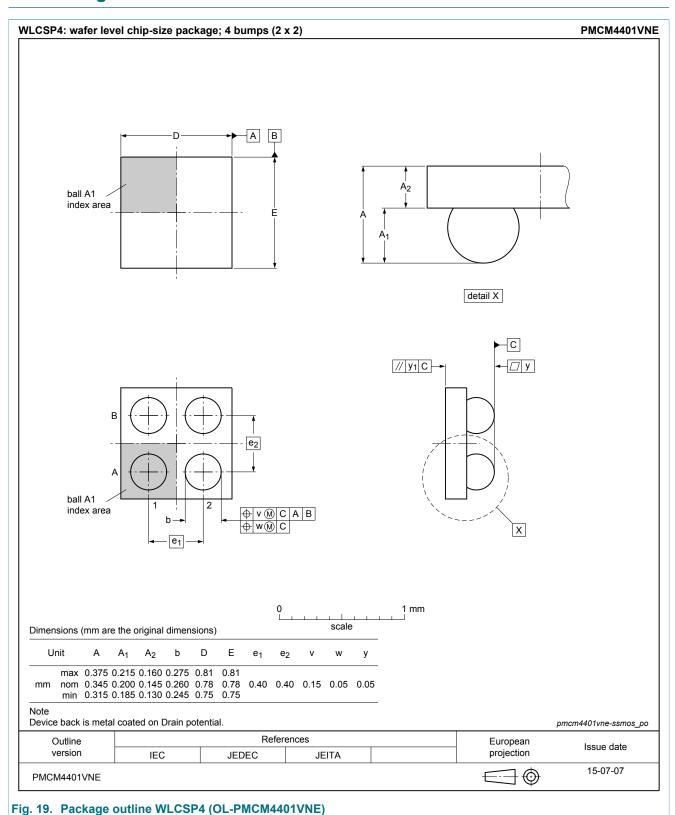


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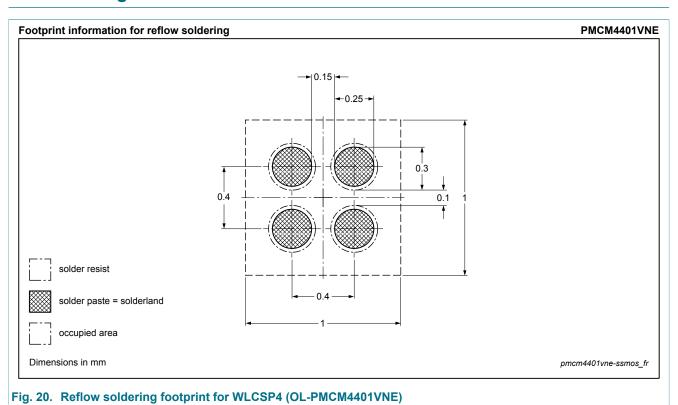
12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMCM4401VNE v.1	20150724	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
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16. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	6
11	Test information	9
12	Package outline	10
13	Soldering	11
14	Revision history	12
15	Legal information	13
15.1	Data sheet status	13
15.2	Definitions	13
15.3	Disclaimers	13
15.4	Trademarks	14

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