

PMCM4402UPE 20 V, P-channel Trench MOSFET 30 May 2017

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a 4 bumps Wafer Level Chip-Size Package (WLCSP) using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Ultra small package 0.78 x 0.78 x 0.35 mm
- Trench MOSFET technology •
- ElectroStatic Discharge (ESD) protection > 2 kV HBM •

3. Applications

- Battery switch
- High-speed line driver
- High-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-4.2	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -3 A; T _j = 25 °C		-	65	80	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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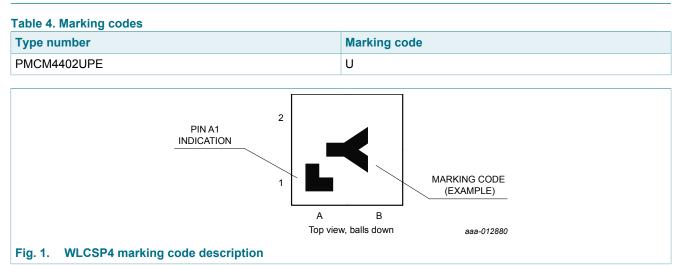
5. Pinning information

Table 2. F	Pinning in	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
A1	G	gate	1 2	D
A2	S	source		
B1	D	drain		G ↓ ↓ ↓
B2	S	source	в	
			Transparent top view WLCSP4 (WLCSP4_2-2)	S 017aaa259

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMCM4402UPE	WLCSP4	wafer level chip-size package; 4 bumps (2 x 2)	WLCSP4_2-2			

7. Marking



8. Limiting values

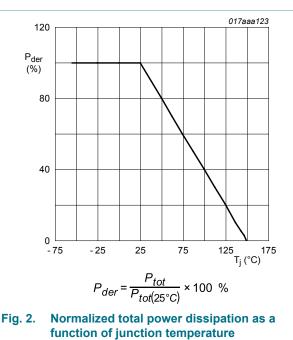
Table 5. Limiting values

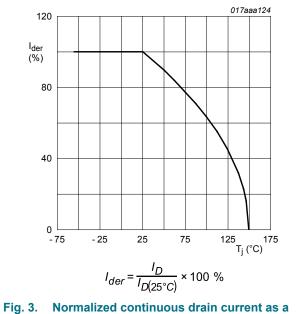
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-4.2	А
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-3.3	А
		V_{GS} = -4.5 V; T_{amb} = 100 °C	[1]	-	-2.1	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-13	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	400	mW
			[1]	-	1.3	W
		T _{sp} = 25 °C		-	12.5	W
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode		·			
ls	source current	T _{amb} = 25 °C	[1]	-	-1.2	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

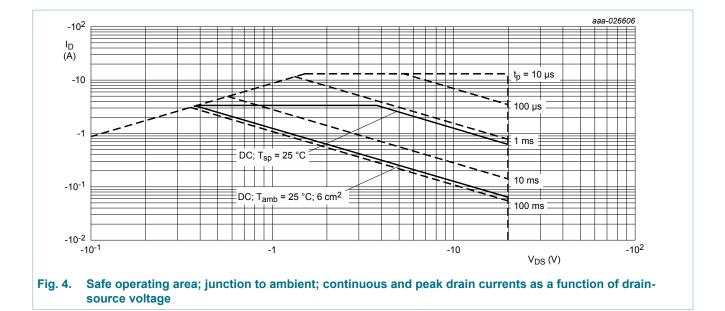






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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	250	300	K/W
	from junction to ambient		[2]	-	70	85	K/W
			[3]	-	85	100	K/W
		in free air; t ≤ 5 s	[3]	-	50	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	5	10	K/W

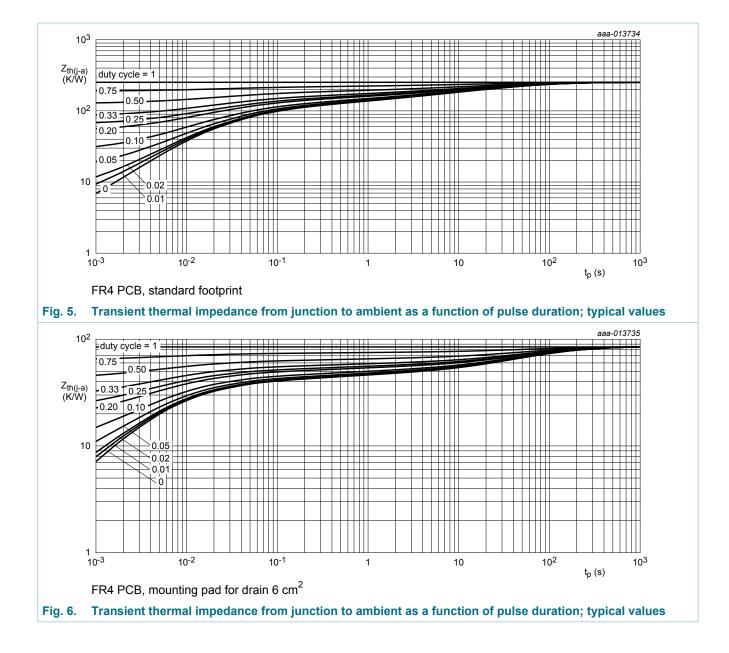
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain, 4-layer, 1 cm². [1]

[2]

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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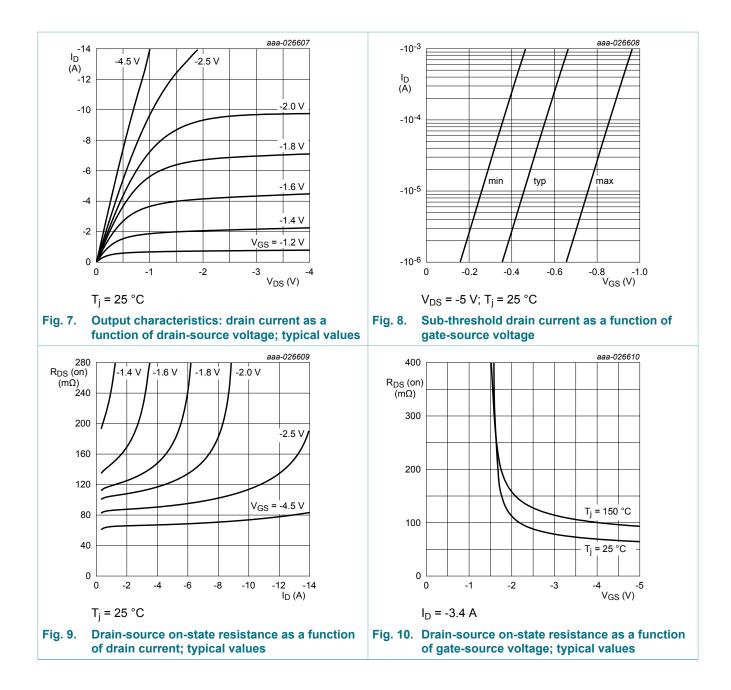
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I_D = -250 µA; V_{GS} = 0 V; T_j = 25 °C	-20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = -250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	-0.4	-0.6	-0.9	V
I _{DSS}	drain leakage current	V_{DS} = -20 V; V_{GS} = 0 V; T_j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V_{GS} = 8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	10	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	1	μA
		V_{GS} = -4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-1	μA
		V_{GS} = 2.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	200	nA
		V_{GS} = -2.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-200	nA
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -3 A; T _j = 25 °C	-	65	80	mΩ
		V _{GS} = -4.5 V; I _D = -3 A; T _j = 150 °C	-	93	114	mΩ
		V_{GS} = -2.5 V; I _D = -2 A; T _j = 25 °C	-	88	110	mΩ
		V _{GS} = -1.8 V; I _D = -0.1 A; T _j = 25 °C	-	120	180	mΩ
9 _{fs}	forward transconductance	V _{DS} = -6 V; I _D = -3 A; T _j = 25 °C	-	14	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	-	6	-	Ω
Dynamic ch	naracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_D = -3 A; V_{GS} = -4.5 V;	-	6.2	10	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.7	-	nC
Q _{GD}	gate-drain charge		-	2	-	nC
C _{iss}	input capacitance	V_{DS} = -10 V; f = 1 MHz; V_{GS} = 0 V;	-	450	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	72	-	pF
C _{rss}	reverse transfer capacitance		-	66	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I _D = -3.3 A; V _{GS} = -4.5 V;	-	4	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	17	-	ns
t _{d(off)}	turn-off delay time		-	26	-	ns
t _f	fall time		-	11	-	ns
Source-drai	in diode	· · ·				
V _{SD}	source-drain voltage	I _S = -1.2 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.8	-1.2	V

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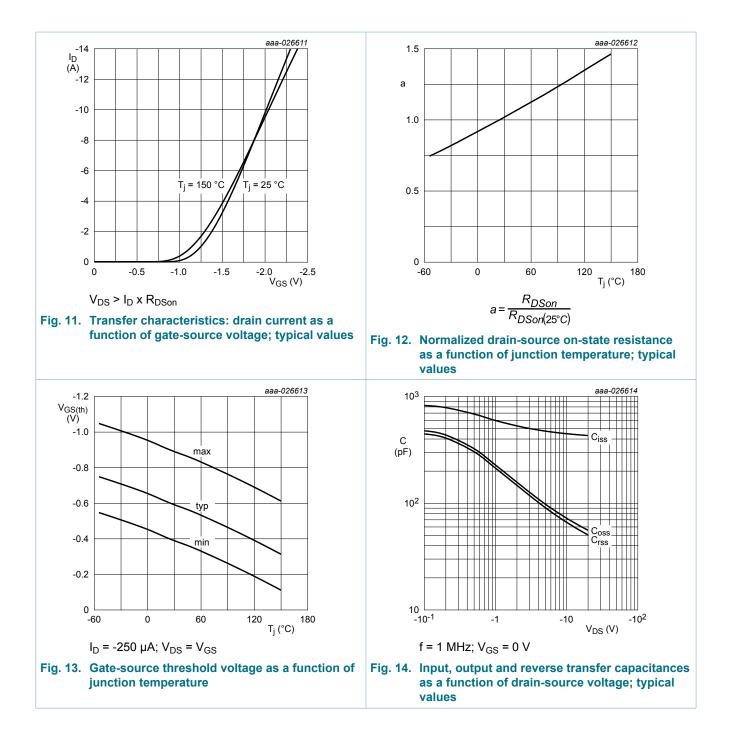
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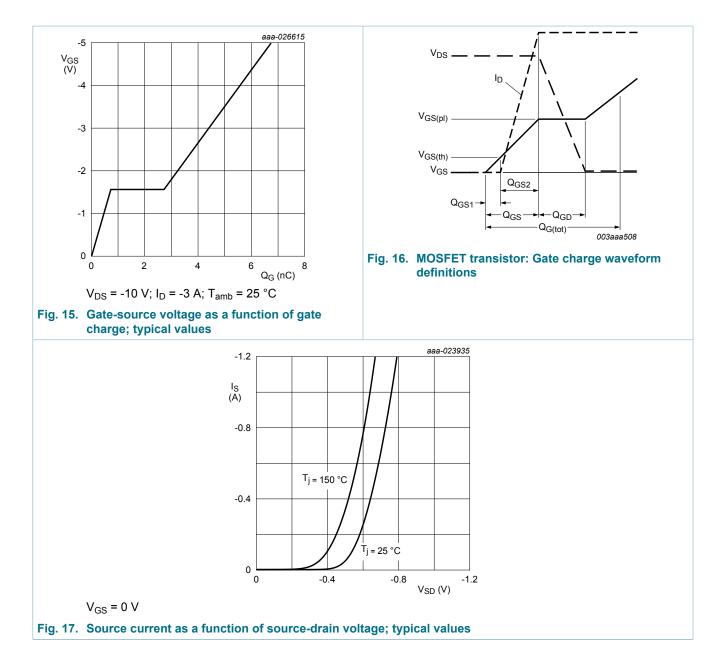
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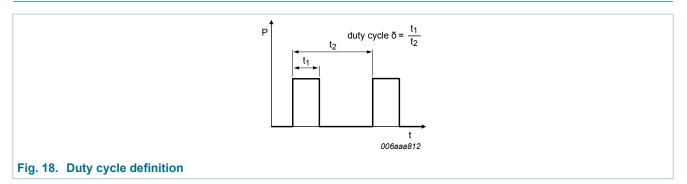


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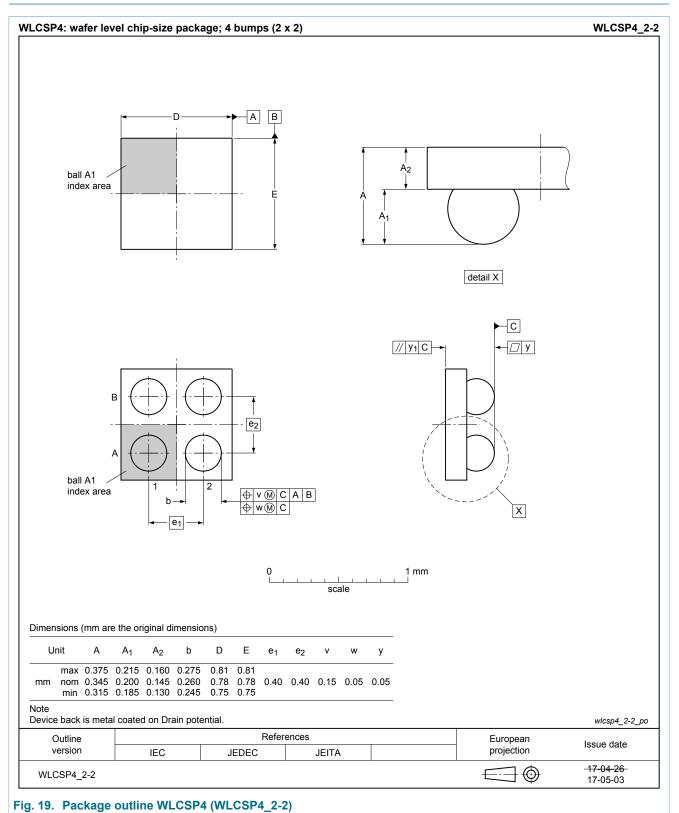


11. Test information



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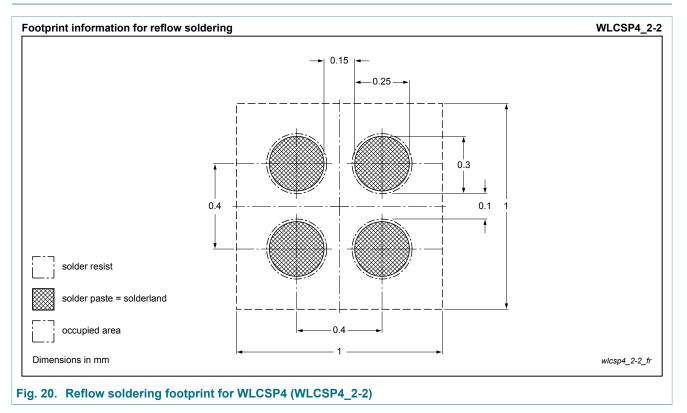
12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMCM4402UPE v.1	20170530	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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