

45 V, 15 A low VF Trench MEGA Schottky barrier rectifier

27 September 2017

Product data sheet

1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 15 A
- Reverse voltage: V_R ≤ 45 V
- Low forward voltage
- Low leakage current due to Trench MEGA Schottky technology
- High power capability due to clip-bonding technology and heat sink
- Small and thin SMD plastic package, typical height 0.78 mm
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	-	45	V
V _F	forward voltage	I_F = 15 A; T_j = 25 °C; pulsed	[1]	-	510	570	mV
I _R	reverse current	V_R = 10 V; T_j = 25 °C; pulsed	[1]	-	14	51	μA
		V_R = 45 V; T_j = 25 °C; pulsed	[1]	-	30	98	μA

[1] Very short pulse, in order to maintain a stable junction temperature.

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5. Pinning information

Table 2	. Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	А	anode		
3	К	cathode	(2 CFP15 (SOT1289)	

6. Ordering information

Table 3. Ordering information							
Type number	Package	age					
	Name	Description	Version				
PMEG045T150EIPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 terminals; 5.8 x 4.3 x 0.78 mm body	SOT1289				

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG045T150EIPD	045T M15E

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _R	reverse voltage	T _j = 25 °C		-	45	V
l _F	forward current	T _{sp} ≤ 118 °C; δ = 1		-	21	Α
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	130	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Therma	al characteristics	·					
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance	in free air	[1] [2]	-	-	90	K/W
	from junction to ambient		[1] [3]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

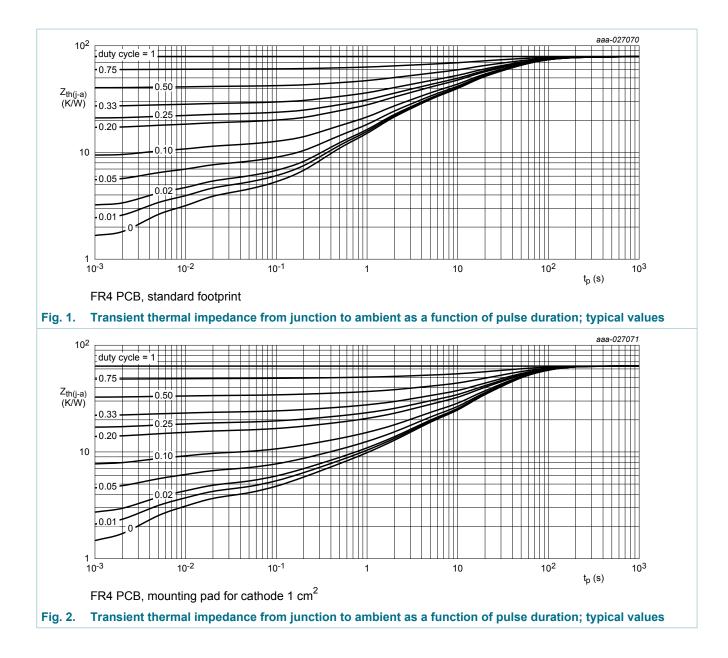
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

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10. Characteristics

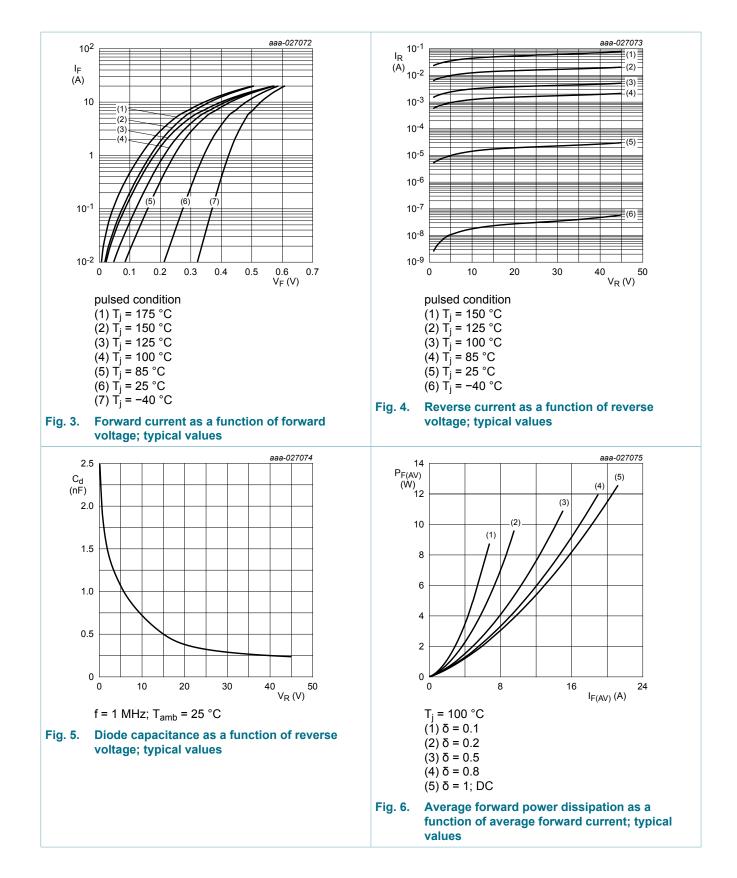
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_R = 1 mA; T_j = 25 °C; pulsed	[1]	45	-	-	V
V _F	forward voltage	$I_F = 1 \text{ A}; T_j = 25 \text{ °C}; \text{ pulsed}$	[1]	-	335	375	mV
		$I_{F} = 5 \text{ A}; T_{j} = 25 \text{ °C}; \text{ pulsed}$	[1]	-	410	460	mV
		I_F = 10 A; T_j = 25 °C; pulsed	[1]	-	465	520	mV
		$I_F = 15 \text{ A}; T_j = 25 \text{ °C}; \text{ pulsed}$	[1]	-	510	570	mV
		I_F = 15 A; T_j = -40 °C; pulsed	[1]	-	550	-	mV
		I _F = 15 A; T _j = 125 °C; pulsed	[1]	-	465	-	mV
I _R	reverse current	V_{R} = 10 V; T _j = 25 °C; pulsed	[1]	-	14	51	μA
		V_{R} = 30 V; T _j = 25 °C; pulsed	[1]	-	23	-	μA
		V_R = 45 V; T_j = 25 °C; pulsed	[1]	-	30	98	μA
		V _R = 45 V; T _j = 125 °C; pulsed	[1]	-	20	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	1.7	-	nF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	0.72	-	nF
t _{rr}	reverse recovery time step recovery	$ \begin{array}{l} {\sf I}_{\sf F} = 0.5 \; {\sf A}; \; {\sf I}_{\sf R} = 0.5 \; {\sf A}; \; {\sf I}_{\sf R(meas)} = 0.1 \; {\sf A}; \\ {\sf T}_{\sf j} = 25 \; ^{\circ}{\rm C} \end{array} $		-	49	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; T _j = 25 °C; I _F = 6 A; V _R = 26 V		-	21	-	ns

[1] Very short pulse, in order to maintain a stable junction temperature.

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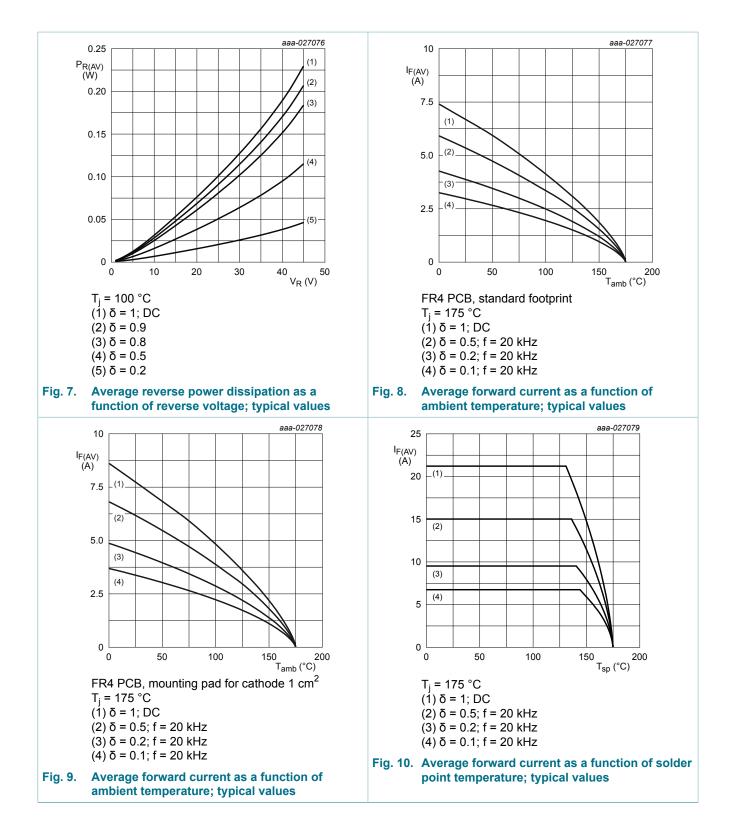
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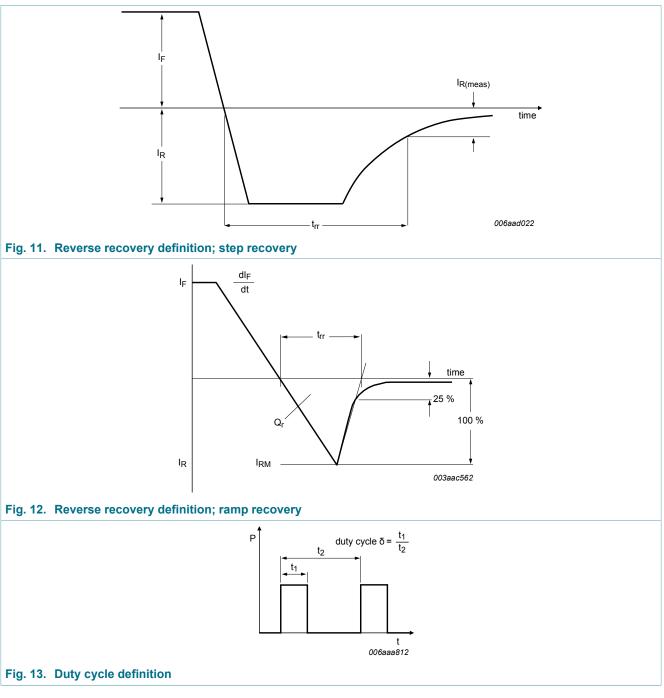
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11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current,

 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$

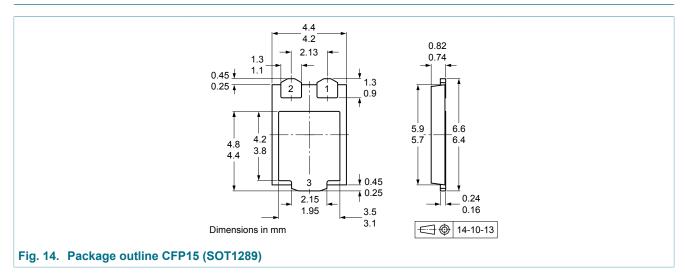
with I_{RMS} defined as RMS current.

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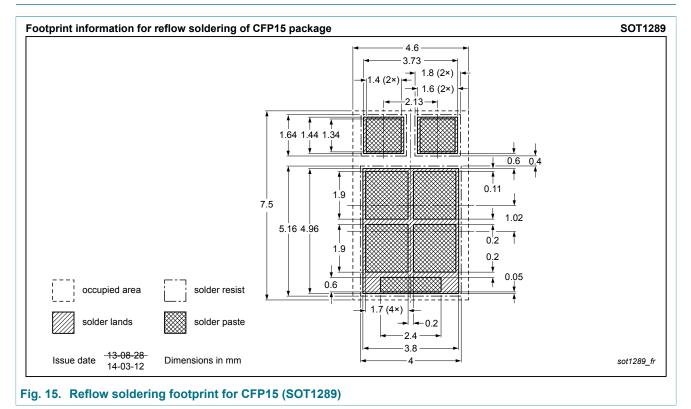
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





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14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG045T150EIPD v.1	20170927	Product data sheet	-	-

PMEG045T150EIPD

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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