

100 V, 2 A low leakage current Schottky barrier rectifier29 November 2017Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 100 V
- Low forward voltage: V<sub>F</sub>= 770 mV
- High power capability due to clip-bonding technology
- Extremely low leakage current I<sub>R</sub> = 40 nA
- High temperature T<sub>i</sub> ≤ 175 °C
- AEC-Q101 qualified
- Capable for reflow and wave soldering

### 3. Applications

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- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

### 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \leq ~160 \ ^{\circ}\text{C};$ square wave		-	-	2	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	100	V
V <sub>F</sub>	forward voltage	$I_{F}$ = 2 A; $t_{p}$ $\leq~$ 300 $\mu s;$ $\bar{\delta}$ $\leq~$ 0.02 $$ ; $T_{j}$ = 25 $^{\circ}C$		-	770	830	mV
I <sub>R</sub>	reverse current	$V_{R}$ = 100 V; $t_{p}$ $\leq~$ 300 $\mu s;~\delta \leq~0.02~$ ; $T_{j}$ = 25 °C		-	40	150	nA

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# 5. Pinning information

Table 2. P	Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	К	cathode[1]		1 🛃 2				
2	A	anode	CFP3 (SOD123W)	sym001				

[1] The marking bar indicates the cathode.

# 6. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG10020ELR	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W		

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG10020ELR	К8

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
l <sub>F</sub>	forward current	T <sub>sp</sub> = 155 °C; δ = 1		-	2.8	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 $~;$ f = 20 kHz; $T_{amb} \leq ~80 ~^\circ\text{C};$ square wave	[1]	-	2	A
		$\delta$ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~160 ~^\circ\text{C};$ square wave		-	2	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	50	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	680	mW
			[3]	-	1150	mW
			[1]	-	2140	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1] [2]	-	-	220	K/W
			[1] [3]	-	-	130	K/W
			[1] [4]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	18	K/W

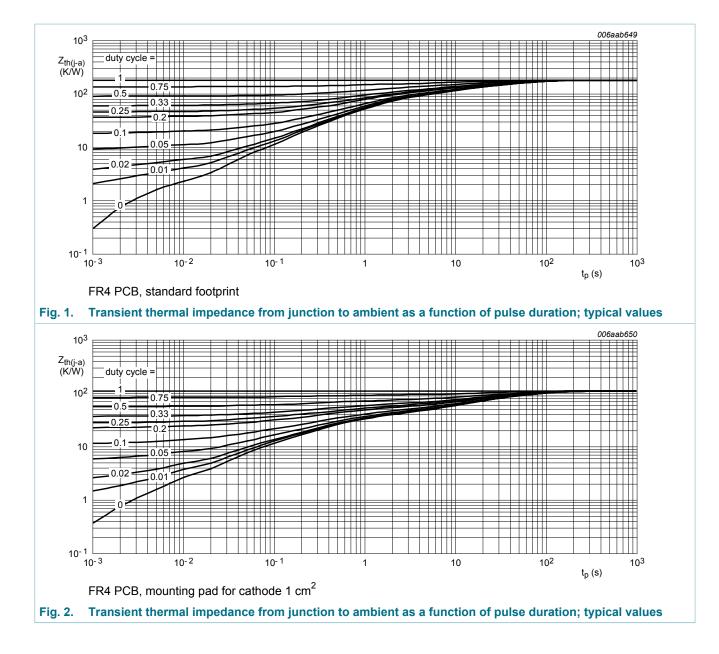
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

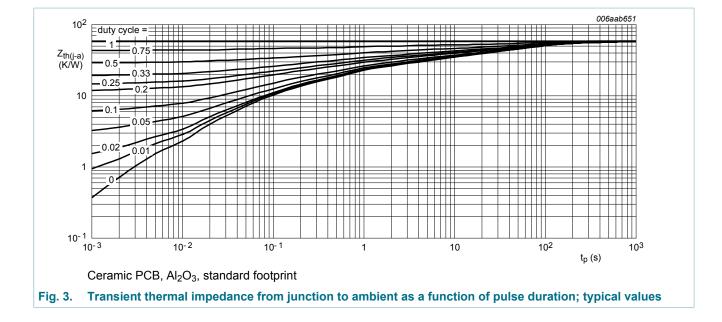
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[5] Soldering point of cathode tab.



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PMEG10020ELR

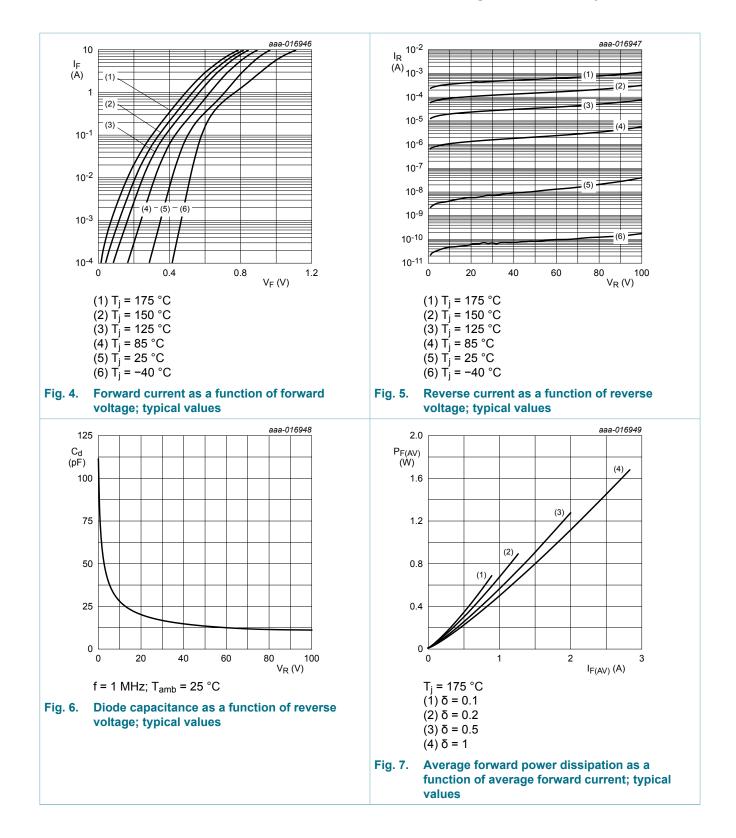
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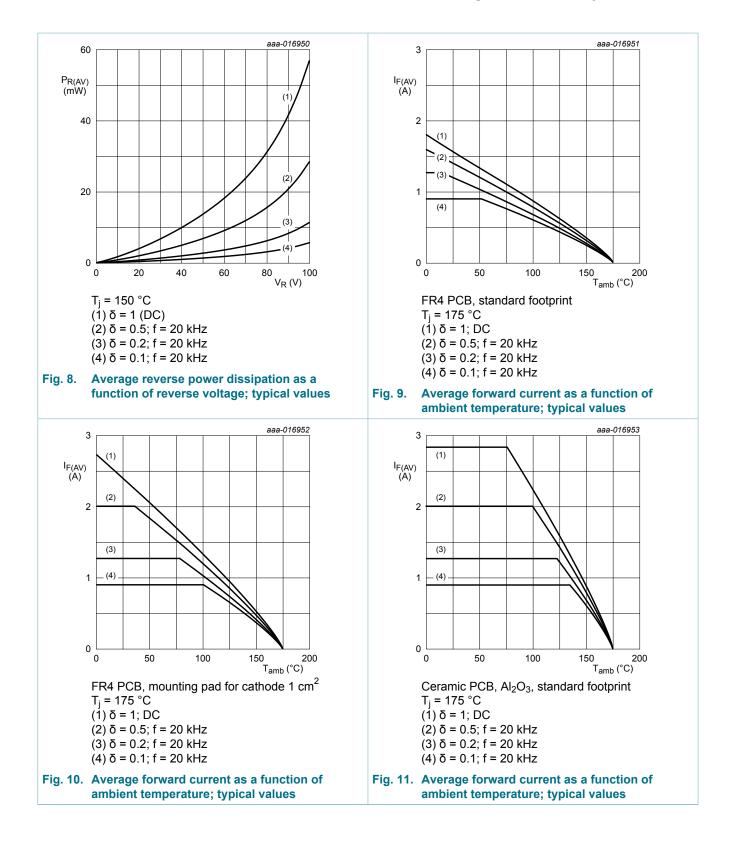
### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
V <sub>(BR)R</sub>	reverse breakdown voltage	$I_R$ = 1 mA; $t_p$ = 300 µs; $\delta$ = 0.02 $\ ;$ $T_j$ = 25 °C	100	-	-	V	
V <sub>F</sub>	V <sub>F</sub>	forward voltage	$I_{F}$ = 0.1 A; $t_{p}$ ≤ 300 μs; δ ≤ 0.02 ; $T_{j}$ = 25 °C	-	505	565	mV
		$I_{F}$ = 0.5 A; $t_{p}$ ≤ 300 μs; δ ≤ 0.02 ; $T_{j}$ = 25 °C	-	640	710	mV	
		$I_{F}$ = 0.7 A; $t_{p}$ ≤ 300 μs; δ ≤ 0.02 ; $T_{j}$ = 25 °C	-	675	740	mV	
		$\begin{array}{l} I_{\text{F}} = 1 \text{ A};  t_{p} \leq \ 300 \ \mu\text{s};  \delta \leq \ 0.02 \ ; \\ T_{j} = 25 \ ^{\circ}\text{C} \end{array}$	-	710	770	mV	
		$I_{F}$ = 1.6 A; $t_{p}$ ≤ 300 μs; δ ≤ 0.02 ; $T_{j}$ = 25 °C	-	750	810	mV	
		$\begin{array}{l} I_F = 2 \; A;  t_p \leq \; 300 \; \mu s;  \delta \leq \; 0.02 \; \; ; \\ T_j = 25 \; ^\circ C \end{array}$	-	770	830	mV	
		$\begin{array}{l} I_F = 2 \; A;  t_p \leq \; 300 \; \mu s;  \delta \leq \; 0.02 \; \; ; \\ T_j = 125 \; ^\circ C \end{array}$	-	635	740	mV	
I <sub>R</sub>	reverse current	$V_{R}$ = 10 V; $t_{p}$ $\leq$ 300 $\mu$ s; $\delta$ $\leq$ 0.02 ; $T_{j}$ = 25 °C	-	4	-	nA	
		$V_{R}$ = 60 V; $t_{p} \le$ 300 µs; $\delta \le$ 0.02 ; $T_{j}$ = 25 °C	-	12	-	nA	
		$V_R$ = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02$ ; T <sub>j</sub> = 25 °C	-	40	150	nA	
		$V_R$ = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02$ ; T <sub>j</sub> = 125 °C	-	70	500	μA	
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	70	-	pF	
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	42	-	pF	
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	28	-	pF	
rr	reverse recovery time	$I_F$ = 0.5 A; $I_R$ = 1 A; $I_{R(meas)}$ = 0.25 A; $T_j$ = 25 °C	-	3.7	-	ns	
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 0.5 A; dI <sub>F</sub> /dt = 20 A/μs; T <sub>j</sub> = 25 °C	-	690	-	mV	

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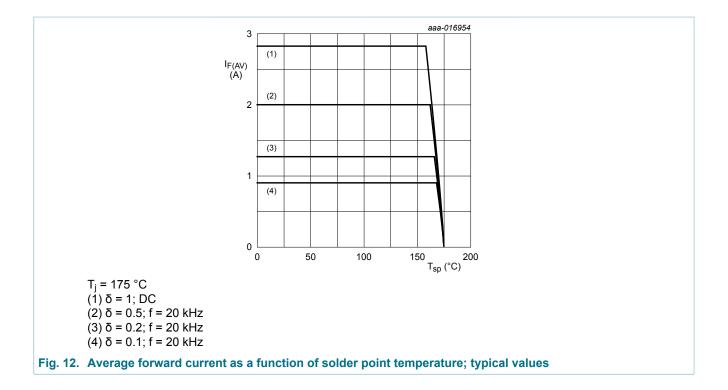




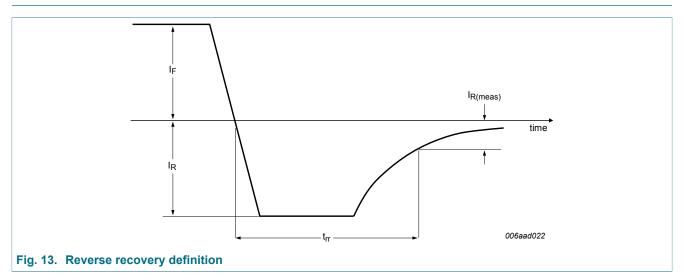
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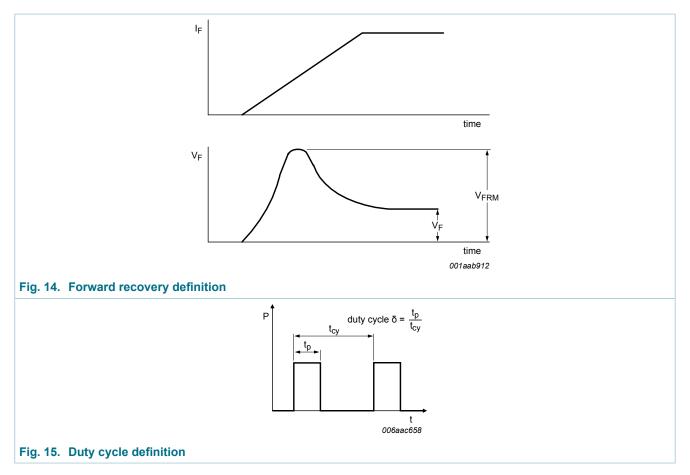
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### 11. Test information



#### 100 V, 2 A low leakage current Schottky barrier rectifier



The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

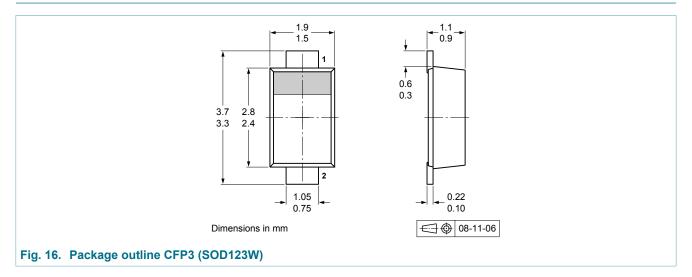
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

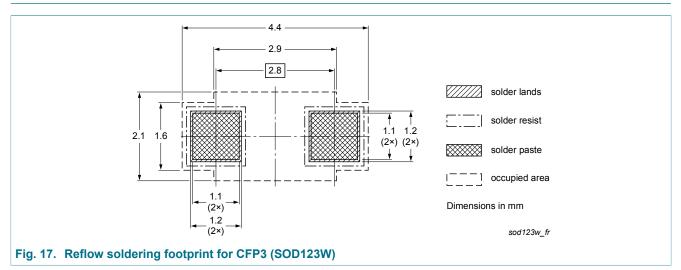
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100 V, 2 A low leakage current Schottky barrier rectifier

### 12. Package outline

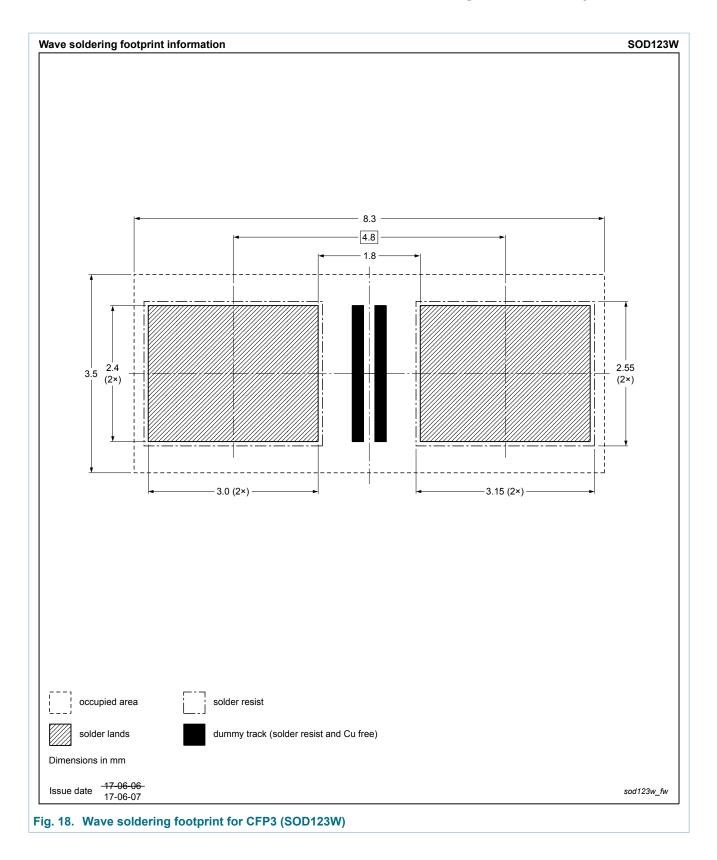


### 13. Soldering



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# PMEG10020ELR



# 14. Revision history

Table 8. Revision histo	ry						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG10020ELR v.3	20171129	Product data sheet	-	PMEG10020ELR v.2			
Modifications:	<ul> <li>Features and benefits: Capable for reflow and wave soldering added</li> <li>Soldering: Wave soldering footprint added</li> </ul>						
PMEG10020ELR v.2	20150507	Product data sheet	-	PMEG10020ELR v.1			
PMEG10020ELR v.1	20150219	Preliminary data sheet	-	-			

#### 100 V, 2 A low leakage current Schottky barrier rectifier

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMEG10020ELR

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