

20 V, 0.5 A low VF MEGA Schottky barrier rectifier

13 February 2015

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection in a DSN0603-2 (SOD962-2) leadless ultra small Chip-Scale Package (CSP).

2. Features and benefits

- Average forward current I_{F(AV)} ≤ 0.5 A
- Reverse voltage V_R ≤ 20 V
- Low forward voltage typ. V_F = 245 mV
- Low reverse current typ. $I_R = 5 \mu A$
- Package height typ. 0.3 mm

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Ultra high speed switching
- LED backlight for mobile application

4. Quick reference data

- - -

| Symbol | Parameter | Conditions | | Min | Тур | Мах | Unit |
|--------------------|-------------------------|--|-----|-----|-----|-----|------|
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; T _{amb} = 115 °C; square wave | [1] | - | - | 0.5 | A |
| | | δ = 0.5; f = 20 kHz; T _{sp} = 145 °C; square wave | | - | - | 0.5 | A |
| V _R | reverse voltage | T _j = 25 °C | | - | - | 20 | V |
| V _F | forward voltage | I_F = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | | - | 245 | 310 | mV |
| I _R | reverse current | V_R = 10 V; T_j = 25 °C; pulsed | | - | 5 | 25 | μA |
| t _{rr} | reverse recovery time | I _F = 500 mA; I _R = 500 mA; I _{R(meas)} = 100 mA; T _j = 25 °C | | - | 1.9 | - | ns |

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

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5. Pinning information

| Table 2. | Pinning | information | | |
|----------|---------|-------------|-------------------------|----------------|
| Pin | Symbol | Description | Simplified outline | Graphic symbol |
| 1 | К | cathode[1] | | 1 🕂 2 |
| 2 | А | anode | | sym001 |
| | | | Transparent top view | |
| | | | DSN0603-2 (SOD962-2) | |

[1] The marking bar indicates the cathode.

6. Ordering information

| Table 3. Ordering information | | | | | | | | |
|---------------------------------|-----------|--|----------|--|--|--|--|--|
| Type number | Package | | | | | | | |
| | Name | Description | Version | | | | | |
| PMEG2005AESF | DSN0603-2 | Leadless ultra small package; 2 terminals; body 0.6 x 0.3 x 0.3 mm | SOD962-2 | | | | | |

7. Marking

| Table 4. Marking codes | |
|------------------------|--------------|
| Type number | Marking code |
| PMEG2005AESF | 6 |

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Мах | Unit |
|--------------------|-------------------------------------|---|-----|-----|------|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 20 | V |
| I _F | forward current | T _{sp} ≤ 140 °C; δ = 1 | | - | 0.71 | А |
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; T _{amb} = 115 °C; square wave | [1] | - | 0.5 | А |
| | | δ = 0.5; f = 20 kHz; T _{sp} = 145 °C; square wave | | - | 0.5 | А |
| I _{FRM} | repetitive peak forward current | $t_p \le 1 \text{ ms}; \delta \le 0.25$ | | - | 2 | А |
| I _{FSM} | non-repetitive peak forward current | t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave | | - | 4.5 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2] | - | 405 | mW |
| | | | [3] | - | 660 | mW |
| | | | [1] | - | 1200 | mW |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for anode and cathode 1 cm² each.

9. Thermal characteristics

Table 6.Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|-----------------------|--|-------------|--------|-----|-----|-----|------|
| fron | thermal resistance | in free air | [1][2] | - | - | 310 | K/W |
| | from junction to ambient | | [1][3] | - | - | 190 | K/W |
| | ambient | | [1][4] | - | - | 105 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | [5] | - | - | 40 | K/W |

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

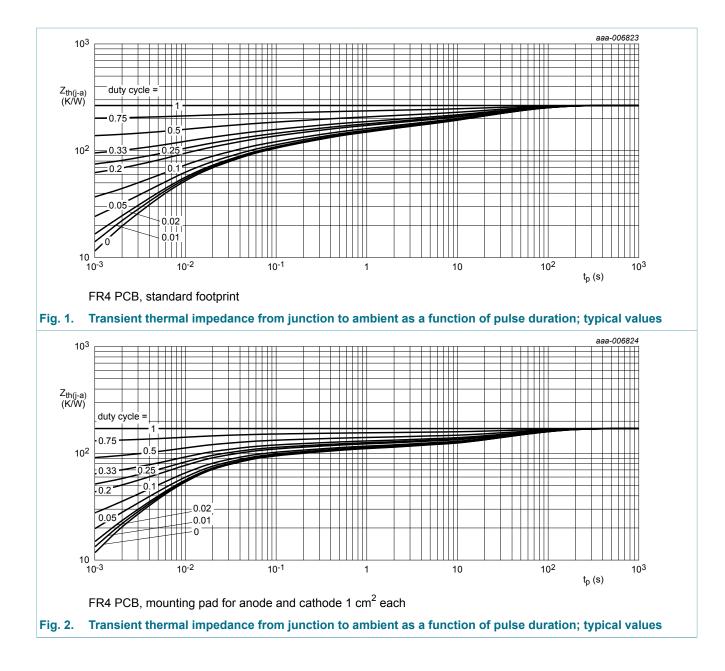
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for anode and cathode 1 cm² each.

- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
 - [5] Soldering point of anode tab.

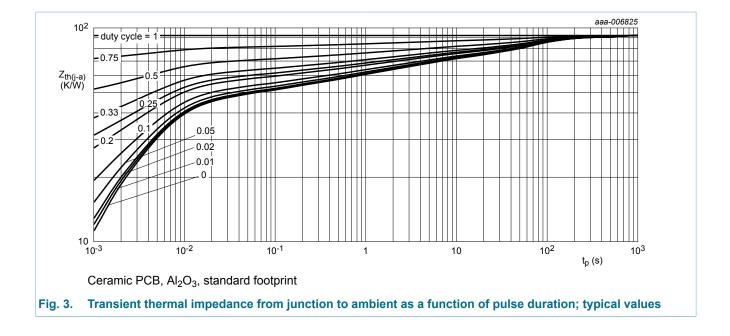
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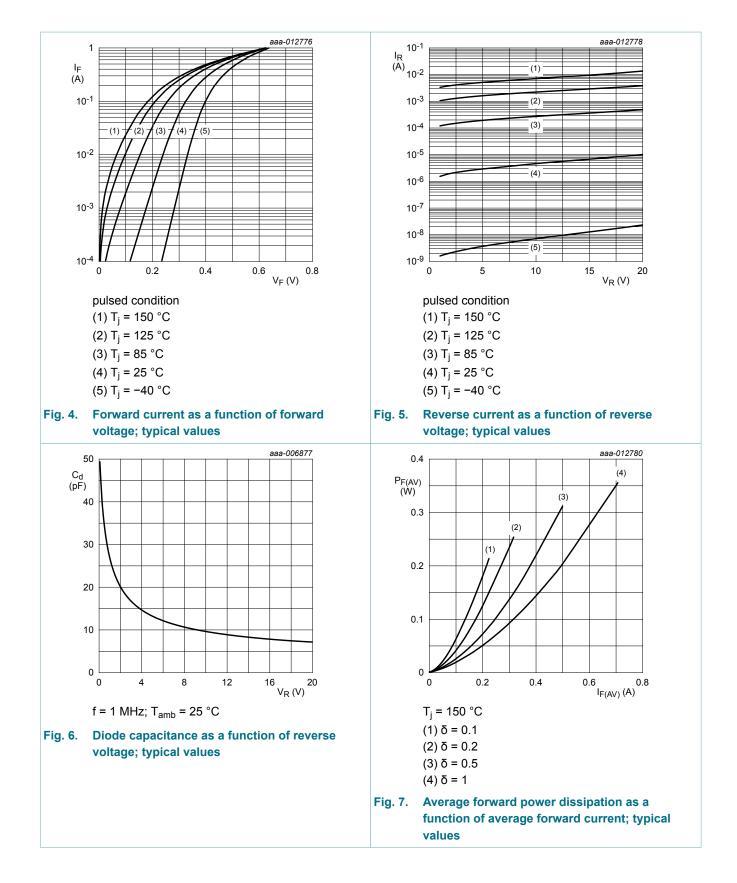
10. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|------------------------------|--|-----|-----|-----|------|
| V _{(BR)R} | reverse breakdown voltage | I _R = 100 μA; t _p = 300 μs; δ = 0.02; T _j = 25 °C | 20 | - | - | V |
| VF | forward voltage | I_F = 0.1 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | - | 120 | 180 | mV |
| | | I_F = 1 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | - | 180 | 250 | mV |
| | | $I_F = 10 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$ | - | 245 | 310 | mV |
| | | I_F = 100 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | - | 330 | 380 | mV |
| | | $I_{F} = 200 \text{ mA}; t_{p} \le 300 \mu\text{s}; \delta \le 0.02;$ $T_{j} = 25 \text{ °C}$ | - | 375 | 420 | mV |
| | | I_F = 500 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | - | 475 | 550 | mV |
| I _R | reverse current | V_R = 6 V; T_j = 25 °C; pulsed | - | 3.2 | - | μA |
| | | V_R = 10 V; T _j = 25 °C; pulsed | - | 5 | 25 | μA |
| | | V_R = 20 V; T _j = 25 °C; pulsed | - | 10 | 45 | μA |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | - | 25 | - | pF |
| | | V _R = 10 V; f = 1 MHz; T _j = 25 °C | - | 10 | - | pF |
| t _{rr} | reverse recovery time | I _F = 500 mA; I _R = 500 mA; I _{R(meas)} = 100 mA; T _i = 25 °C | - | 1.9 | - | ns |

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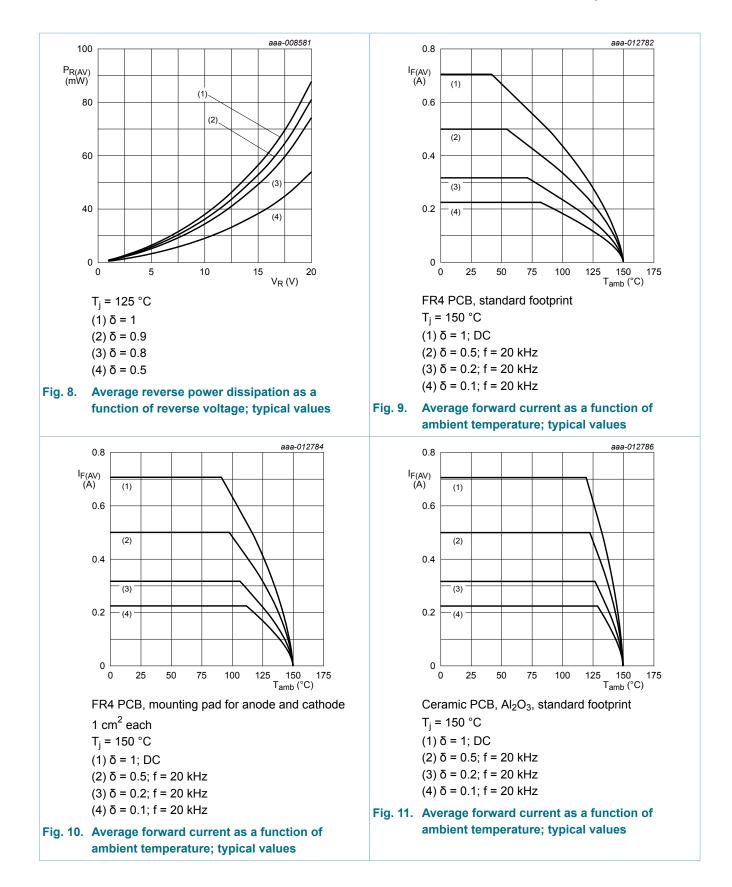
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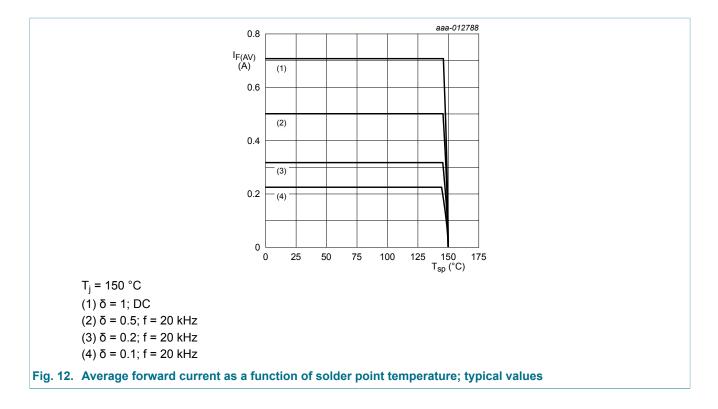
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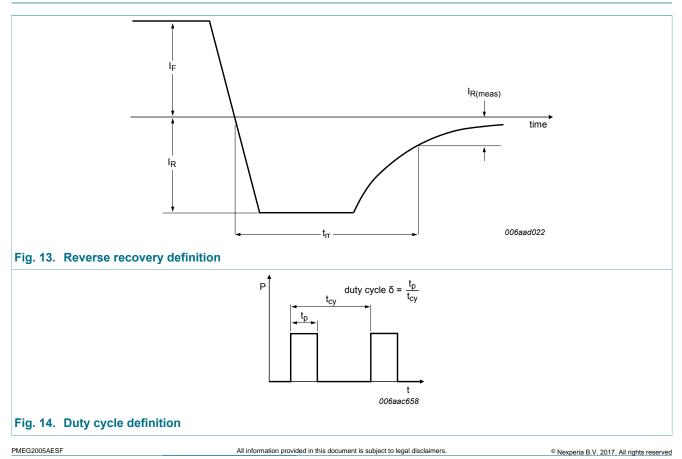
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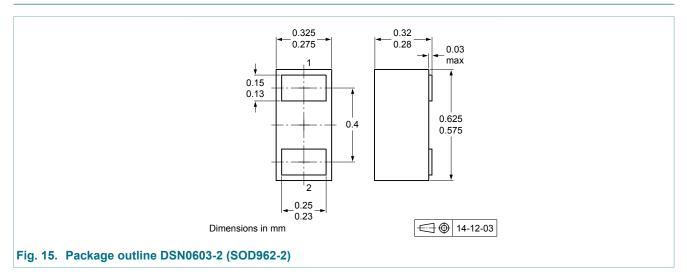
11. Test information



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The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

12. Package outline



13. Soldering

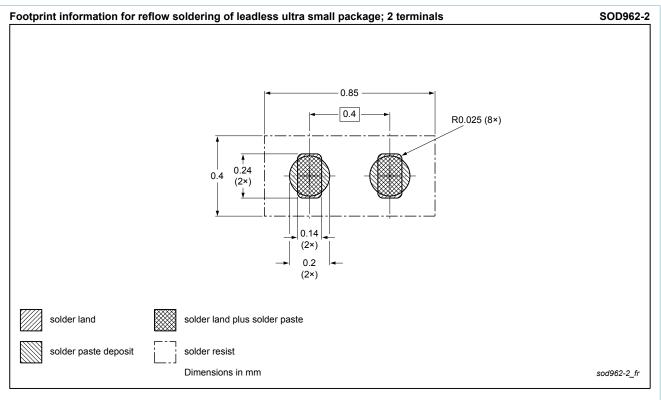


Fig. 16. Reflow soldering footprint for DSN0603-2 (SOD962-2)

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14. Revision history

| Table 8.Revision history | | | | |
|--------------------------|--------------|------------------------|---------------|------------------|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
| PMEG2005AESF v.2 | 20150213 | Product data sheet | - | PMEG2005AESF v.1 |
| Modifications: | Product sta | itus changed | | |
| PMEG2005AESF v.1 | 20141219 | Preliminary data sheet | - | - |

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15. Legal information

15.1 Data sheet status

| Document status [1][2] | Product status [<u>3]</u> | Definition |
|--------------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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