

# PMEG2005EL

20 V, 0.5 A very low  $V_F$  MEGA Schottky barrier rectifier in leadless ultra small SOD882 package

Rev. 02 — 15 January 2010

Product data sheet

## 1. Product profile

## 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier diode with an integrated guard ring for stress protection encapsulated in a SOD882 leadless ultra small plastic package.

### 1.2 Features

- Forward current: 0.5 A
- Reverse voltage: 20 V
- Very low forward voltage
- Leadless ultra small plastic package
- Power dissipation comparable to SOT23

## 1.3 Applications

- Ultra high-speed switching
- Voltage clamping
- Protection circuits
- Low voltage rectification
- High efficiency DC-to-DC conversion
- Low power consumption applications

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Value	Unit
I <sub>F</sub>	forward current	0.5	Α
V <sub>R</sub>	reverse voltage	20	V



## 2. Pinning information

Table 2. Discrete pinning

10010 21	Discrete pirining	
Pin	Description	Simplified outline Symbol
1	cathode	[1]
2	anode	1 2 2 sym001 Bottom view
		Top view
		001aaa332

<sup>[1]</sup> The marking bar indicates the cathode.

## 3. Ordering information

Table 3. Ordering information

Type number	Package	Package					
	Name	Description	Version				
PMEG2005EL	-	leadless ultra small plastic package; 2 terminals; body $1.0 \times 0.6 \times 0.5$ mm	SOD882				

## 4. Marking

Table 4. Marking

Type number	Marking code
PMEG2005EL	F5

## 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{R}$	continuous reverse voltage		-	20	V
I <sub>F</sub>	continuous forward current		-	0.5	Α
I <sub>FRM</sub>	repetitive peak forward current	$\begin{array}{l} t_p \leq 1 \text{ ms;} \\ \delta \leq 0.25 \end{array}$	-	2.5	A
I <sub>FSM</sub>	non-repetitive peak forward current	t = 8 ms square wave	-	3.0	A
Tj	junction temperature		[1] _	150	°C
T <sub>amb</sub>	operating ambient temperature		<u>[1]</u> –65	+150	°C
$T_{stg}$	storage temperature		-65	+150	°C

[1] For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses. Nomograms for determining the reverse power losses P<sub>R</sub> and I<sub>F(AV)</sub> rating will be available on request.

## 6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Value	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1][2]	500	K/W

<sup>[1]</sup> Refer to SOD882 standard mounting conditions (footprint), FR4 with 60  $\mu$ m copper strip line.

## 7. Characteristics

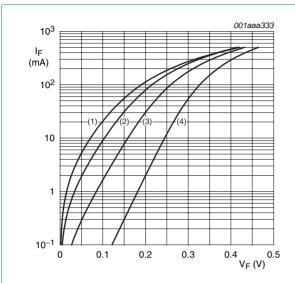
Table 7. Characteristics

 $T_{amb} = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	T	ӯр	Max	Unit
$V_{F}$	continuous forward voltage	see <u>Figure 1</u> I <sub>F</sub> = 0.1 mA	1.	25	180	mV
		I <sub>F</sub> = 1 mA	1	85	240	mV
		I <sub>F</sub> = 10 mA	2	250	290	mV
		I <sub>F</sub> = 100 mA	3	325	380	mV
		$I_F = 500 \text{ mA}$	4	50	500	mV
I <sub>R</sub>	continuous reverse current	V <sub>R</sub> = 10 V; see <u>Figure 2</u>	[1] 4	ļ	30	μΑ
C <sub>d</sub>	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz;}$ see Figure 3	2	24	30	pF

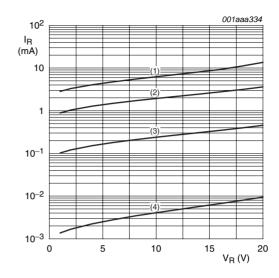
<sup>[1]</sup> Pulse test:  $t_0 \le 300 \ \mu s$ ;  $\delta \le 0.02$ .

<sup>[2]</sup> For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses. Nomograms for determining the reverse power losses  $P_R$  and  $I_{F(AV)}$  rating will be available on request.



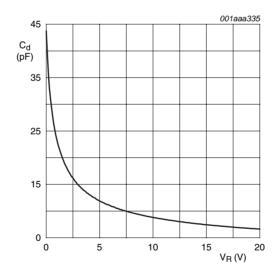
- (1)  $T_j = 150 \, ^{\circ}C$
- (2)  $T_i = 125 \, ^{\circ}\text{C}$
- (3)  $T_i = 85 \, ^{\circ}C$
- (4)  $T_i = 25 \,^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1)  $T_j = 150 \,^{\circ}\text{C}$
- (2)  $T_i = 125 \, ^{\circ}C$
- (3)  $T_j = 85 \, ^{\circ}C$
- (4)  $T_j = 25 \, ^{\circ}C$

Fig 2. Reverse current as a function of reverse voltage; typical values



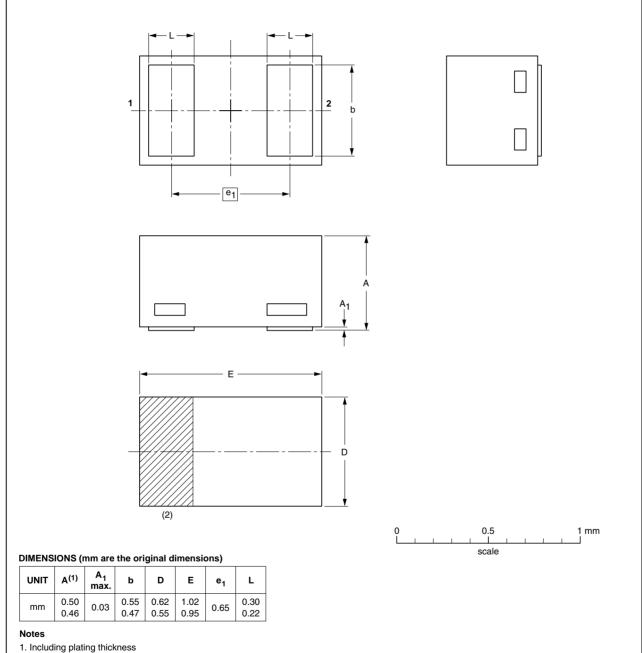
 $f = 1 \text{ MHz}; T_{amb} = 25 \,^{\circ}\text{C}$ 

Fig 3. Diode capacitance as a function of reverse voltage; typical values

#### **Package outline** 8.

Leadless ultra small plastic package; 2 terminals; body 1.0 x 0.6 x 0.5 mm

**SOD882** 



- 2. The marking bar indicates the cathode

OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOD882						<del>-03-04-16</del> 03-04-17

Fig 4. Package outline

PMEG2005EL\_2



## 9. Revision history

### Table 8. Revision history

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Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2005EL_2	20100115	Product specification	-	PMEG2005EL_1
Modifications:	<ul> <li>This data sheet was changed to reflect the new company name NXP, including new legal definitions and disclaimers. No changes were made to the technical content.</li> </ul>			
PMEG2005EL_1	20040211	Product specification	-	-

## 10. Legal information

### 10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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