

60 V, 2 A low leakage current Schottky barrier rectifier28 February 2019Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current:  $I_{F(AV)} \le 2 A$
- Reverse voltage: V<sub>R</sub> ≤ 60 V
- Extremely low leakage current
- Low forward voltage
- · High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T<sub>i</sub> ≤ 175 °C
- Capable for reflow and wave soldering

### 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 160 °C; square wave	-	-	2	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	-	60	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 2 A; T <sub>j</sub> = 25 °C	-	690	760	mV
I <sub>R</sub>	reverse current	$V_R = 60 \text{ V}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^\circ\text{C}; \text{ pulsed}$	-	90	300	nA

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### 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	К	cathode[1]		K 🛃 A			
2	А	anode		sym001			
			CFP3 (SOD123W)				

[1] The marking bar indicates the cathode.

### 6. Ordering information

#### Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMEG6020ELR		plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W			

### 7. Marking

Table 4. Marking codes					
Type number	Marking code				
PMEG6020ELR	К2				

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	60	V
l <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> = 155 °C		-	2.83	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; T <sub>amb</sub> $\leq$ 90 °C; square wave	[1]	-	2	A
		$\delta$ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 160 °C; square wave		-	2	A
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> = 8 ms; square wave; T <sub>j(init)</sub> = 25 °C		-	50	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	680	mW
			[3]	-	1150	mW
			[1]	-	2140	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from	[1]	[1] [2]	-	-	220	K/W
	junction to ambient		[1] [3]	-	-	130	K/W
			[1] [4]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	18	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

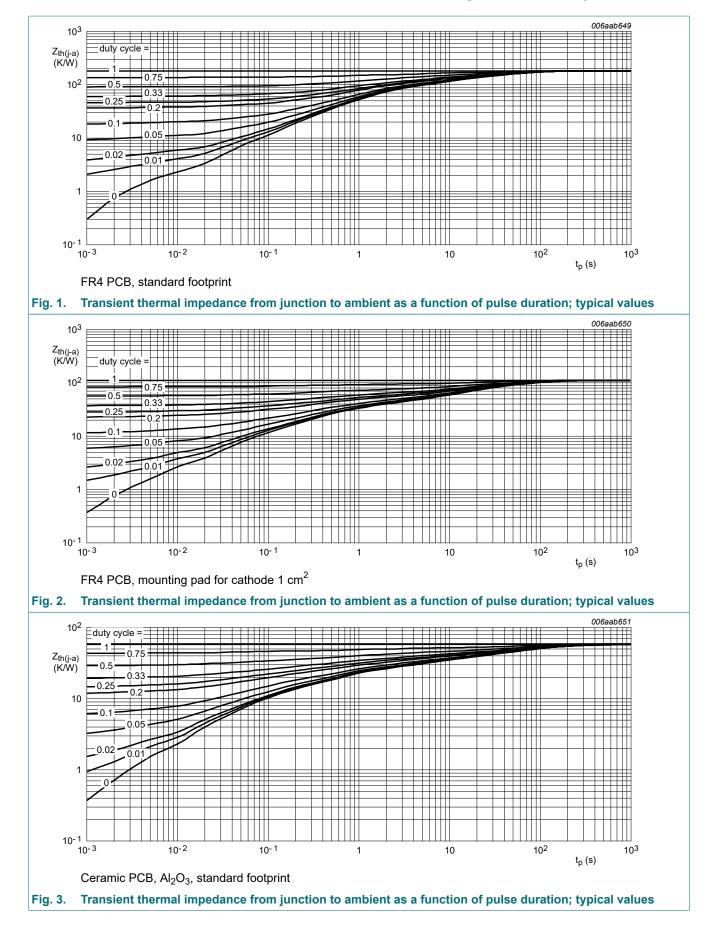
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[5] Soldering point of cathode tab.

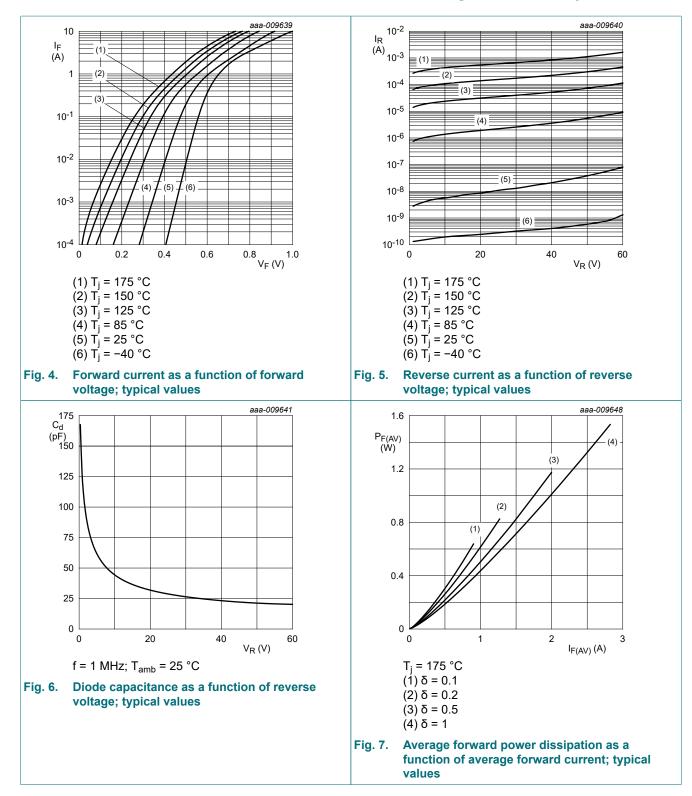
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### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>j</sub> = 25 °C	60	-	-	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	475	540	mV
		I <sub>F</sub> = 0.5 A; T <sub>j</sub> = 25 °C	-	550	605	mV
		I <sub>F</sub> = 0.7 A; T <sub>j</sub> = 25 °C	-	575	625	mV
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	605	660	mV
		I <sub>F</sub> = 1.6 A; T <sub>j</sub> = 25 °C	-	660	720	mV
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 25 °C	-	690	760	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	5	-	nA
		V <sub>R</sub> = 10 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	6	-	nA
		V <sub>R</sub> = 40 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	25	50	nA
		V <sub>R</sub> = 60 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	90	300	nA
		V <sub>R</sub> = 10 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 125 °C; pulsed	-	25	-	μA
		V <sub>R</sub> = 60 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 125 °C; pulsed	-	120	-	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	110	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	65	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	45	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	4.5	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 0.5 A; dI <sub>F</sub> /dt = 20 A/μs; T <sub>j</sub> = 25 °C	-	580	-	mV

**Product data sheet** 

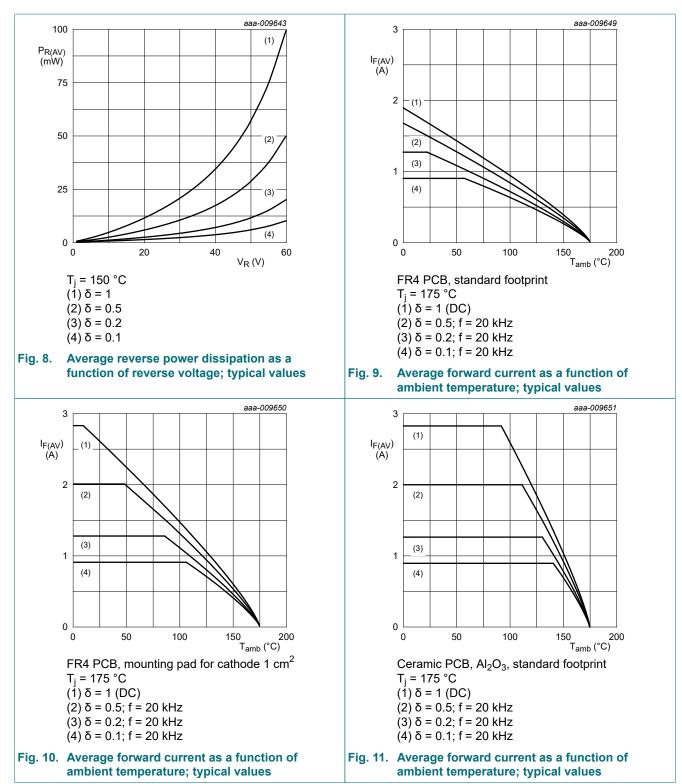


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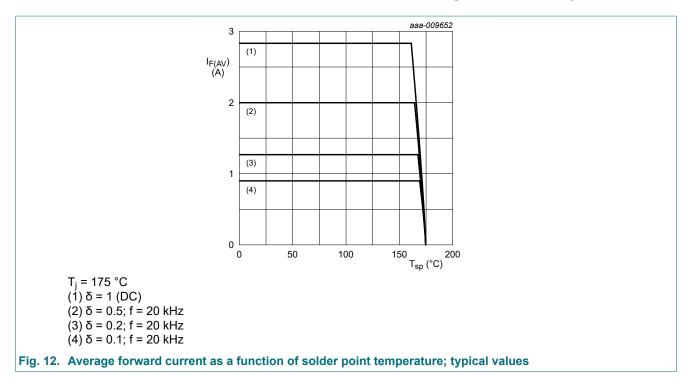
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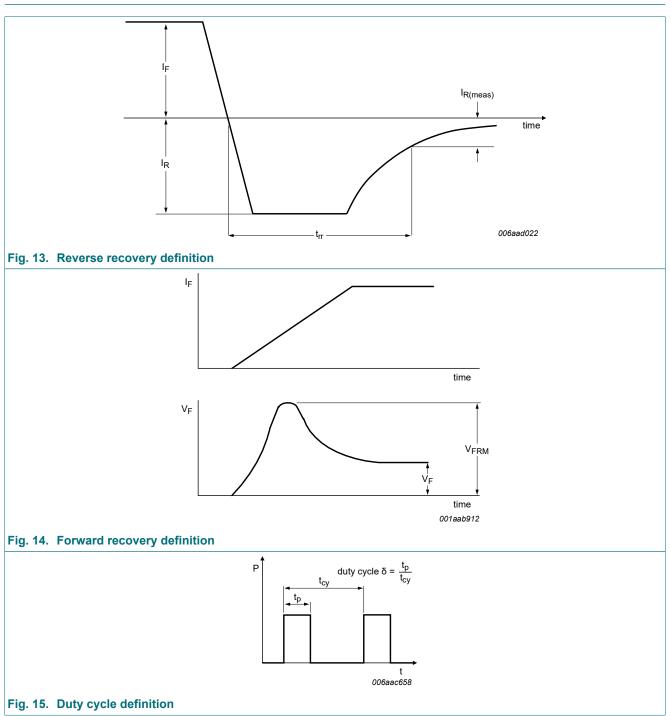


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### **11. Test information**

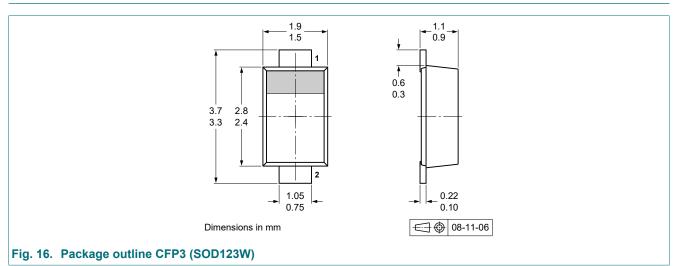


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

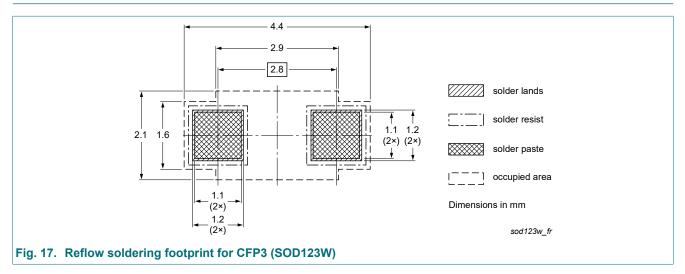
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

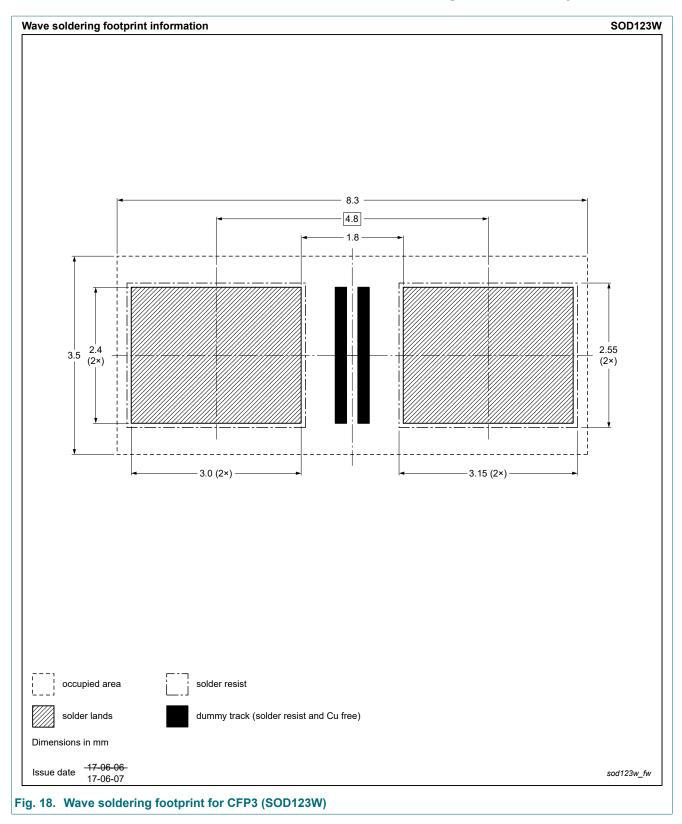
### 12. Package outline



### 13. Soldering



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### 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG6020ELR v.4	20190228	Product data sheet	-	PMEG6020ELR v.3			
Modifications:		<ul><li>Features and benefits: Capable for reflow and wave soldering added.</li><li>Soldering: Wave soldering footprint added.</li></ul>					
PMEG6020ELR v.3	20160908	Product data sheet	-	PMEG6020ELR v.2			
PMEG6020ELR v.2	20140603	Product data sheet	-	PMEG6020ELR v.1			
PMEG6020ELR v.1	20131108	Preliminary data sheet	-	-			

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### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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