



PMEG60T20ELP

60 V, 2 A low leakage current Trench MEGA Schottky barrier rectifier

24 May 2018

Product data sheet

1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP5 (SOD128) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 2$ A
- Reverse voltage: $V_R \leq 60$ V
- Low forward voltage
- Low leakage current due to Trench MEGA Schottky technology
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package
- Capable for reflow and wave soldering
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data



Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20$ kHz; $T_{sp} \leq 159$ °C; square wave		-	-	2	A
V_R	reverse voltage	$T_j = 25$ °C		-	-	60	V
V_F	forward voltage	$I_F = 2$ A; pulsed; $T_j = 25$ °C	[1]	-	550	620	mV
I_R	reverse current	$V_R = 10$ V; pulsed; $T_j = 25$ °C	[1]	-	0.08	0.6	μ A
		$V_R = 60$ V; pulsed; $T_j = 25$ °C	[1]	-	0.2	1.2	μ A

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 CFP5 (SOD128)	 sym001
2	A	anode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG60T20ELP	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG60T20ELP	E1

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	$T_j = 25\text{ °C}$		-	60	V
I_F	forward current	$\delta = 1; T_{sp} \leq 154\text{ °C}$		-	2.8	A
$I_{F(AV)}$	average forward current	$\delta = 0.5; f = 20\text{ kHz}; T_{sp} \leq 159\text{ °C};$ square wave		-	2	A
I_{FSM}	non-repetitive peak forward current	$t_p = 8\text{ ms};$ square wave; $T_{j(\text{init})} = 25\text{ °C}$		-	40	A
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	0.75	W
			[2]	-	1.2	W
T_j	junction temperature			-	175	°C
T_{amb}	ambient temperature			-55	175	°C
T_{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	200	K/W
			[1] [3]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	12	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

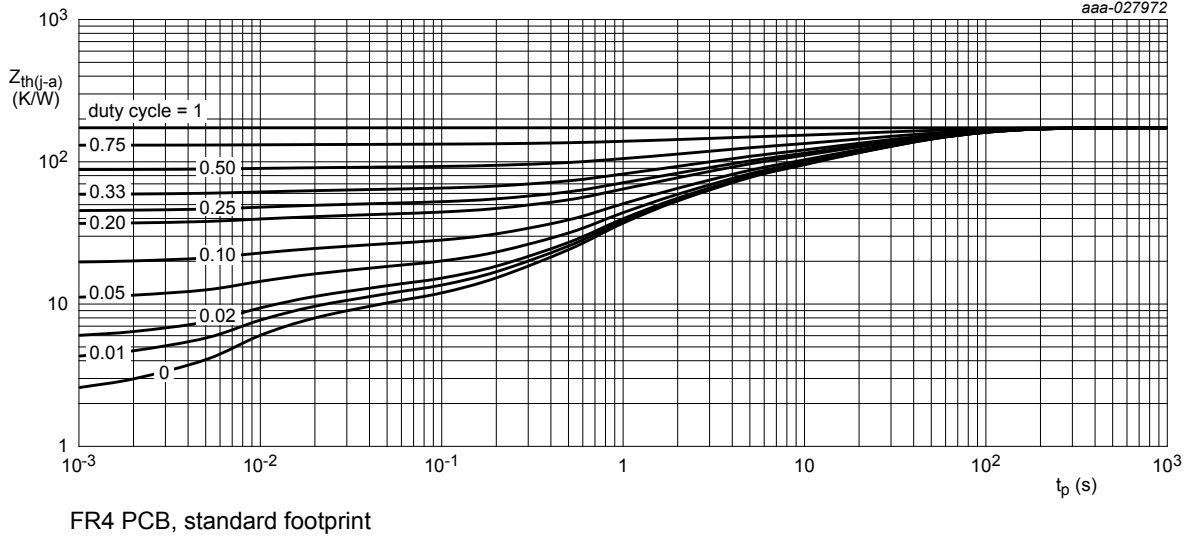


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

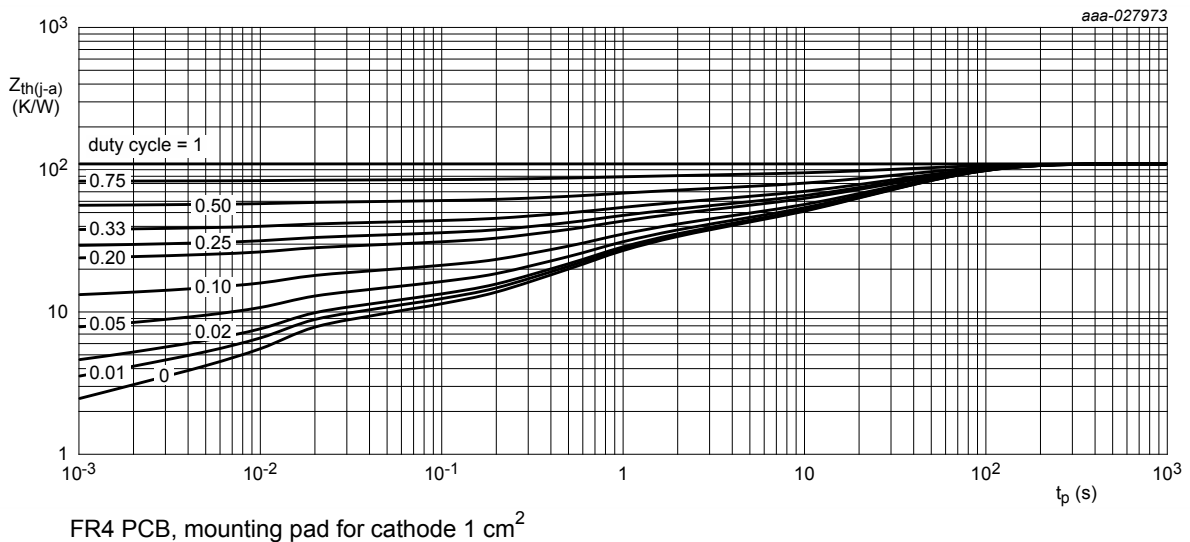


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

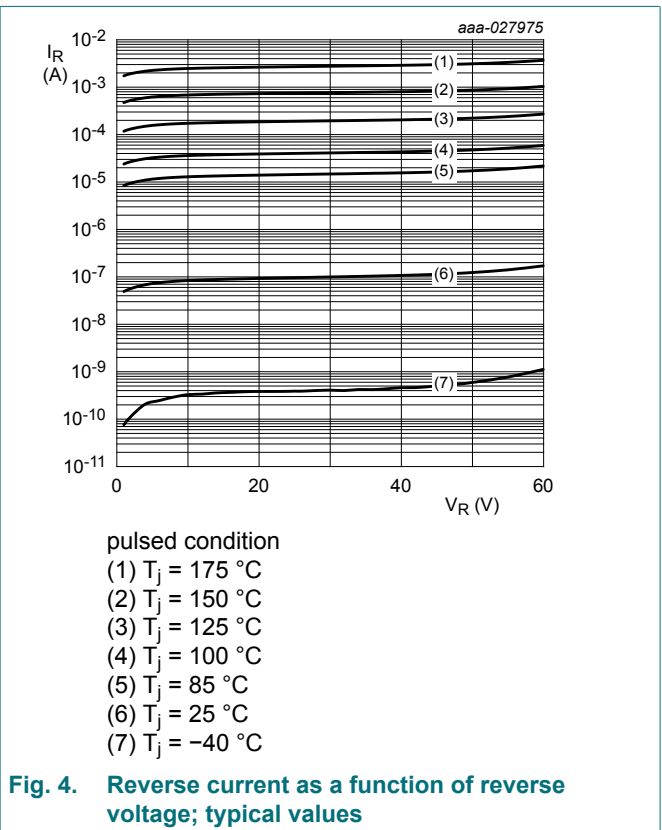
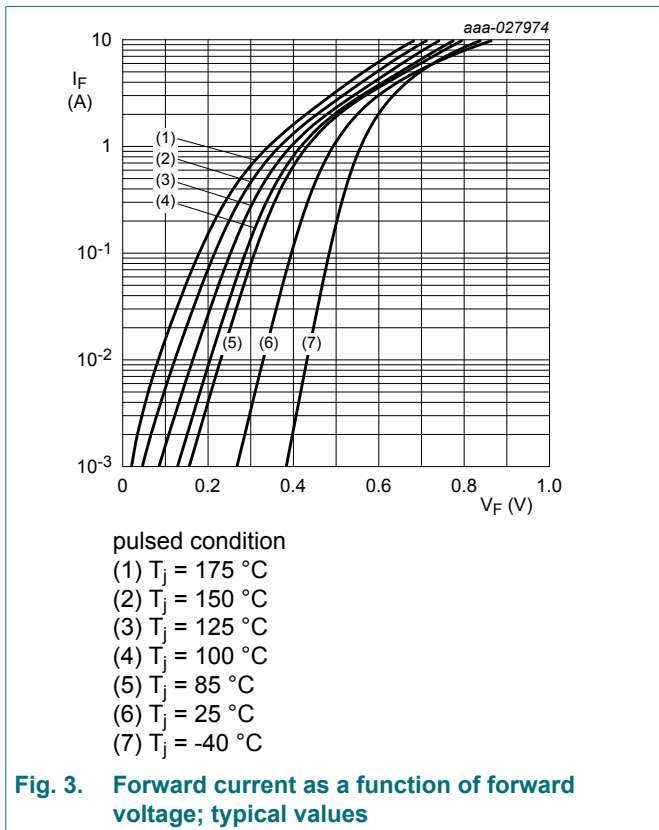
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 1 \text{ mA}$; pulsed; $T_j = 25 \text{ }^\circ\text{C}$	[1]	60	-	-	V

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Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
V _F	forward voltage	I _F = 0.1 A; pulsed; T _j = 25 °C	[1]	-	400	460	mV
		I _F = 0.5 A; pulsed; T _j = 25 °C	[1]	-	460	520	mV
		I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	495	560	mV
		I _F = 2 A; pulsed; T _j = 25 °C	[1]	-	550	620	mV
		I _F = 2 A; pulsed; T _j = -40 °C	[1]	-	605	-	mV
		I _F = 2 A; pulsed; T _j = 125 °C	[1]	-	475	-	mV
I _R	reverse current	V _R = 10 V; pulsed; T _j = 25 °C	[1]	-	0.08	0.6	μA
		V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	0.12	-	μA
		V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	0.2	1.2	μA
		V _R = 60 V; pulsed; T _j = 125 °C	[1]	-	0.3	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	400	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	120	-	pF
t _{rr}	reverse recovery time step recovery	I _F = 0.5 A; I _R = 0.5 A; I _{R(meas)} = 0.1 A; T _j = 25 °C		-	11	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/μs; I _F = 6 A; V _R = 26 V; T _j = 25 °C		-	14	-	ns
V _{FRM}	peak forward recovery voltage	I _F = 0.5 A; dI _F /dt = 20 A/μs; T _j = 25 °C		-	500	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



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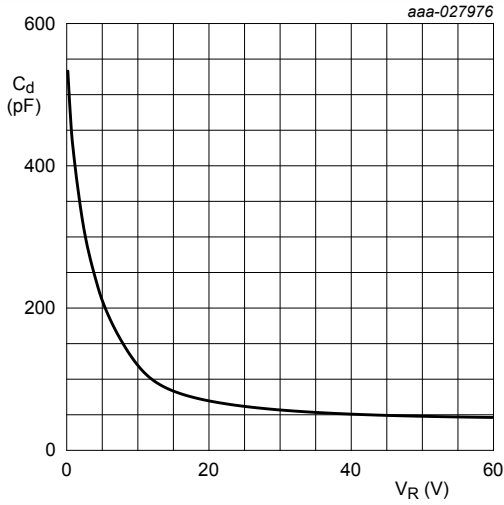


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

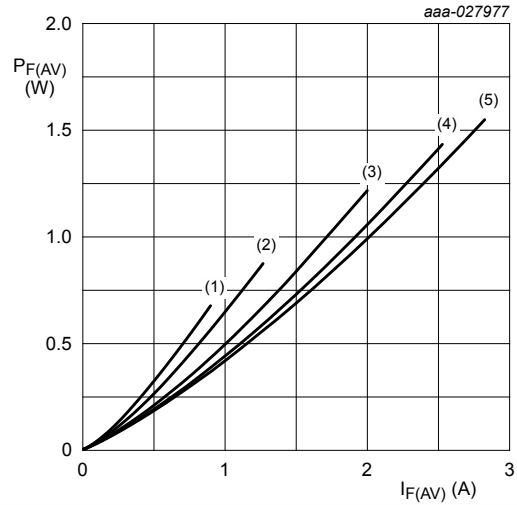


Fig. 6. Average forward power dissipation as a function of average forward current; typical values

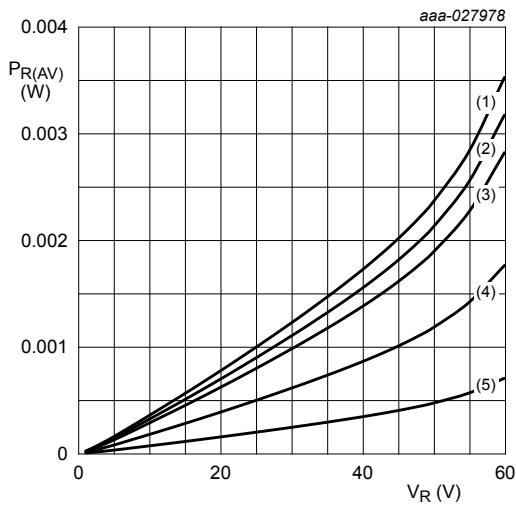


Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values

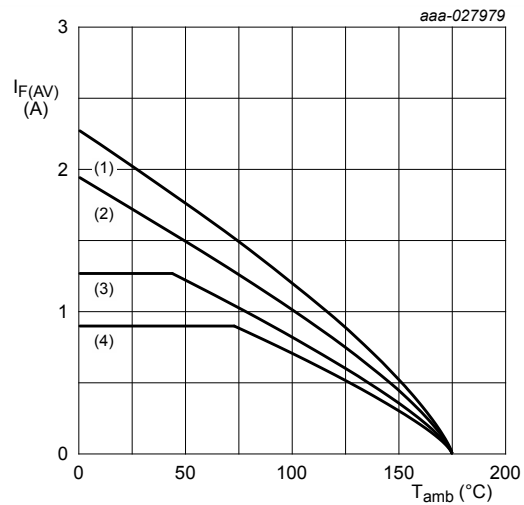
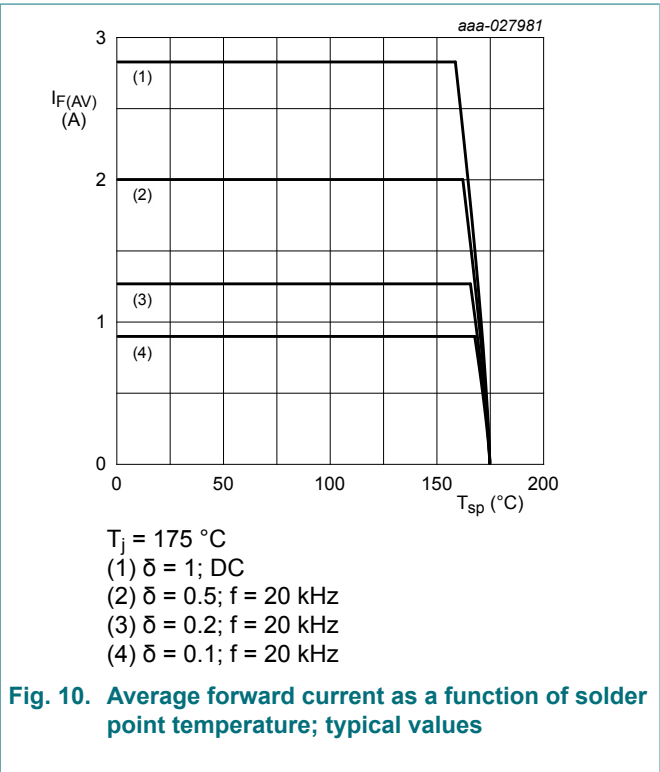
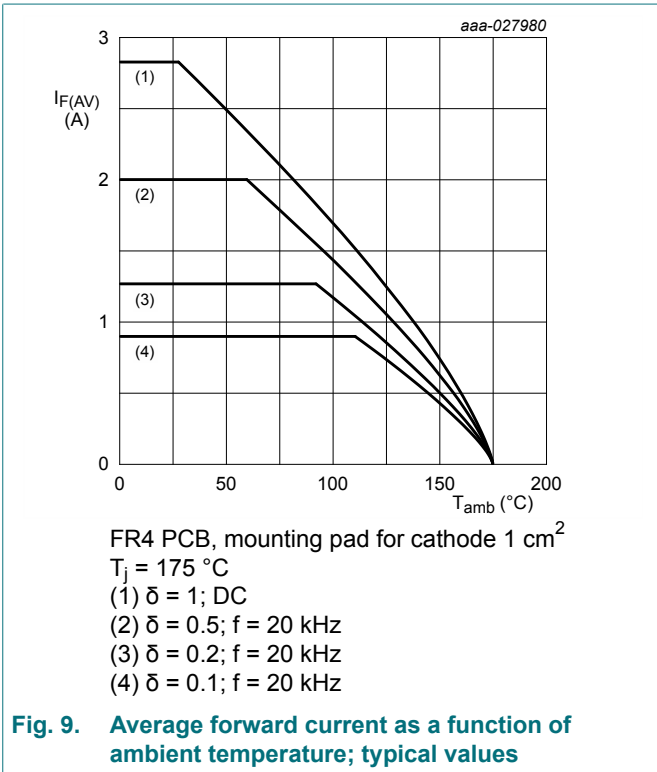
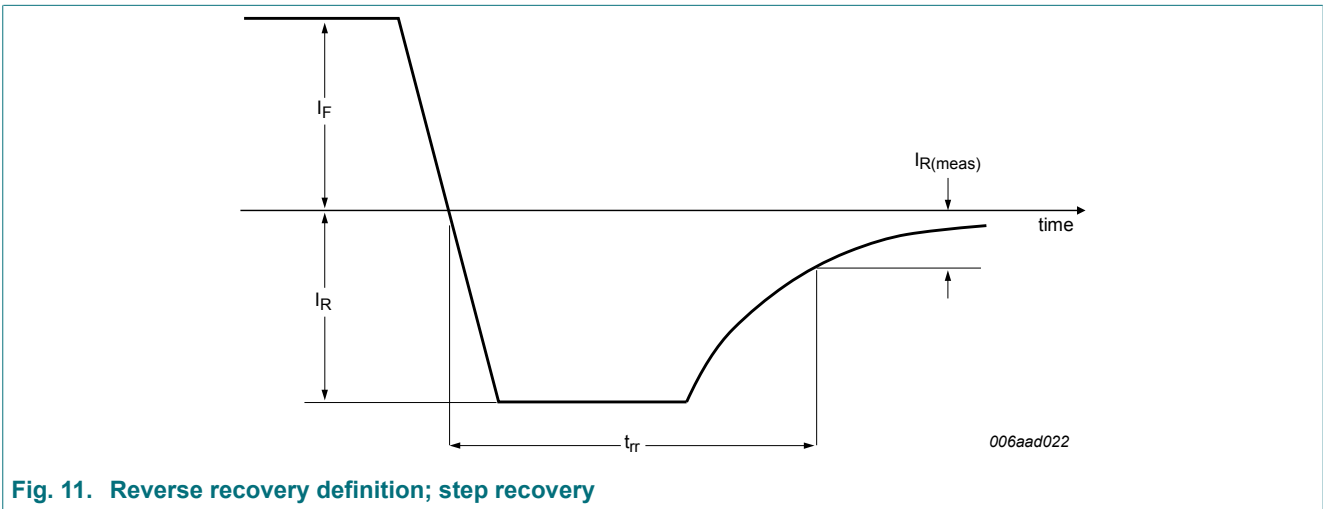


Fig. 8. Average forward current as a function of ambient temperature; typical values



11. Test information



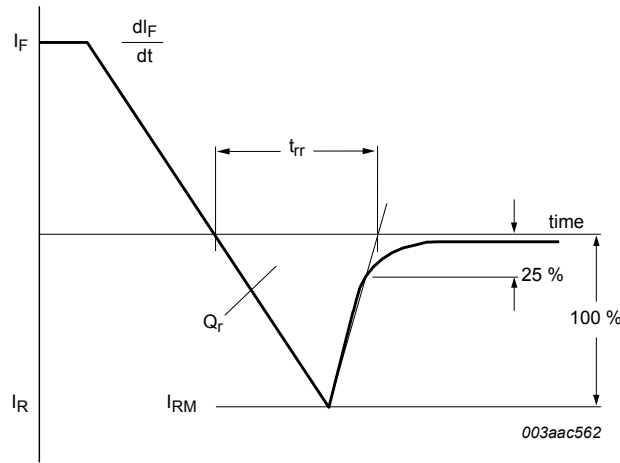


Fig. 12. Reverse recovery definition; ramp recovery

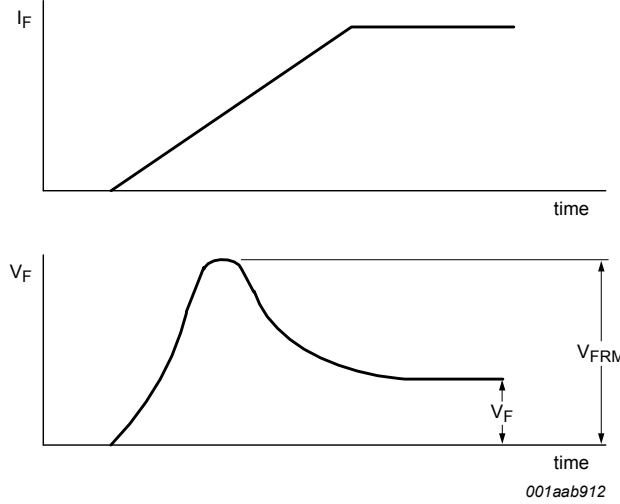


Fig. 13. Forward recovery definition

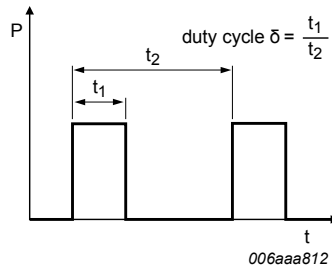


Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$$I_{F(AV)} = I_M \times \delta \text{ with } I_M \text{ defined as peak current,}$$

$$I_{RMS} = I_{F(AV)} \text{ at DC, and } I_{RMS} = I_M \times \sqrt{\delta}$$

with I_{RMS} defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline

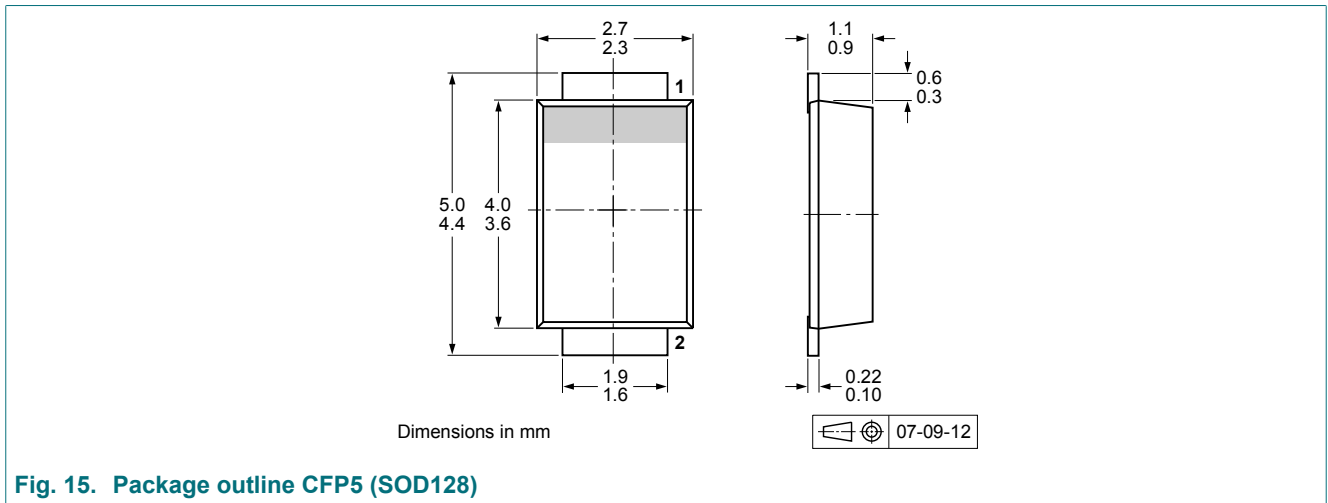


Fig. 15. Package outline CFP5 (SOD128)

13. Soldering

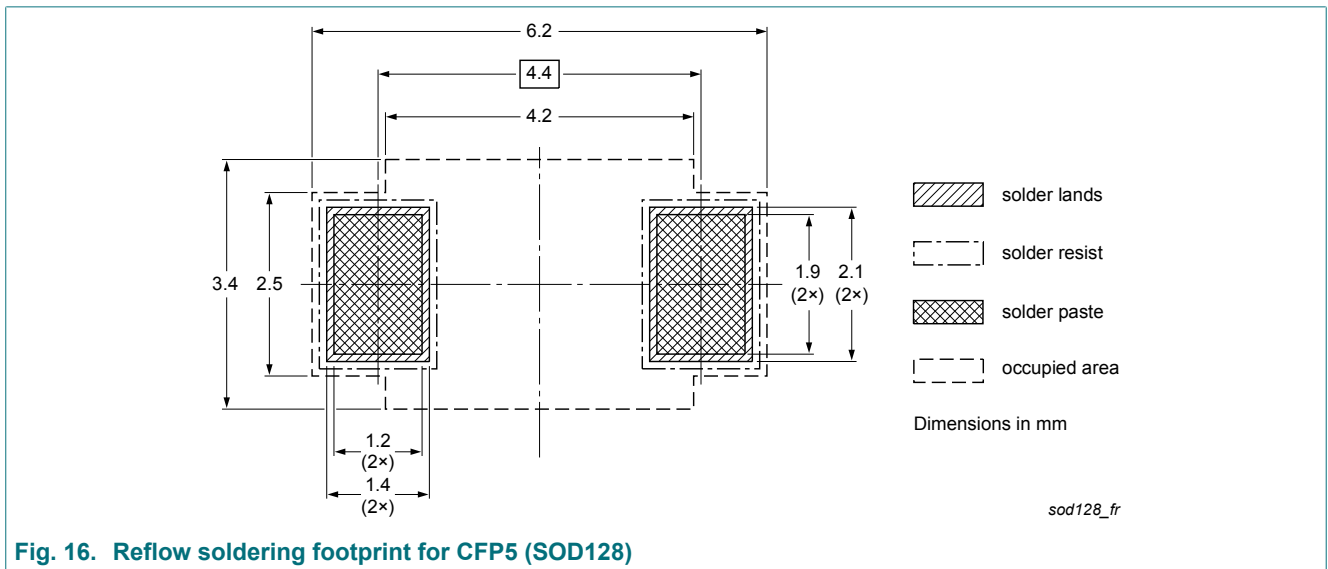


Fig. 16. Reflow soldering footprint for CFP5 (SOD128)

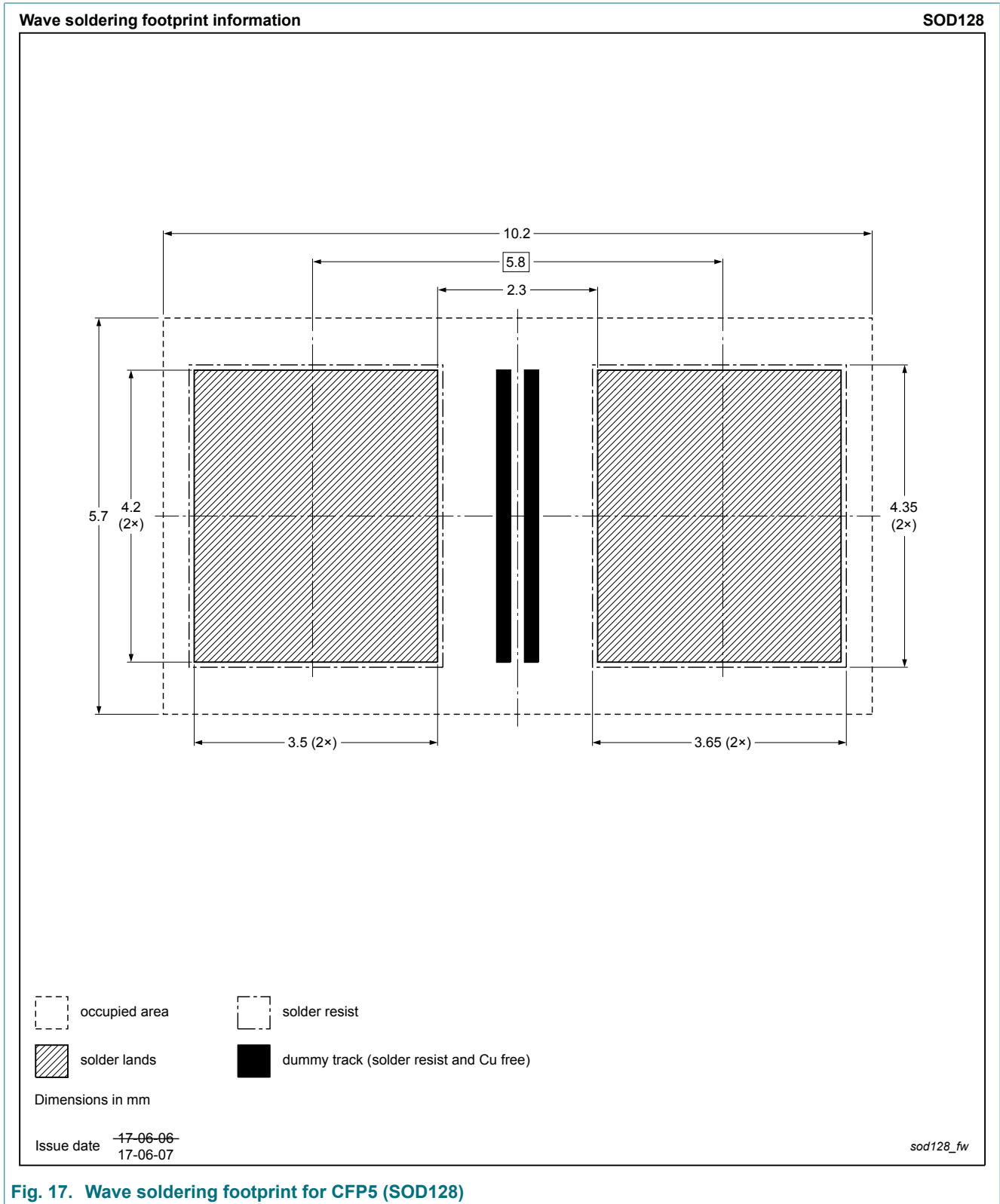


Fig. 17. Wave soldering footprint for CFP5 (SOD128)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG60T20ELP v.2	20180524	Product data sheet	-	PMEG60T20ELP v.1
Modifications:	<ul style="list-style-type: none">Product status changed			
PMEG60T20ELP v.1	20180227	Preliminary data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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