

20 V, P-channel Trench MOSFET

21 March 2014

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Fast switching
- Trench MOSFET technology
- 2 kV ESD protection
- AEC-Q101 qualified

3. Applications

- Relay driver
- High-speed line driver
- High-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-5.7	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -3 A; T _j = 25 °C		-	41	46	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain		
3	G	gate		G (T
4	S	source	TSOP6 (SOT457)	
5	D	drain		
6	D	drain		S 017aaa259

6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PMN42XPEA	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMN42XPEA	В9

8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-12	12	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-5.7	А
		V_{GS} = -4.5 V; T_{amb} = 25 °C	[1]	-	-4	А
		V_{GS} = -4.5 V; T_{amb} = 100 °C	[1]	-	-2.9	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-16	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	500	mW
			[1]	-	1310	mW
		T _{sp} = 25 °C		-	8330	mW

PMN42XPEA

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Symbol	Parameter	Conditions		Min	Max	Unit
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode	·	· · · ·			
l _S	source current	T _{amb} = 25 °C	[1]	-	-1.4	А
ESD maxim	um rating		'			
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	2000	V

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.

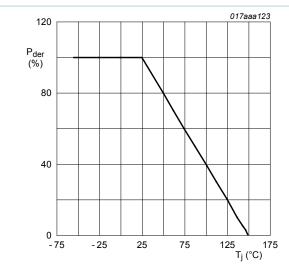


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

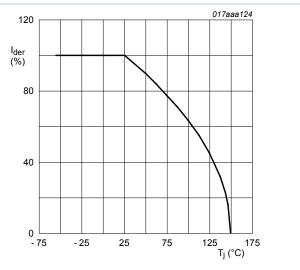
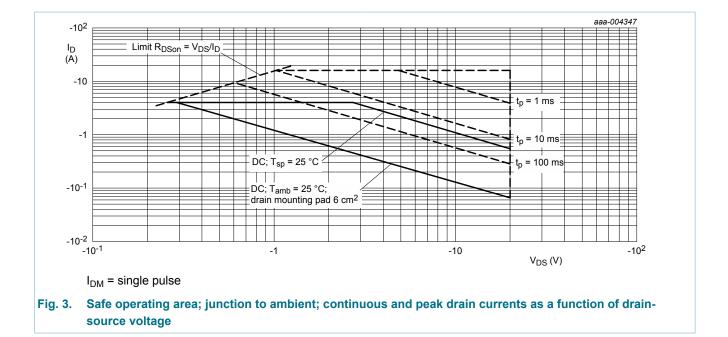


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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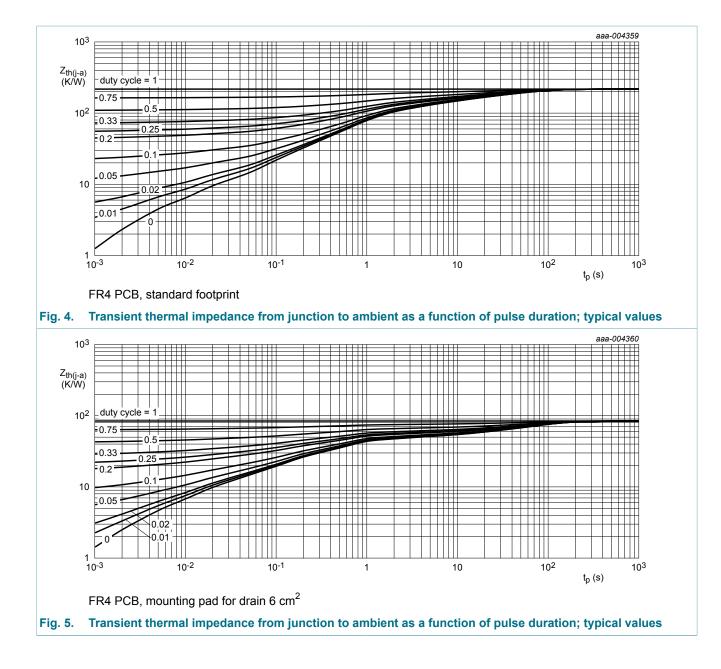
9. Thermal characteristics

Table 6. T	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
fror	thermal resistance	-	[1]	-	216	250	K/W
	from junction to ambient		[2]	-	83	95	K/W
	ambient	in free air; t ≤ 5 s	[2]	-	51	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	10	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

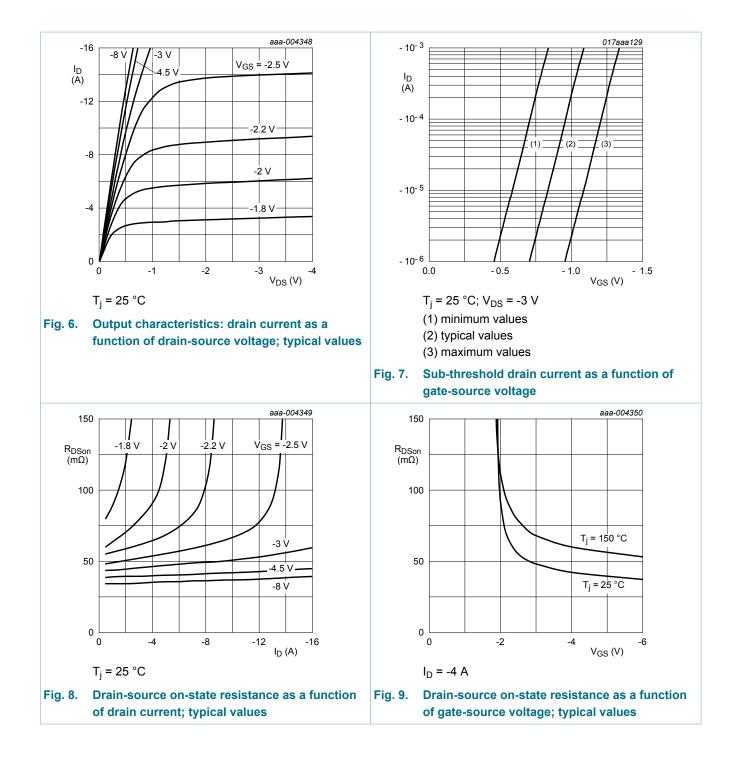
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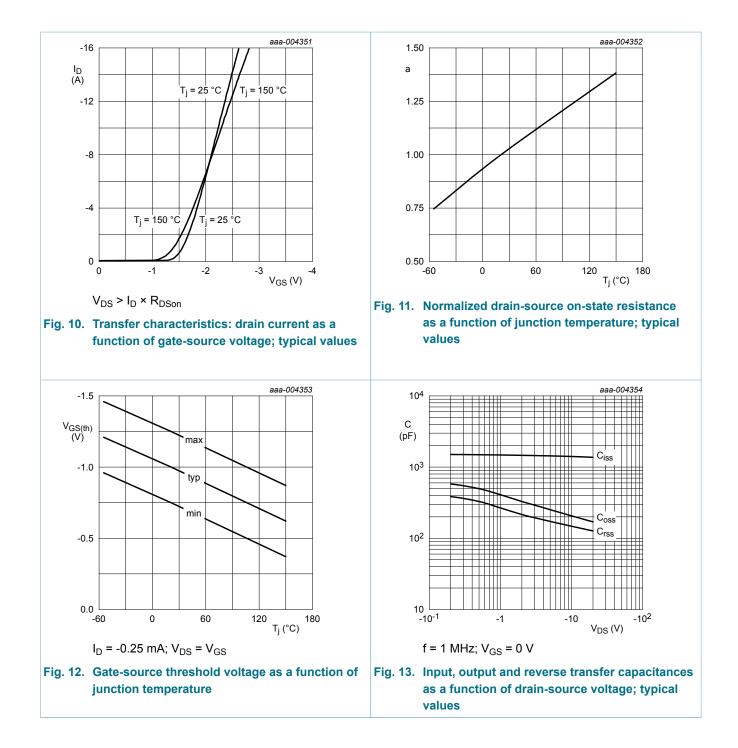
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · · · · · · · · · · · · · · · · ·				
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = -250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	-0.75	-1	-1.25	V
I _{DSS}	drain leakage current	V _{DS} = -20 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
		V_{DS} = -20 V; V_{GS} = 0 V; T_{amb} = 150 °C	-	-	-10	μA
I _{GSS}	gate leakage current	V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	10	μA
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
R _{DSon} drain-source on-state resistance	drain-source on-state	V _{GS} = -4.5 V; I _D = -3 A; T _j = 25 °C	-	41	46	mΩ
	V _{GS} = -4.5 V; I _D = -3 A; T _j = 150 °C	-	56	64	mΩ	
		V _{GS} = -2.5 V; I _D = -3 A; T _j = 25 °C	-	56	64	mΩ
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -4 A; T _j = 25 °C	-	12.5	-	S
Dynamic cl	naracteristics		I			
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I _D = -4 A; V _{GS} = -4.5 V;	-	11.5	17.3	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	2.7	-	nC
Q _{GD}	gate-drain charge		-	2.4	-	nC
C _{iss}	input capacitance	V _{DS} = -10 V; f = 1 MHz; V _{GS} = 0 V;	-	1410	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	207	-	pF
C _{rss}	reverse transfer capacitance		-	148	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I _D = -4 A; V _{GS} = -4.5 V;	-	17	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	27	-	ns
t _{d(off)}	turn-off delay time	1	-	33	-	ns
t _f	fall time		-	27	-	ns
Source-dra	in diode		1		1	
V _{SD}	source-drain voltage	I _S = -1.2 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.7	-1.2	V

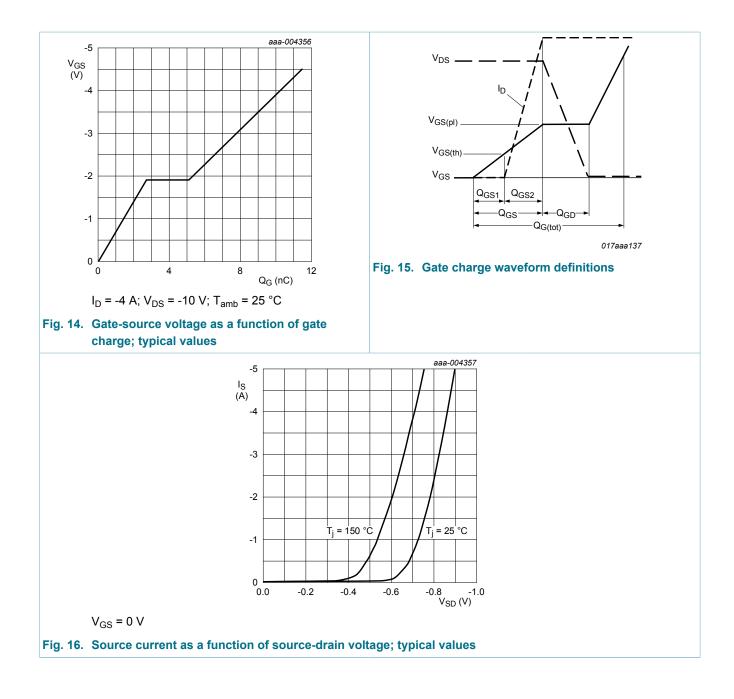
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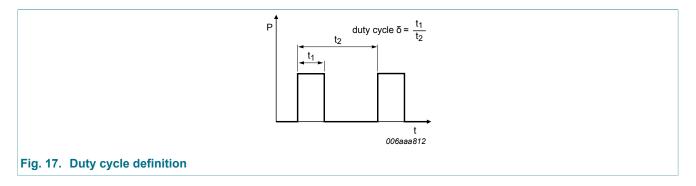
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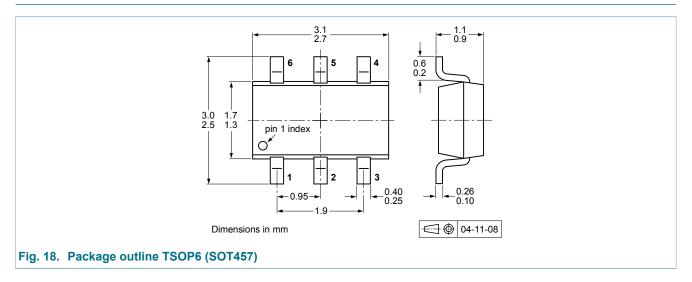
11. Test information



11.1 Quality information

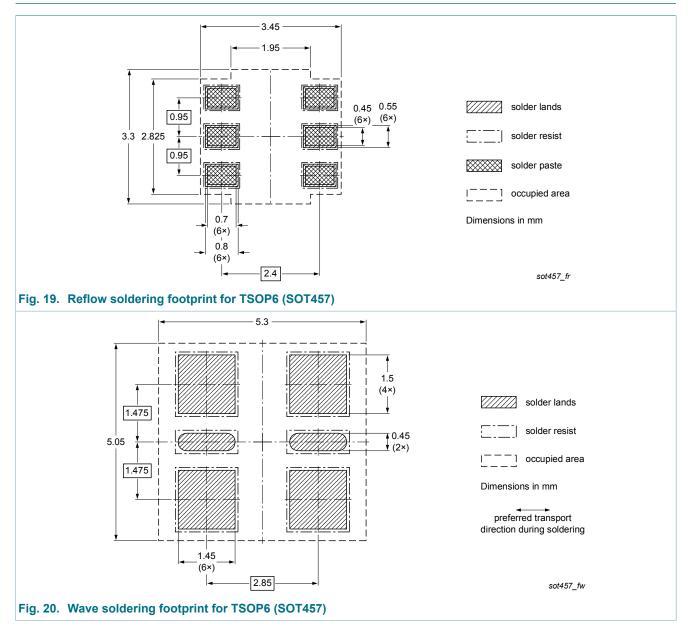
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMN42XPEA v.1	20140321	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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