Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- · Logic level compatible
- Very fast switching
- Trench MOSFET technology
- Enhanced power dissipation capability of 1115 mW

3. Applications

- Relay driver
- · High-speed line driver
- Low-side load switch
- · Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V_{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	5.6	Α
Static chara	acteristics					·	'
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 4.5 \text{ A}; T_j = 25 \text{ °C}$		-	31	36	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G P
			1 2	S 017aaa253
			SOT23	

6. Ordering information

Table 3. Ordering information

lable of ordering information								
Type number	Package	ackage						
	Name	Description	Version					
PMV37EN2		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23					

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMV37EN2	%K7

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	5.6	А
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	4.5	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	2.8	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	16	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			[1]	-	1115	mW
		T _{sp} = 25 °C		-	5000	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode					•
I _S	source current	T _{amb} = 25 °C	[1]	-	1	А

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

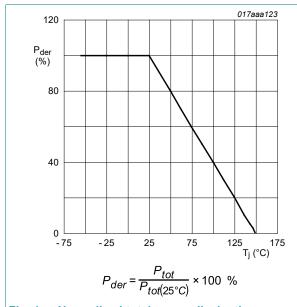


Fig. 1. Normalized total power dissipation as a function of junction temperature

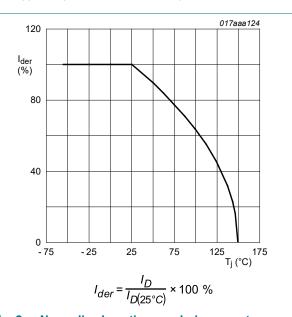


Fig. 2. Normalized continuous drain current as a function of junction temperature

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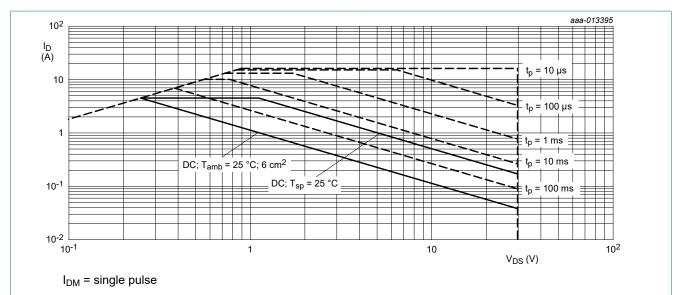


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

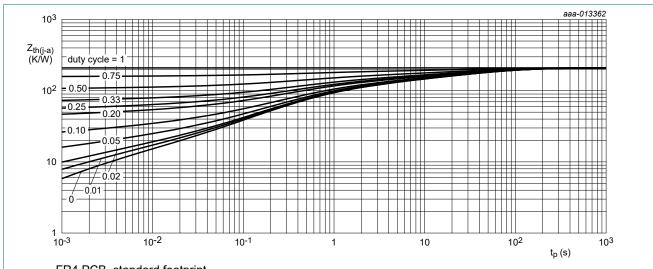
30 V, N-channel Trench MOSFET

9. Thermal characteristics

Table 6. Thermal characteristics

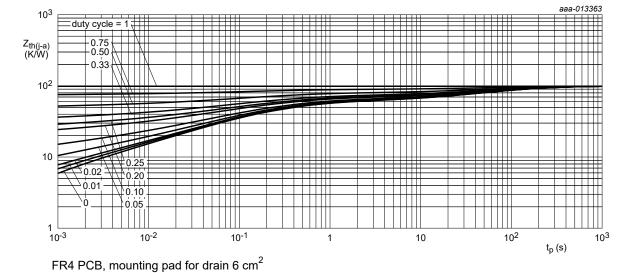
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	209	246	K/W
	junction to ambient		[2]	-	95	112	K/W
		in free air; t ≤ 5 s	[2]	-	63	72	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	20	25	K/W

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



FR4 PCB, standard footprint

Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 4.



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	1	1.5	2	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μΑ
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 4.5 A; T _j = 25 °C	-	31	36	mΩ
resistance	resistance	V _{GS} = 10 V; I _D = 4.5 A; T _j = 150 °C	-	48	56	mΩ
		$V_{GS} = 4.5 \text{ V}; I_D = 3.9 \text{ A}; T_j = 25 \text{ °C}$	-	37	47	mΩ
9 _{fs}	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 2 \text{ A}; T_j = 25 \text{ °C}$	-	13	-	S
R_G	gate resistance	f = 1 MHz; T _j = 25 °C	-	2.3	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = 15 V; I _D = 3.2 A; V _{GS} = 10 V;	-	3.6	6.3	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.5	-	nC
Q _{GD}	gate-drain charge		-	0.4	-	nC
C _{iss}	input capacitance	V _{DS} = 15 V; f = 1 MHz; V _{GS} = 0 V;	-	209	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	50	-	pF
C _{rss}	reverse transfer capacitance		-	17	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 15 V; I _D = 3.2 A; V _{GS} = 10 V;	-	3	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	12	-	ns
t _{d(off)}	turn-off delay time	1	-	11	-	ns
t _f	fall time		-	2	-	ns
Source-drai	in diode					
V _{SD}	source-drain voltage	I _S = 1 A; V _{GS} = 0 V; T _i = 25 °C	-	0.75	1.2	V

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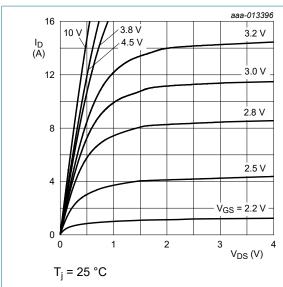


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

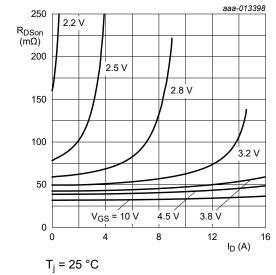


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

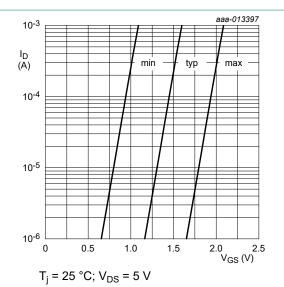


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

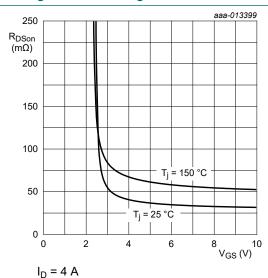


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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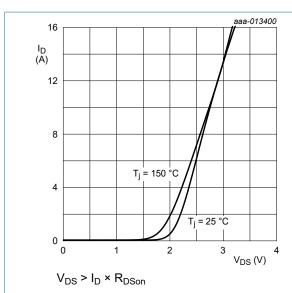


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

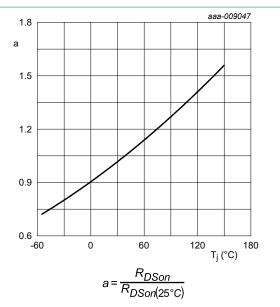


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

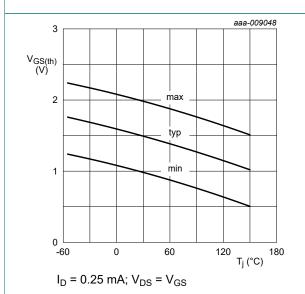
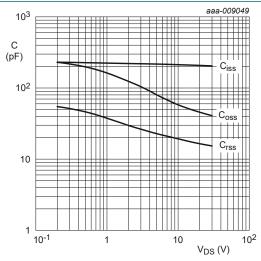


Fig. 12. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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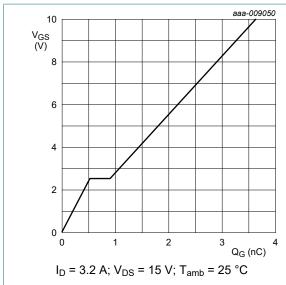


Fig. 14. Gate-source voltage as a function of gate charge; typical values

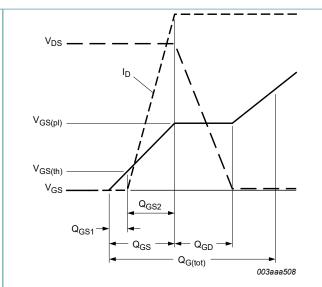


Fig. 15. Gate charge waveform definitions

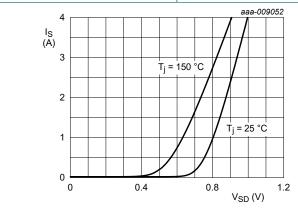
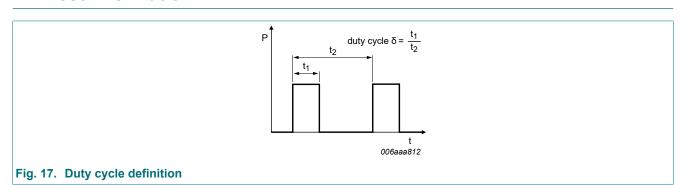


Fig. 16. Source current as a function of source-drain voltage; typical values

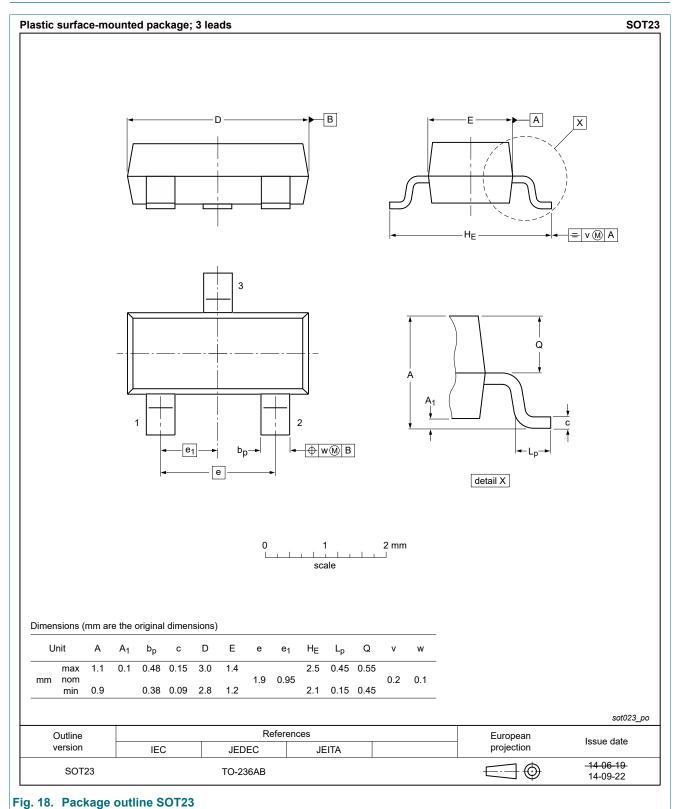
11. Test information

 $V_{GS} = 0 V$



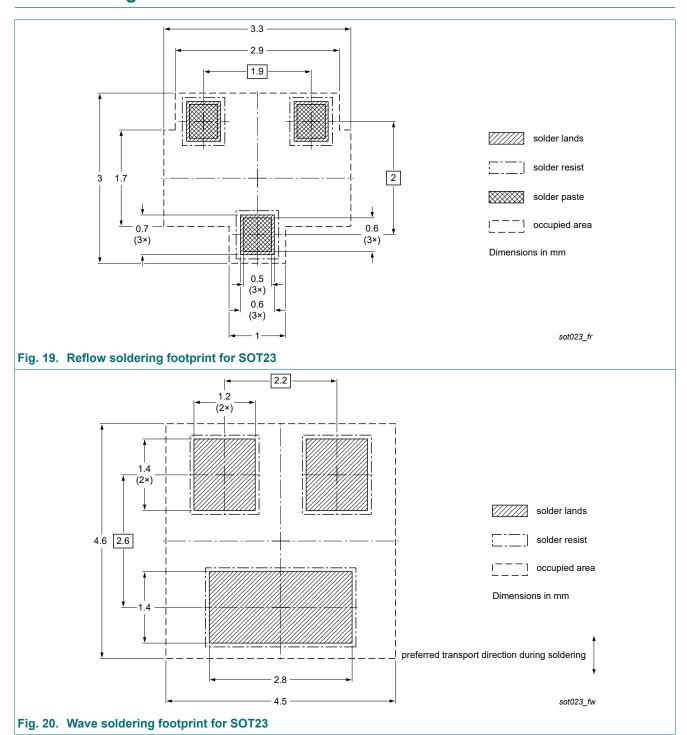
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12. Package outline



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13. Soldering



30 V, N-channel Trench MOSFET

14. Revision history

Table 8. Revision history

Table 6. Nevision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMV37EN2 v.3	20201027	Product data sheet	-	PMV37EN2 v.2			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
PMV37EN2 v.2	20170110	Product data sheet	-	PMV37EN2 v.1			
PMV37EN2 v.1	20140603	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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