



PMV42ENE

30 V, N-channel Trench MOSFET

16 March 2016

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic level compatible
- Low on-state resistance
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 1 kV HBM
- Enhanced power dissipation capability of 1 W

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

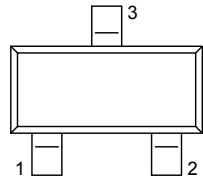
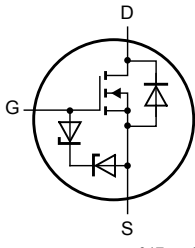
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_J = 25\text{ }^{\circ}\text{C}$		-	-	30	V
V_{GS}	gate-source voltage			-20	-	20	V
I_D	drain current	$V_{GS} = 10\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}; t \leq 5\text{ s}$	[1]	-	-	5.1	A
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10\text{ V}; I_D = 4.4\text{ A}; T_J = 25\text{ }^{\circ}\text{C}$		-	29	36	m Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 TO-236AB (SOT23)	 017aaa255
2	S	source		
3	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMV42ENE	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code
PMV42ENE	BL%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	5.1	A
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	4.4	A
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	2.7	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	18	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	500	mW
			[1]	-	1.04	W
		T _{sp} = 25 °C		-	5	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	1	A

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

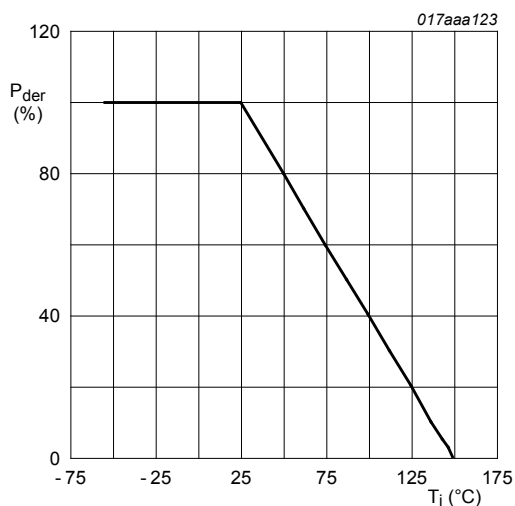


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ\text{C})}} \times 100 \%$$

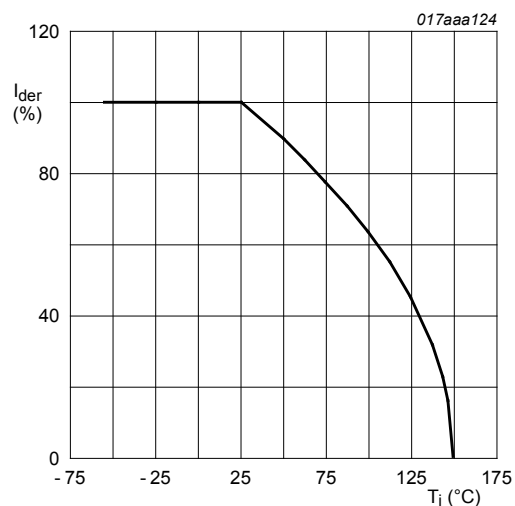


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^\circ\text{C})}} \times 100 \%$$

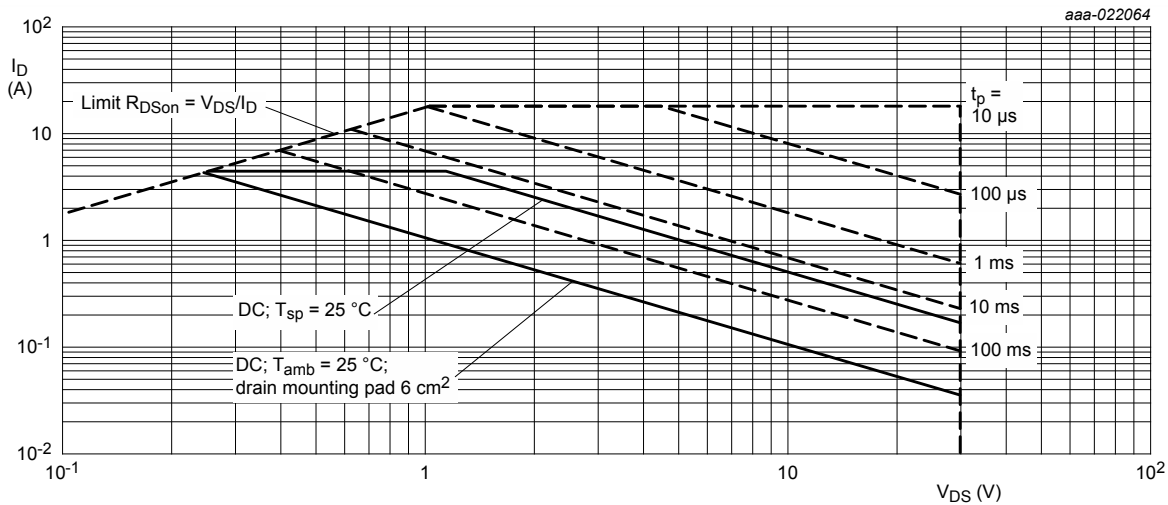


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	218	250	K/W
			[2]	-	105	120	K/W
		in free air; $t \leq 5$ s	[2]	-	72	83	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	20	25	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

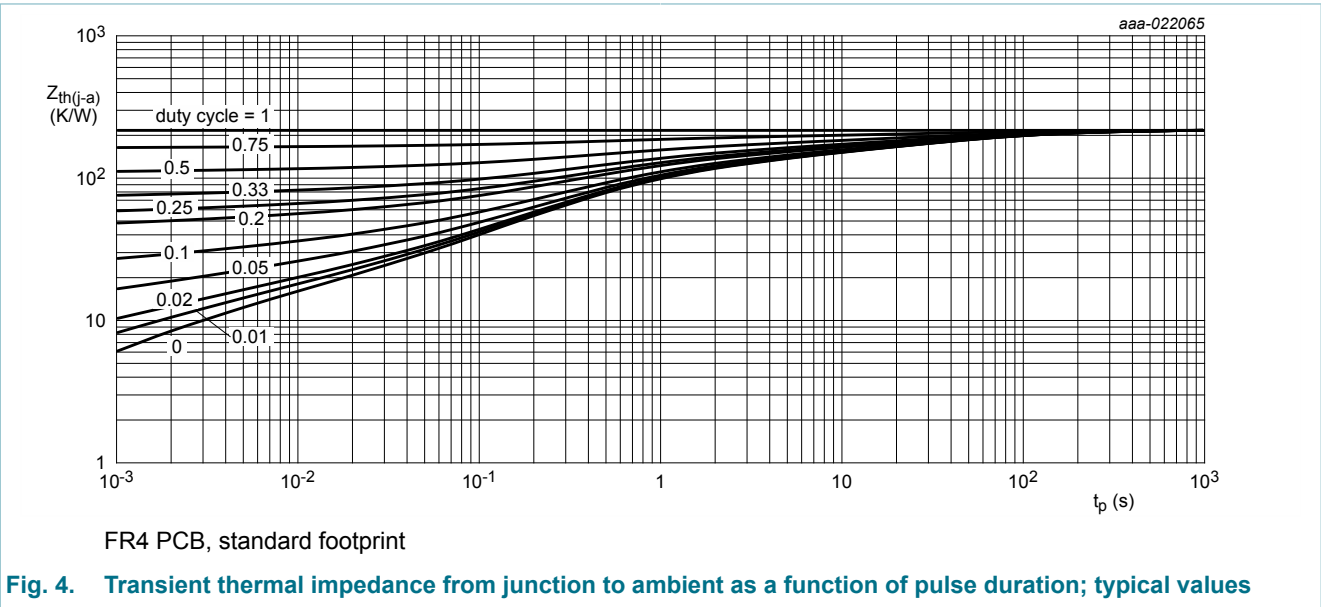
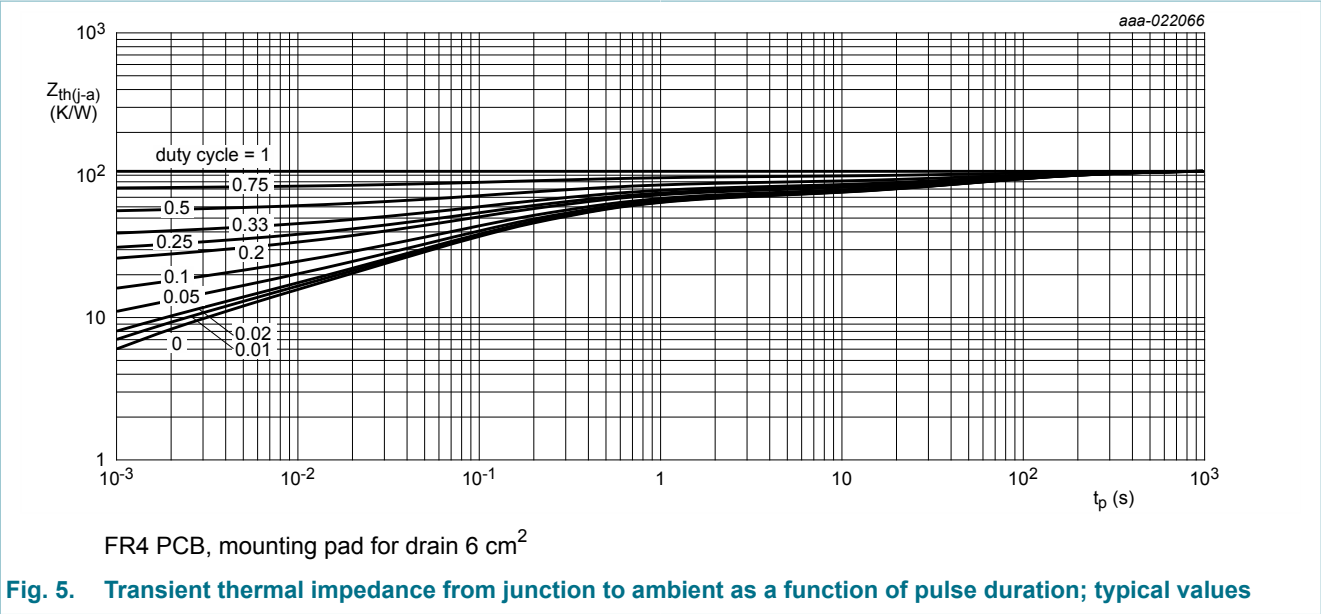


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C		30	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C		1	1.4	2	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 16 V; V _{DS} = 0 V; T _j = 25 °C		-	-	10	μA
		V _{GS} = -16 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-10	μA
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 4.4 A; T _j = 25 °C		-	29	36	mΩ
		V _{GS} = 10 V; I _D = 4.4 A; T _j = 150 °C		-	46	57	mΩ
		V _{GS} = 4.5 V; I _D = 3.5 A; T _j = 25 °C		-	42	54	mΩ
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 4.4 A; T _j = 25 °C		-	7	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C		-	1.5	-	Ω
Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = 15 V; I _D = 4.4 A; V _{GS} = 10 V; T _j = 25 °C		-	5.5	9	nC
Q _{GS}	gate-source charge			-	0.8	-	nC
Q _{GD}	gate-drain charge			-	1.1	-	nC
C _{iss}	input capacitance	V _{DS} = 15 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	281	-	pF
C _{oss}	output capacitance			-	42	-	pF
C _{rss}	reverse transfer capacitance			-	32	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 15 V; I _D = 4.4 A; V _{GS} = 10 V; R _{G(ext)} = 6 Ω; T _j = 25 °C		-	5	-	ns
t _r	rise time			-	18	-	ns
t _{d(off)}	turn-off delay time			-	9	-	ns
t _f	fall time			-	3	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 1 A; V _{GS} = 0 V; T _j = 25 °C		-	0.8	1.2	V

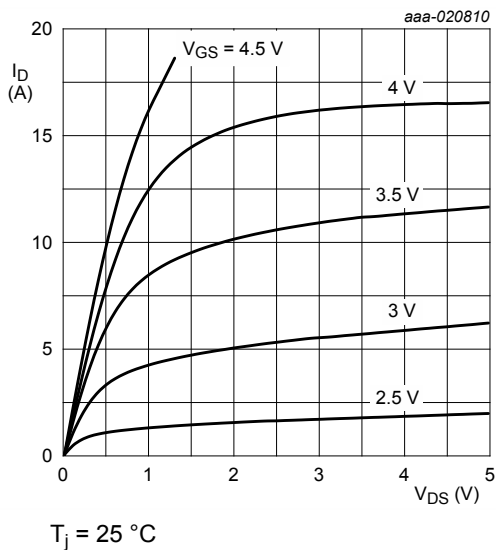


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

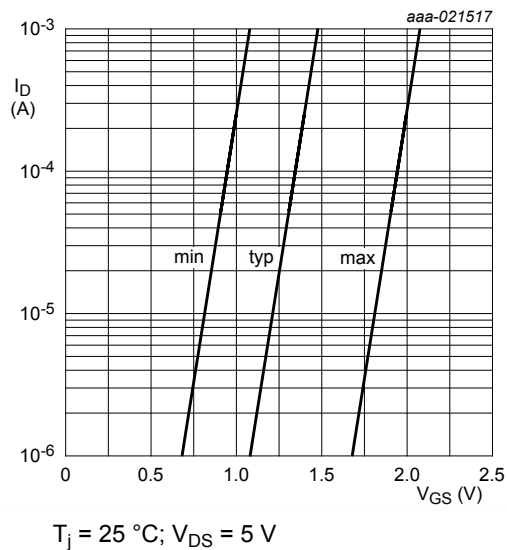


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

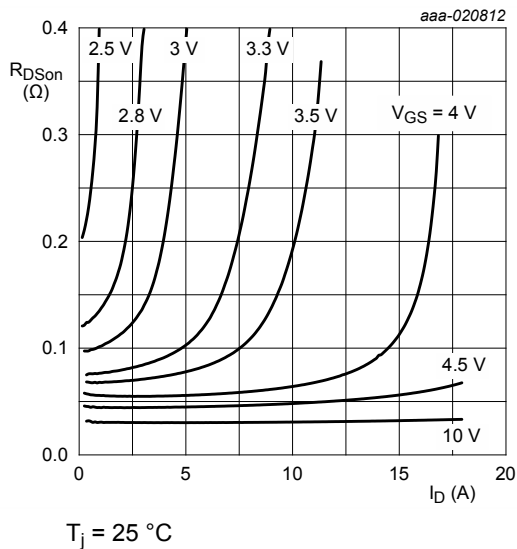


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

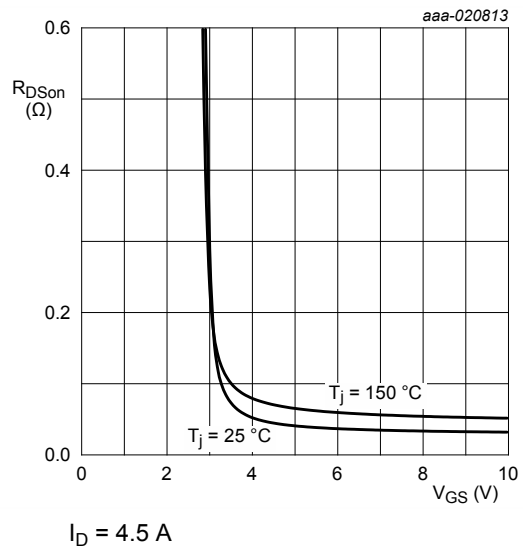


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

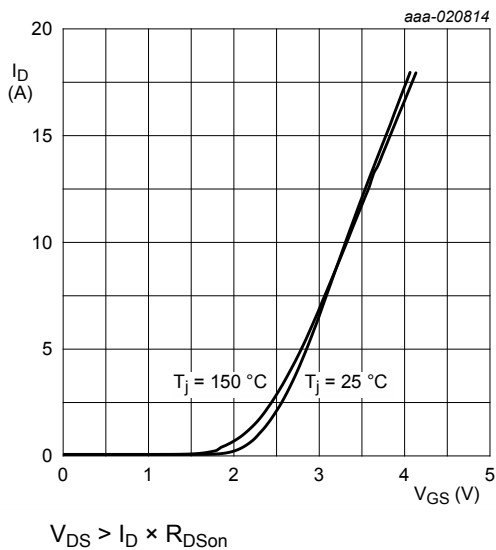


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

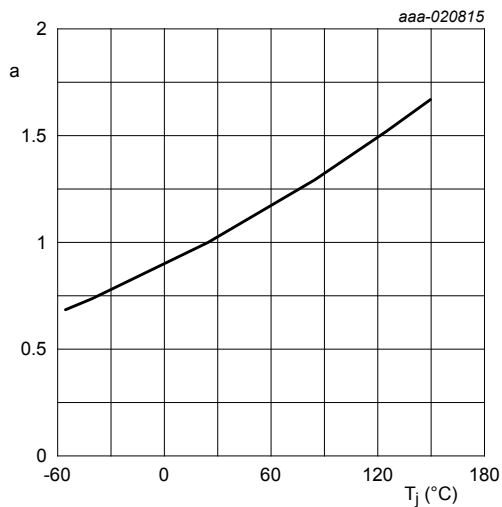


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}\text{C})}}$$

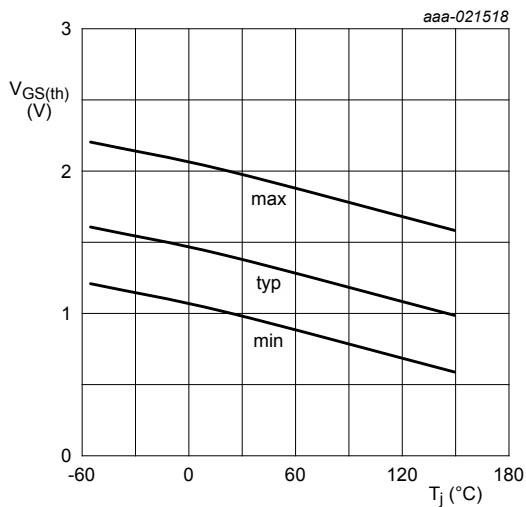


Fig. 12. Gate-source threshold voltage as a function of junction temperature

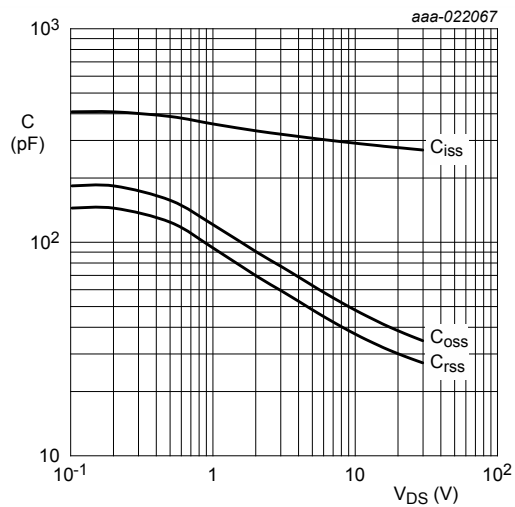


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

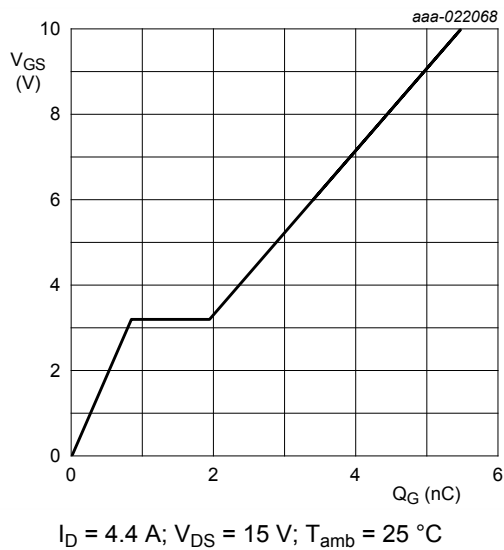


Fig. 14. Gate-source voltage as a function of gate charge; typical values

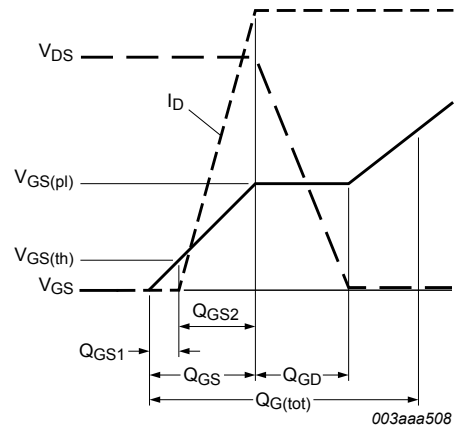
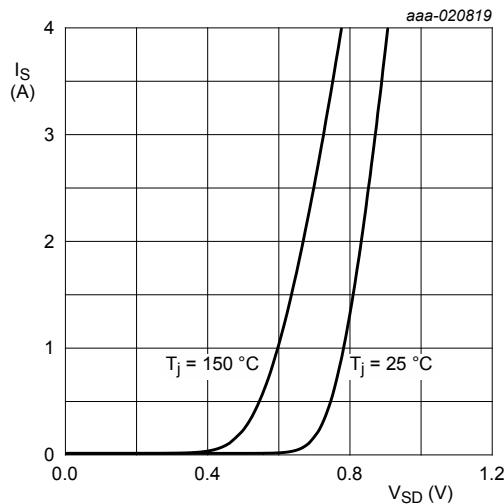


Fig. 15. MOSFET transistor: Gate charge waveform definitions



$V_{GS} = 0$ V

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

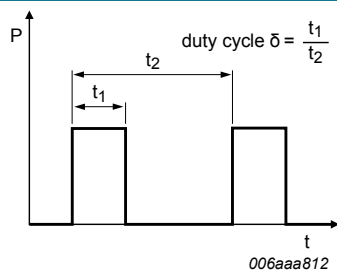


Fig. 17. Duty cycle definition

12. Package outline

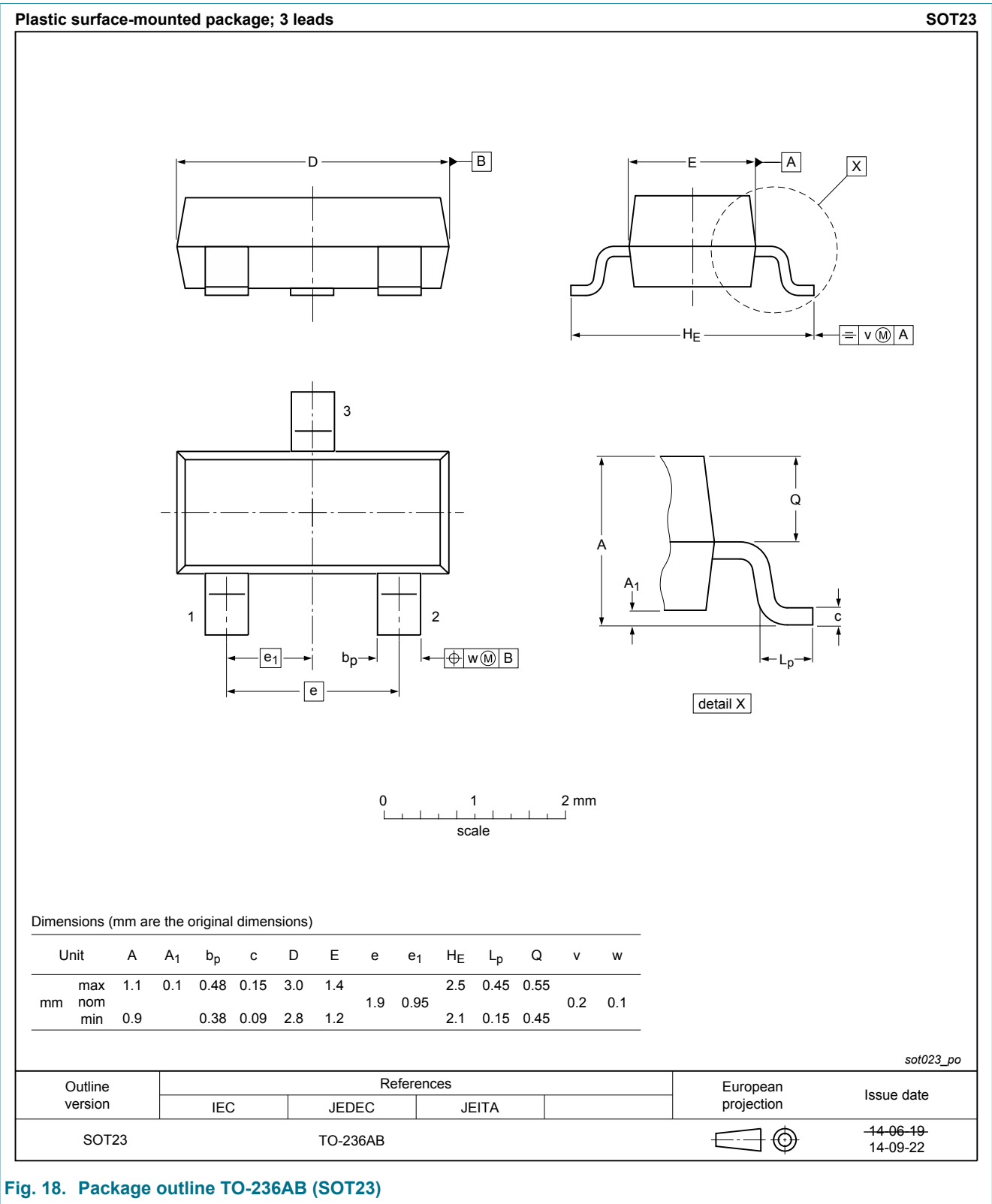


Fig. 18. Package outline TO-236AB (SOT23)

13. Soldering

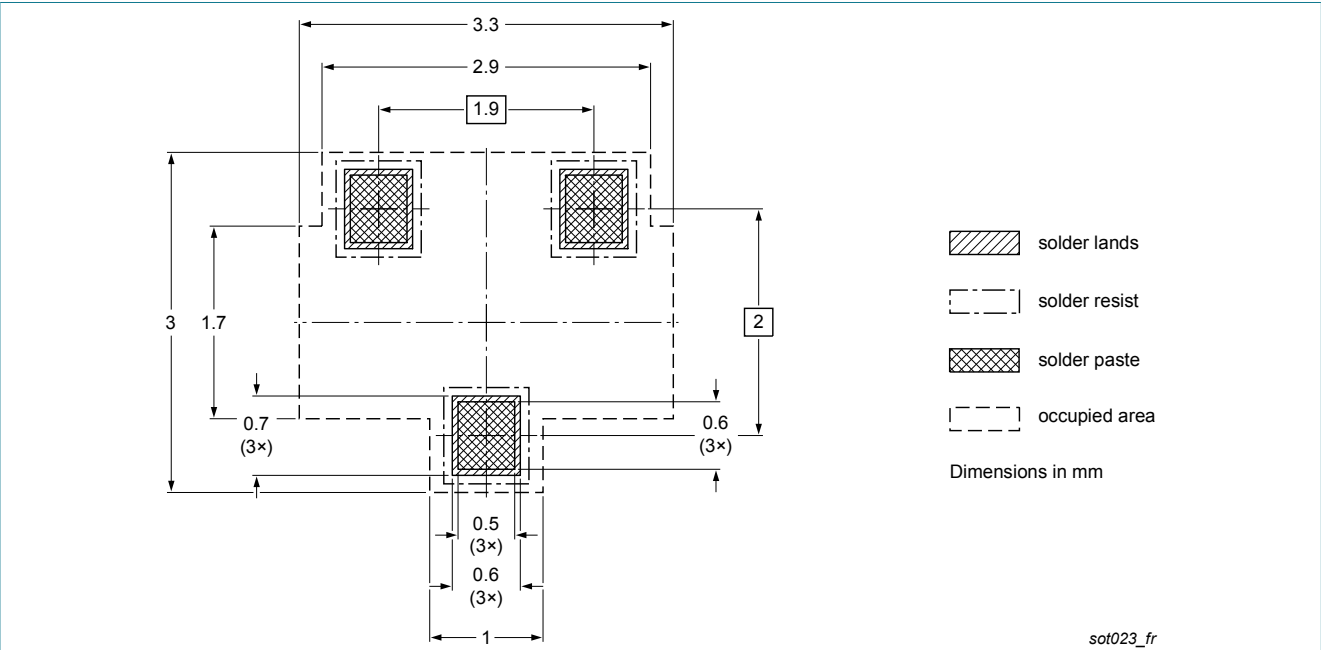


Fig. 19. Reflow soldering footprint for TO-236AB (SOT23)

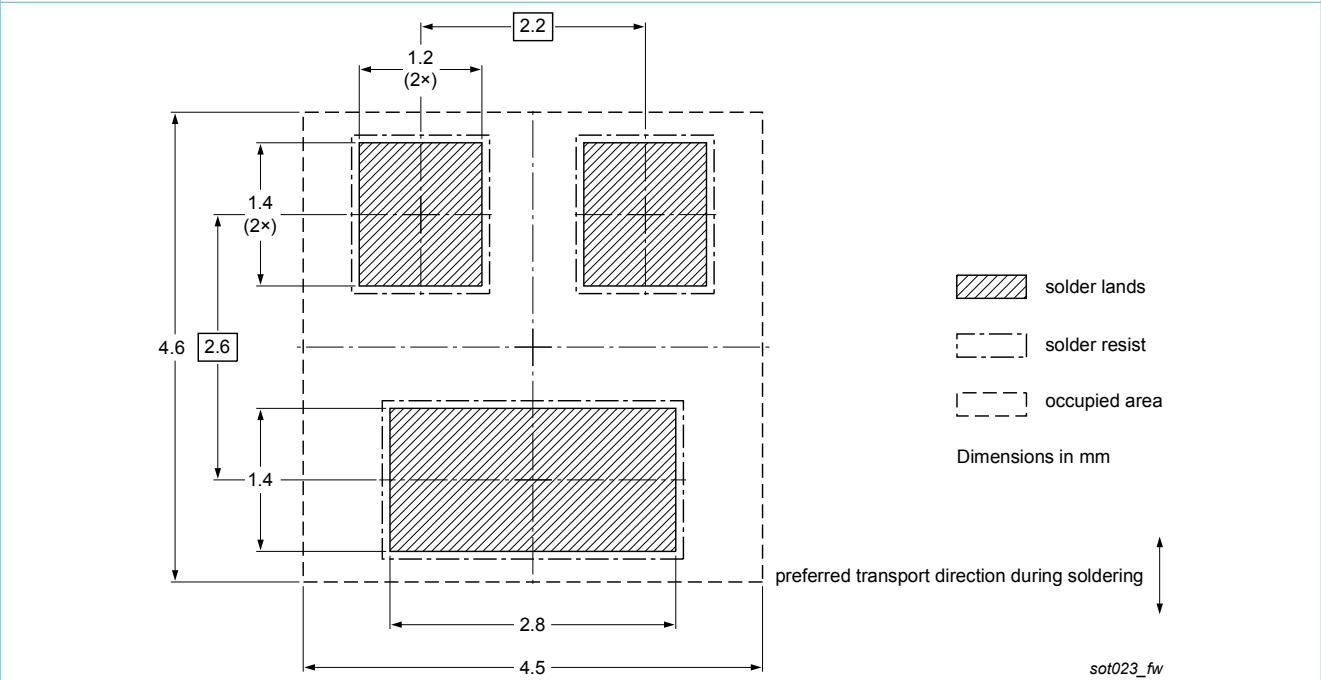


Fig. 20. Wave soldering footprint for TO-236AB (SOT23)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMV42ENE v.1	20160316	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
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Product [short] data sheet	Production	This document contains the product specification.

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16. Contents

1 General description 1

2 Features and benefits 1

3 Applications 1

4 Quick reference data 1

5 Pinning information 2

6 Ordering information 2

7 Marking 2

8 Limiting values 3

9 Thermal characteristics 5

10 Characteristics 7

11 Test information 11

12 Package outline 12

13 Soldering 13

14 Revision history 14

15 Legal information 15

15.1 Data sheet status 15

15.2 Definitions 15

15.3 Disclaimers 15

15.4 Trademarks 16

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