

PMV75UP 20 V, P-channel Trench MOSFET 25 April 2014

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- Enhanced power dissipation capability: Ptot = 1000 mW

3. Applications

- LED driver
- Power management
- High-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-3.2	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -2.5 A; T _j = 25 °C		-	77	102	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G
			TO-236AB (SOT23)	S 017aaa257

6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PMV75UP	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			

7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMV75UP	%CN

[1] % = placeholder for manufacturing site code

Limiting values 8.

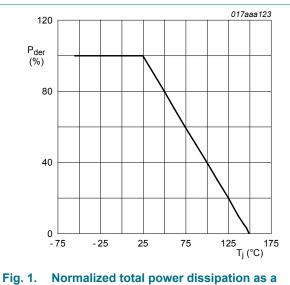
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-12	12	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-3.2	А
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-2.5	А
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-1.6	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-10	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	490	mW
			[1]	-	1000	mW
		T _{sp} = 25 °C		-	5000	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	-0.9	А

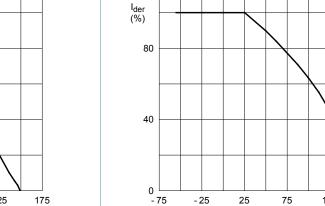
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm². [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

120



function of junction temperature

 $P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$



- 25

Fig. 2. Normalized continuous drain current as a function of junction temperature

25

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

75

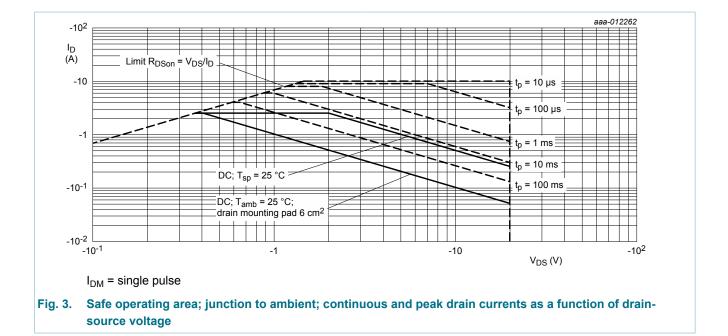
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125 175 T_i (°C)

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9. Thermal characteristics

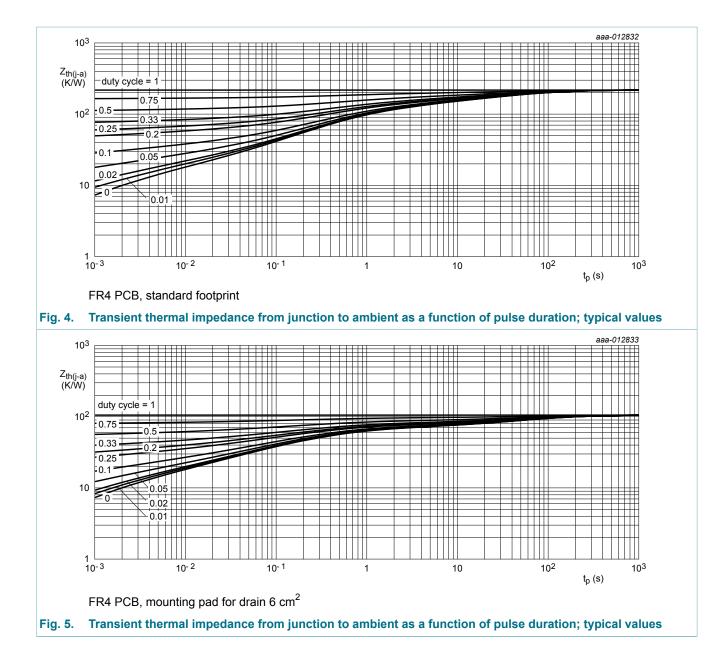
Table 6. T	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	217	255	K/W
	from junction to ambient		[2]	-	105	124	K/W
	ampient	in free air; t ≤ 5 s	[2]	-	73	86	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	20	25	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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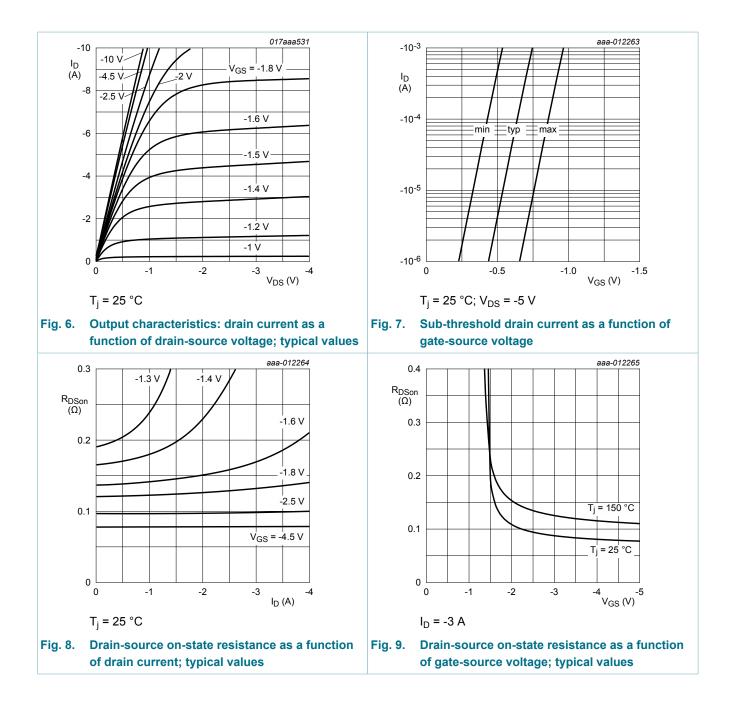


10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static chara	octeristics	· · · · ·	I			
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = -250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	-0.47	-0.68	-0.9	V
I _{DSS}	drain leakage current	V_{DS} = -20 V; V_{GS} = 0 V; T_j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
R _{DSon}	drain-source on-state	V _{GS} = -4.5 V; I _D = -2.5 A; T _j = 25 °C	-	77	102	mΩ
	resistance	V _{GS} = -4.5 V; I _D = -2.4 A; T _j = 150 °C	-	110	146	mΩ
		V _{GS} = -2.5 V; I _D = -2.2 A; T _j = 25 °C	-	95	125	mΩ
		V _{GS} = -1.8 V; I _D = -1 A; T _j = 25 °C	-	120	156	mΩ
9 _{fs}	forward transconductance	V_{DS} = -10 V; I _D = -2 A; T _j = 25 °C	-	15	-	S
R _G	internal gate resistance (AC)	f = 1 MHz	-	41	-	Ω
Dynamic ch	aracteristics	· · · · · · · · · · · · · · · · · · ·				
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I _D = -2.5 A; V _{GS} = -4.5 V;	-	5	7.5	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.7	-	nC
Q _{GD}	gate-drain charge		-	0.9	-	nC
C _{iss}	input capacitance	V _{DS} = -10 V; f = 1 MHz; V _{GS} = 0 V;	-	550	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	63	-	pF
C _{rss}	reverse transfer capacitance		-	53	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I _D = -2.5 A; V _{GS} = -4.5 V;	-	6	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	14	-	ns
t _{d(off)}	turn-off delay time		-	120	-	ns
t _f	fall time	1	-	50	-	ns
Source-drai	n diode	т <u> </u>	I			
V _{SD}	source-drain voltage	I _S = -0.9 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.8	-1.2	V

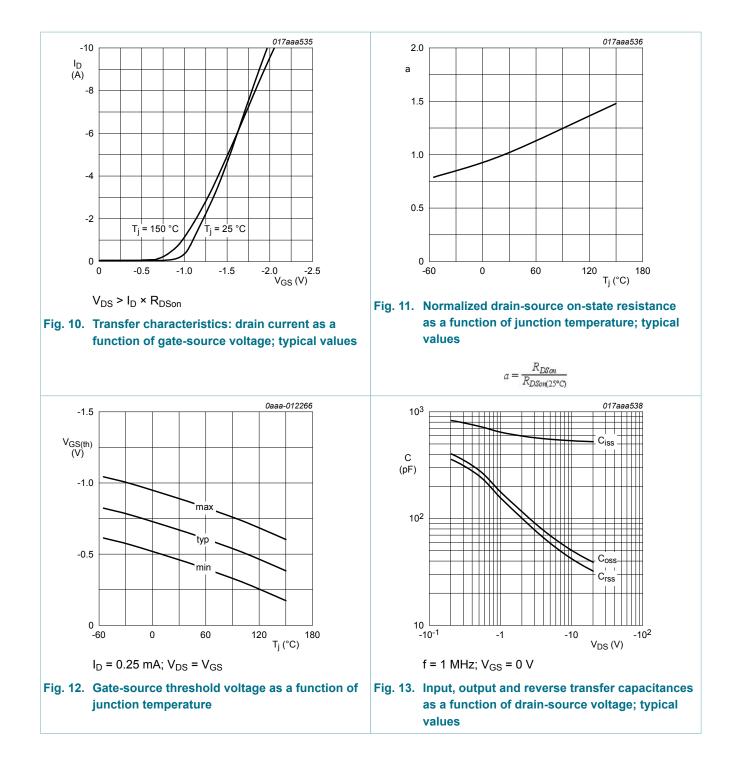
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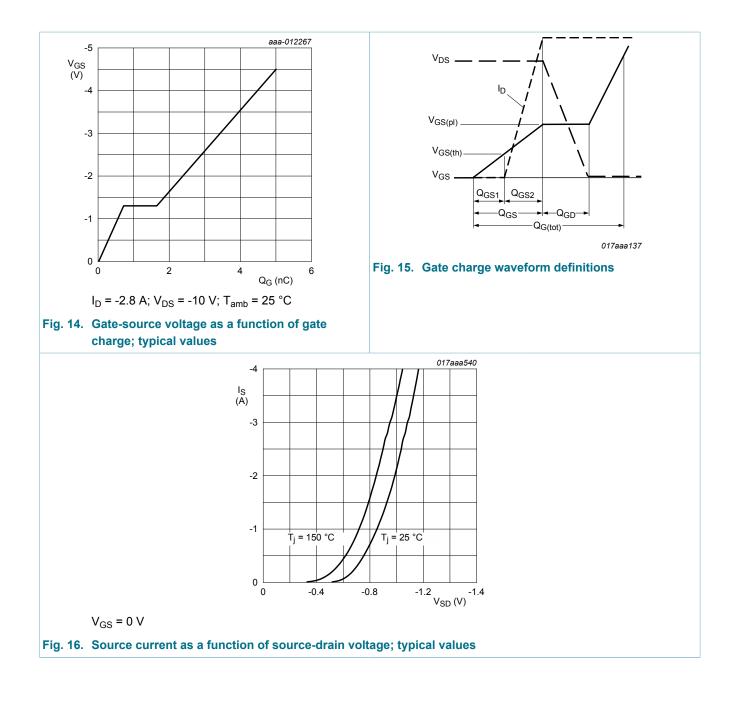
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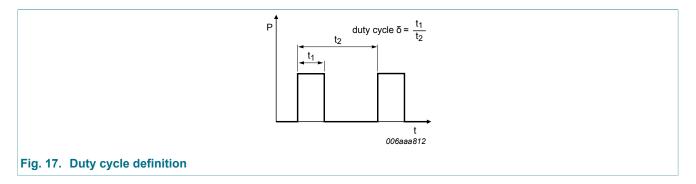


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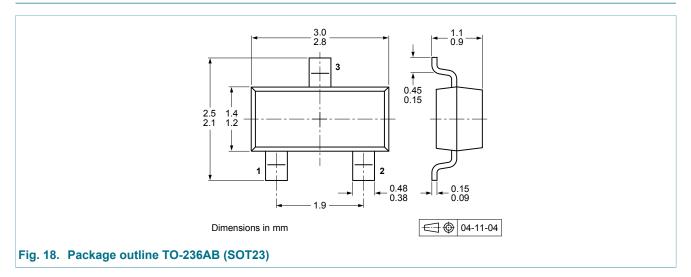
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11. Test information

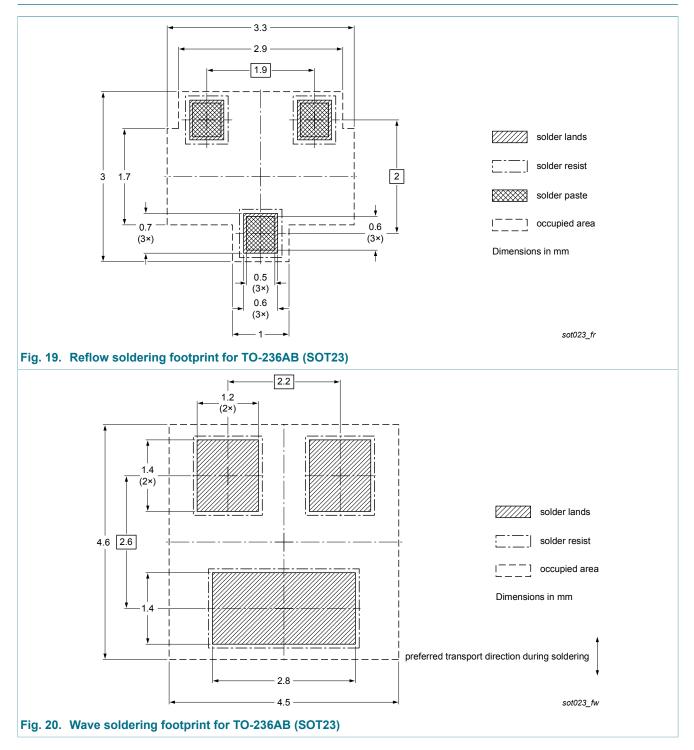


12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision his	e 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMV75UP v.1	20140425	Product data sheet	-	-			

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15. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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