

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN0603-3 (SOT8013) Surface-Mounted Device (SMD) using Trench MOSFET technology.

### 2. Features and benefits

- · Low threshold voltage
- Leadless ultra small package; 0.63 x 0.33 x 0.25 mm
- Trench MOSFET technology
- Low profile (0.25 mm)

### 3. Applications

- Battery switch
- High-speed line driver
- Low-side load switch
- Switching circuits

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>GS</sub>	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-	1.3	A
Static characte	eristics						
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 25 °C		-	122	210	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



# 5. Pinning information

. Pinning info	ormation		
Symbol	Description	Simplified outline	Graphic symbol
G	gate		D
S	source	1 2 3	
D	drain		G_(JE_A)
		Transparent top view DFN0603-3 (SOT8013)	mbb076 S
	<b>Symbol</b> G	G gate   S source	SymbolDescriptionSimplified outlineGgateImage: 1 transformation of t

# 6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
PMX100UN	DFN0603-3	DFN0603-3; plastic, ultra small and leadless full encapsulated package; 3 terminals; 0.225 mm pitch; 0.63 mm x 0.33 mm x 0.25 mm body	SOT8013				

### 7. Marking

### Table 4. Marking codes

Type number	Marking code
PMX100UN	R

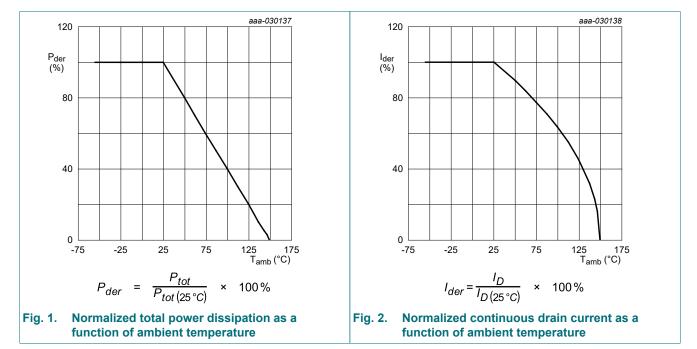
### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

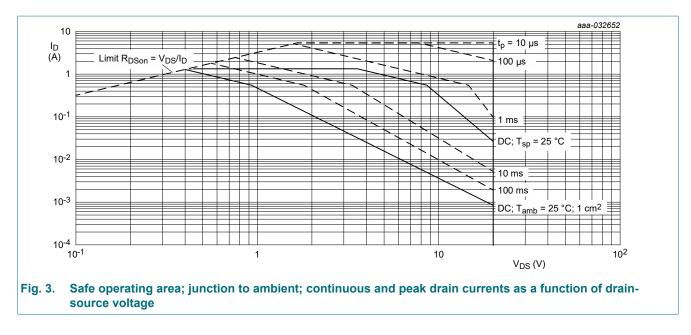
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
V <sub>GS</sub>	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	1.3	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	0.8	A
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	5.2	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	300	mW
			[1]	-	500	mW
		T <sub>sp</sub> = 25 °C		-	4.7	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode					
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.5	А

Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



**Product data sheet** 

### 20 V, N-channel Trench MOSFET



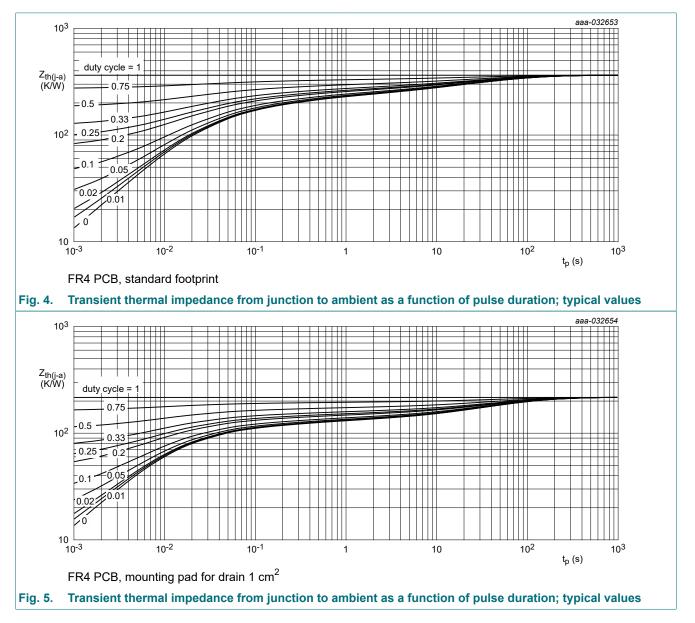
PMX100UN

### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance from	m in free air	[1]	-	360	415	K/W
		[2]	-	215	250	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	23	26.5	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

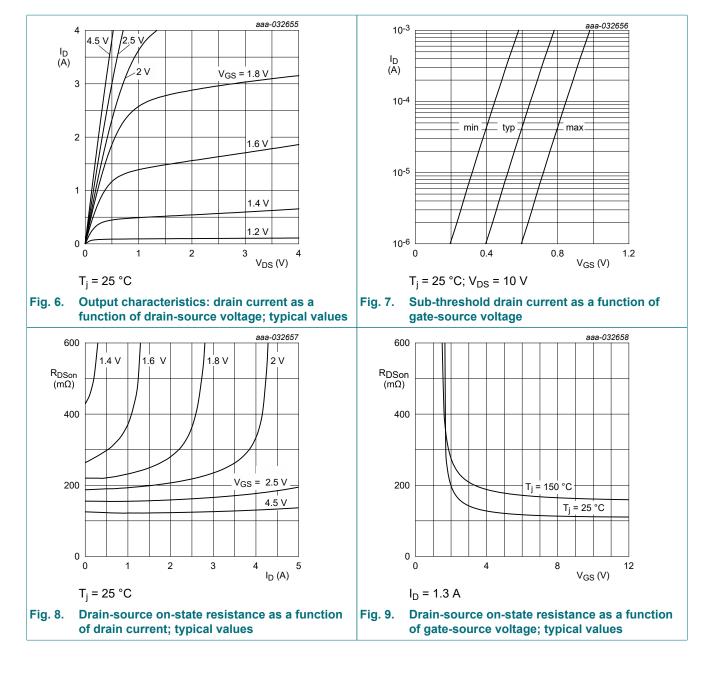
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics		I			
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	0.5	0.7	0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 20 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
		V <sub>GS</sub> = 12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 25 °C	-	122	210	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 150 °C	-	184	320	mΩ
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 25 °C	-	160	230	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 0.5 A; T <sub>j</sub> = 25 °C	-	233	360	mΩ
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 1 A; T <sub>j</sub> = 25 °C	-	2.5	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	11	-	Ω
Dynamic ch	aracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS} = 10 \text{ V}; \text{ I}_{D} = 1.3 \text{ A}; \text{ V}_{GS} = 4.5 \text{ V};$	-	1.5	2.3	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.2	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.4	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS} = 10 \text{ V}; \text{ f} = 1 \text{ MHz}; V_{GS} = 0 \text{ V};$	-	144	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	14	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	12	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 1 A; V <sub>GS</sub> = 4.5 V;	-	1	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	2	-	ns
t <sub>d(off)</sub>	turn-off delay time	1	-	4	-	ns
t <sub>f</sub>	fall time	] [	-	1	-	ns
Source-drai	n diode			·		
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 0.5 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.8	1.2	V

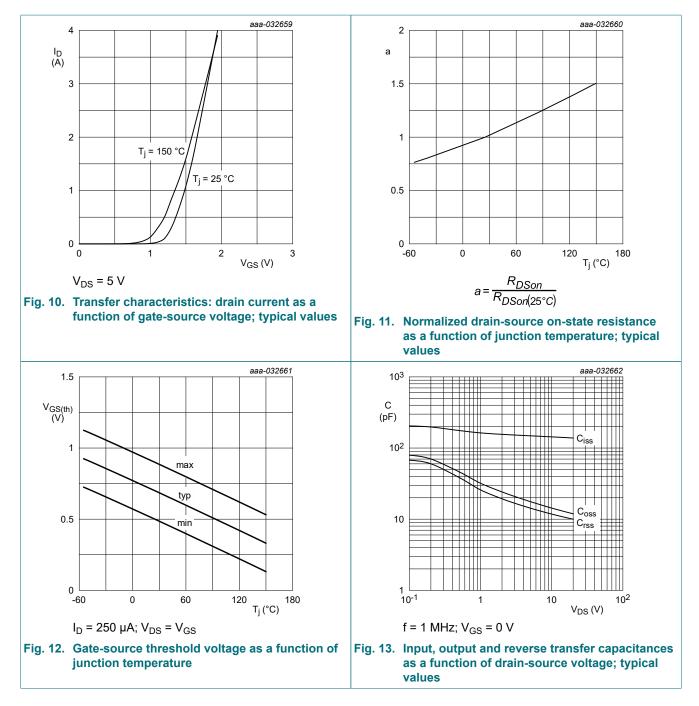
#### 20 V, N-channel Trench MOSFET



**Product data sheet** 

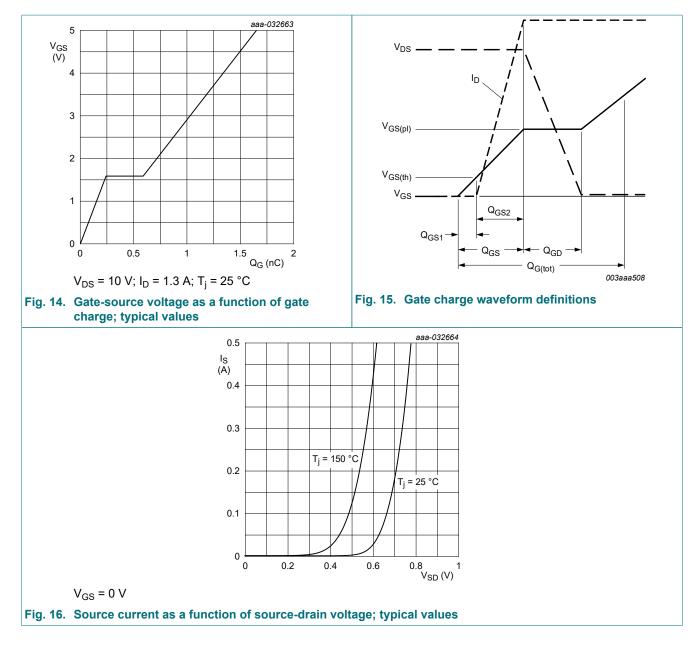
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#### 20 V, N-channel Trench MOSFET

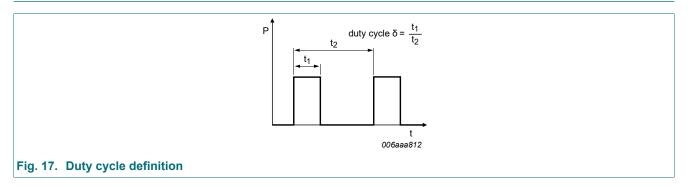


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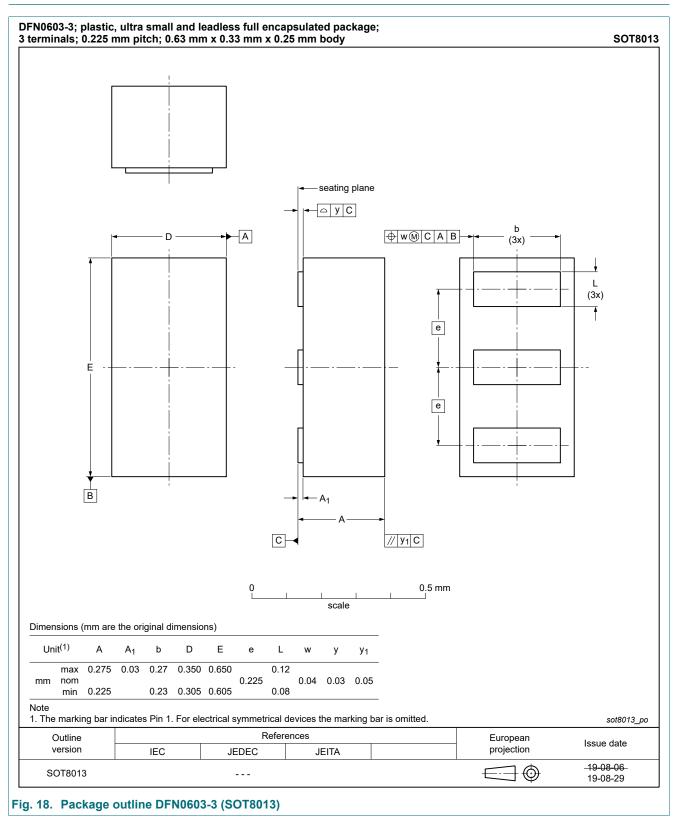
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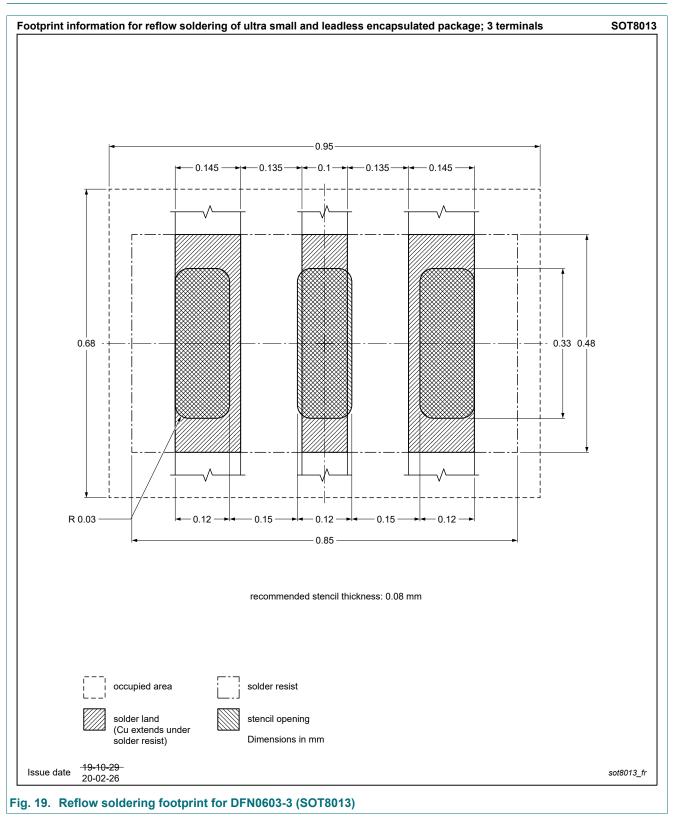
### 11. Test information



### 12. Package outline



### 13. Soldering



# 14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMX100UN v.2	20211202	Product data sheet	-	PMX100UN v.1			
Modifications:	Chapter "Characteris	Chapter "Characteristics": typo correction on Fig. 16					
PMX100UN v.1	20210407	Product data sheet	-	-			

PMX100UN

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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