



PTVS10VZ1USK

Transient voltage suppressor in DSN1608-2 for mobile applications

11 September 2020

Product data sheet

1. General description

Unidirectional Transient Voltage Suppressor (TVS) in a very small leadless DSN1608-2 (SOD964) package.

2. Features and benefits

- Rated peak pulse current: $I_{PPM} = 75 \text{ A}$ (8/20 μs pulse)
- Rated peak pulse power: $P_{PPM} = 2000 \text{ W}$ (8/20 μs pulse)
- Dynamic resistance $R_{dyn} = 0.11 \Omega$
- Reverse current: $I_{RM} = 0.1 \text{ nA}$ typ.
- Very low package height: 0.29 mm

3. Applications

- Power supply protection
- Industrial application
- Power management

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	10	V
I_{PPM}	rated peak pulse current	$t_p = 8/20 \text{ } \mu\text{s}$	[1] [2]	-	-	75	A
		$t_p = 10/1000 \text{ } \mu\text{s}$	[3] [2]	-	-	12.5	A

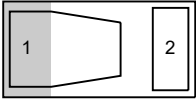

[1] In accordance with IEC 61000-4-5 (8/20 μs current waveform).

[2] Measured from pin 1 to pin 2.

[3] In accordance with IEC 61643-321 (10/1000 μs current waveform).

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 <p>Transparent top view DSN1608-2 (SOD964)</p>	 <p>1 2 <i>sym035</i></p>
2	A	anode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PTVS10VZ1USK	DSN1608-2	silicon, leadless very small package; 2 terminals; 0.6 mm pitch; 1.6 mm x 0.8 mm x 0.29 mm body	SOD964

7. Marking

Table 4. Marking codes

Type number	Marking code
PTVS10VZ1USK	Z4

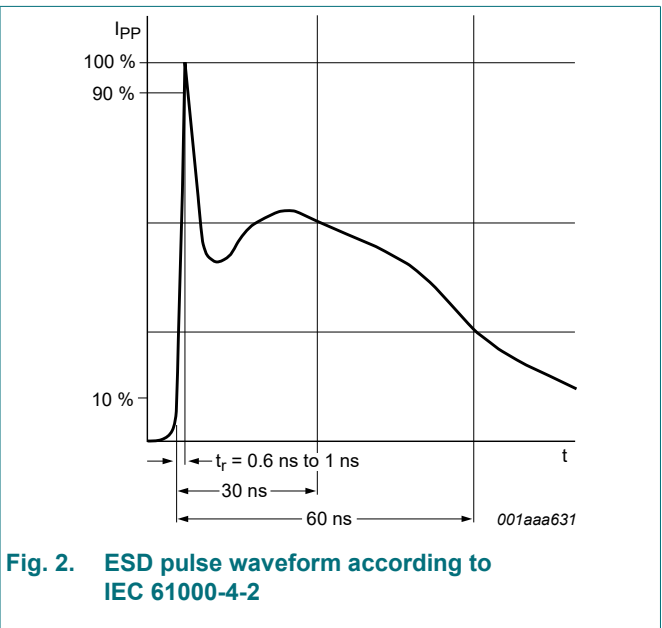
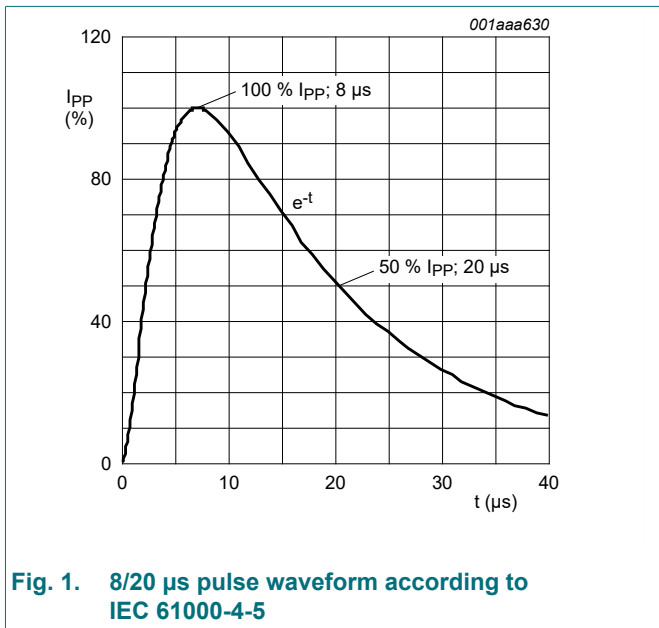
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	2000	W
		t _p = 10/1000 μs	[3] [2]	-	220	W
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	75	A
		t _p = 10/1000 μs	[3] [2]	-	12.5	A
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-40	125	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[4] [2]	-	30	kV
		IEC 61000-4-2; air discharge	[4] [2]	-	30	kV

- [1] In accordance with IEC 61000-4-5 (8/20 μs current waveform).
- [2] Measured from pin 1 to pin 2.
- [3] In accordance with IEC 61643-321 (10/1000 μs current waveform).
- [4] Device stressed with ten non-repetitive ESD pulses.



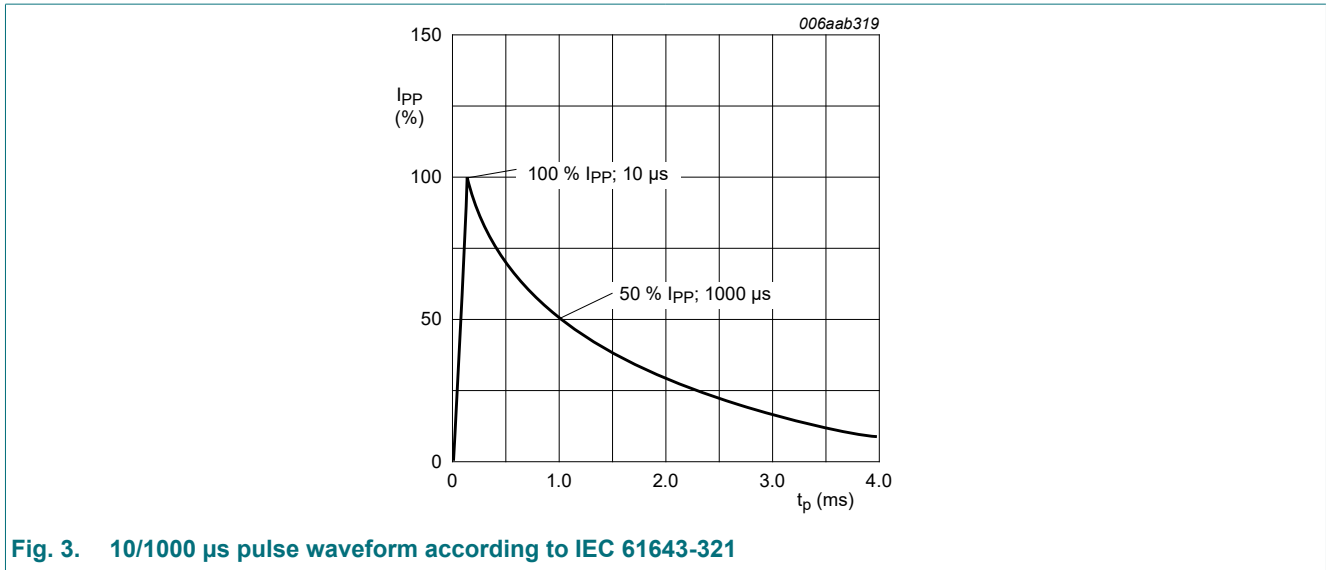


Fig. 3. 10/1000 μs pulse waveform according to IEC 61643-321

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	10	V
V _{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	11.1	12	12.9	V
I _{RM}	reverse leakage current	V _{RWM} = 10 V; T _{amb} = 25 °C	[1]	-	0.1	200	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	500	-	pF
V _{CL}	clamping voltage	I _{PPM} = 75 A; t _p = 8/20 μs; T _{amb} = 25 °C	[2] [1]	-	22.5	27	V
		I _{PPM} = 12.5 A; t _p = 10/1000 μs; T _{amb} = 25 °C	[3] [1]	-	15.1	18.2	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[4] [1]	-	0.11	-	Ω

[1] Measured from pin 1 to 2.

[2] In accordance with IEC 61000-4-5 (8/20 μs current waveform).

[3] In accordance with IEC 61643-321 (10/1000 μs current waveform).

[4] Non-repetitive current pulse, Transmission Line Pulse (TLP) t_p = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

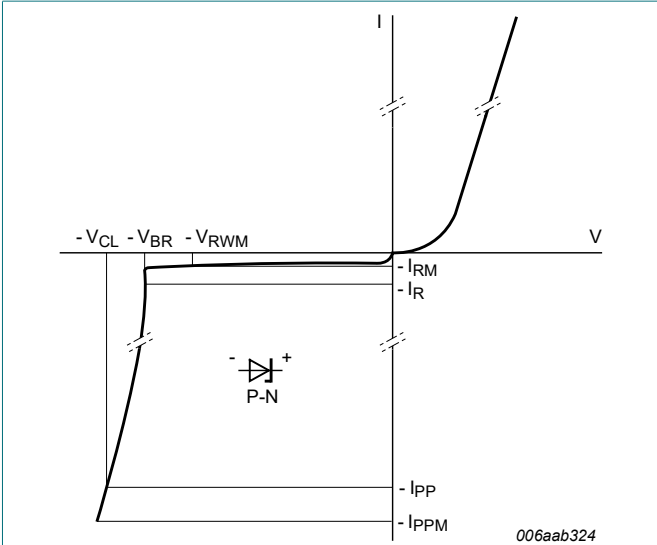


Fig. 4. V-I characteristics for a unidirectional TVS protection diode

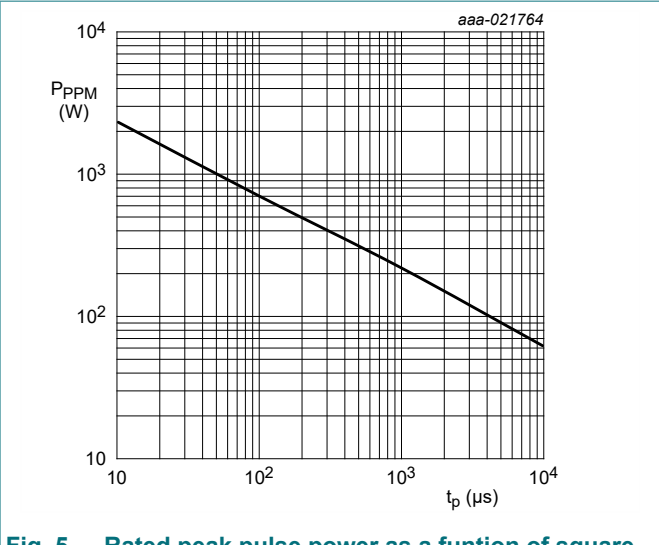


Fig. 5. Rated peak pulse power as a function of square pulse duration; typical values

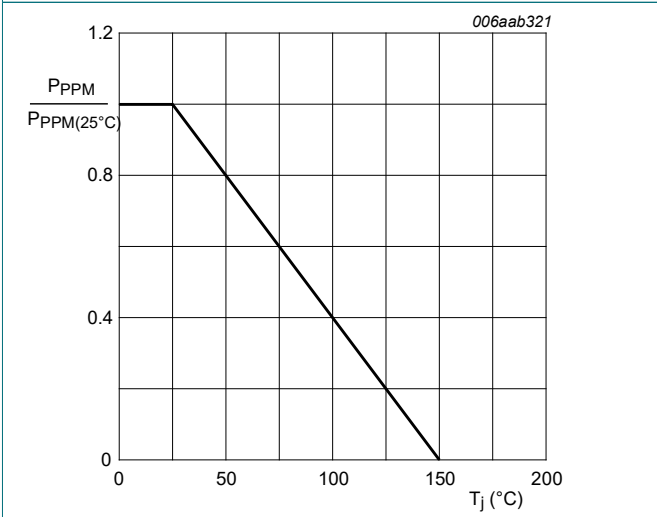


Fig. 6. Relative variation of rated peak pulse power as a function of junction temperature; typical values

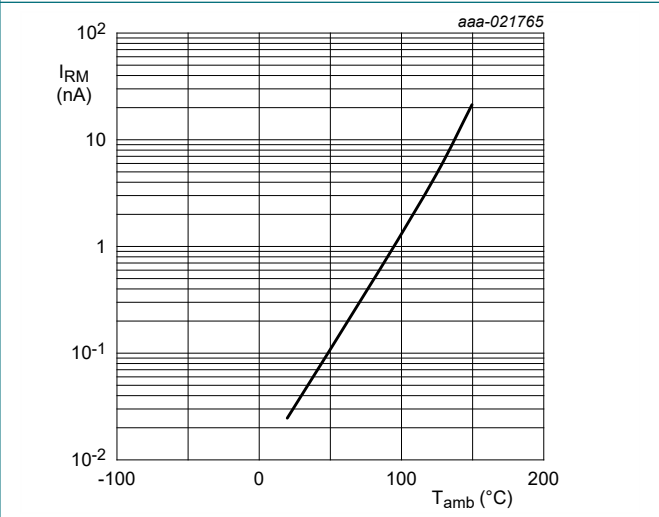


Fig. 7. Relative variation of reverse leakage current as a function of ambient temperature; typical values
 $V_{RWM} = 10 \text{ V}$

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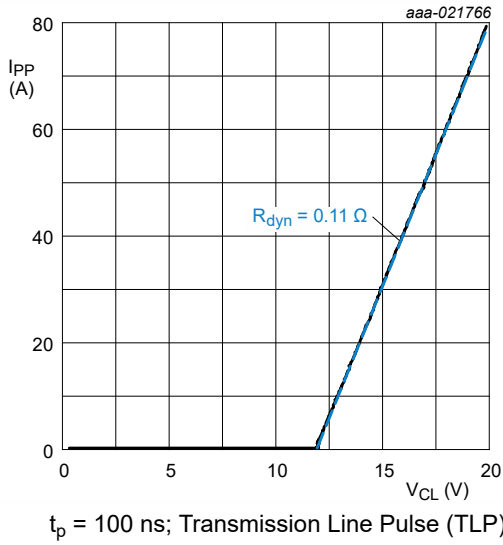


Fig. 8. Positive clamping voltage (TLP); typical values

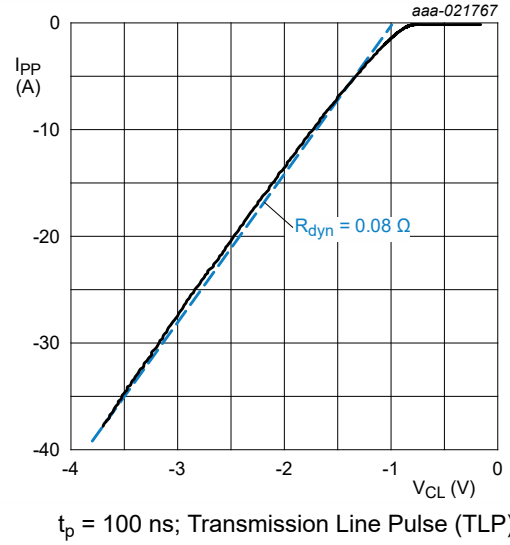


Fig. 9. Negative clamping voltage (TLP); typical values

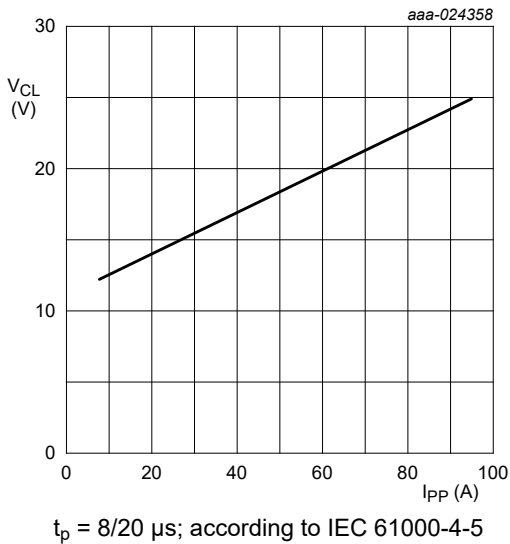


Fig. 10. Positive clamping voltage (8/20 μs pulse); typical values

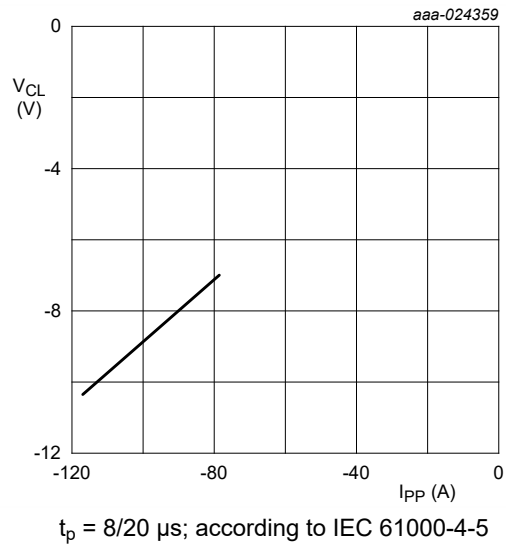


Fig. 11. Negative clamping voltage (8/20 μs pulse); typical values

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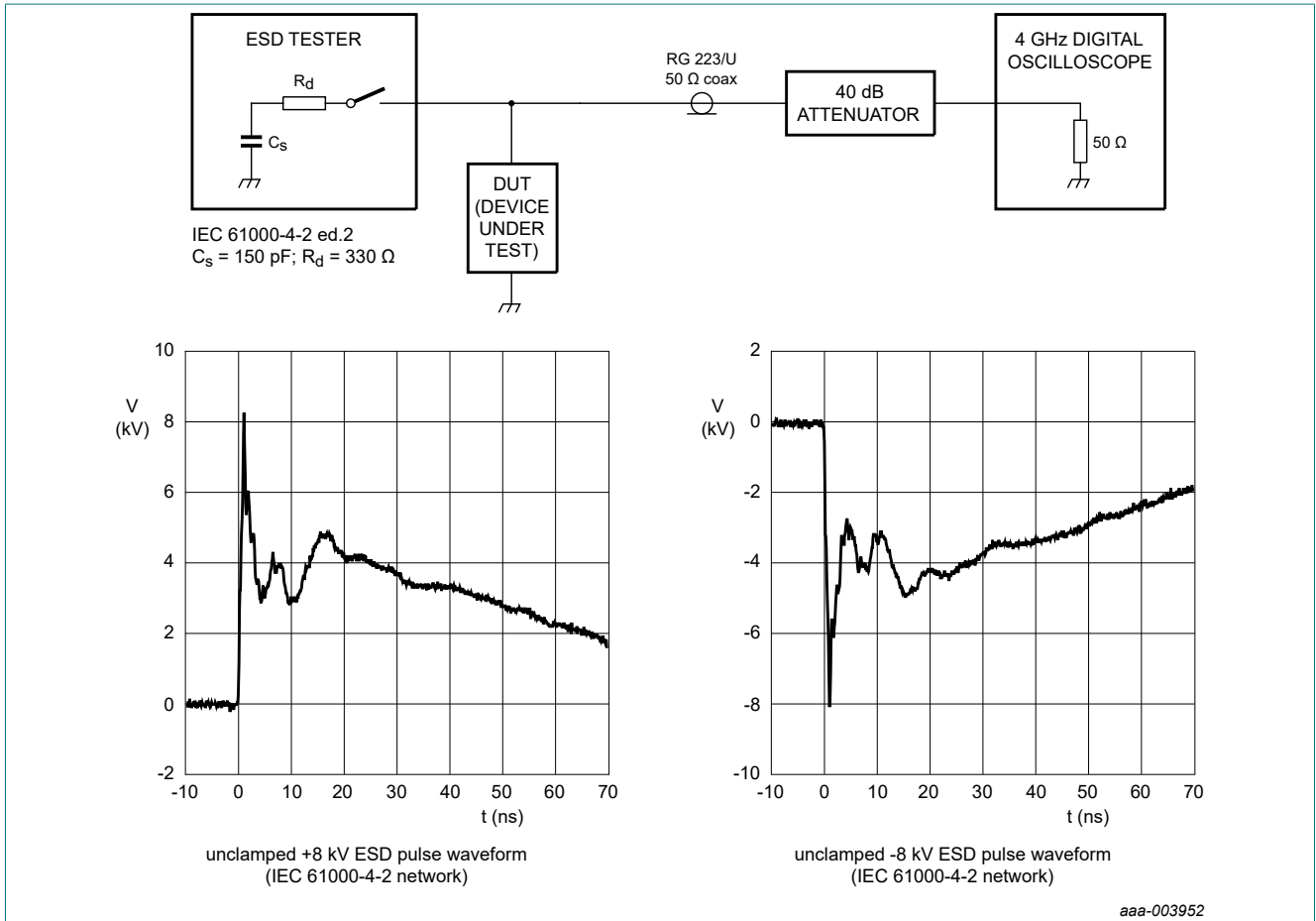
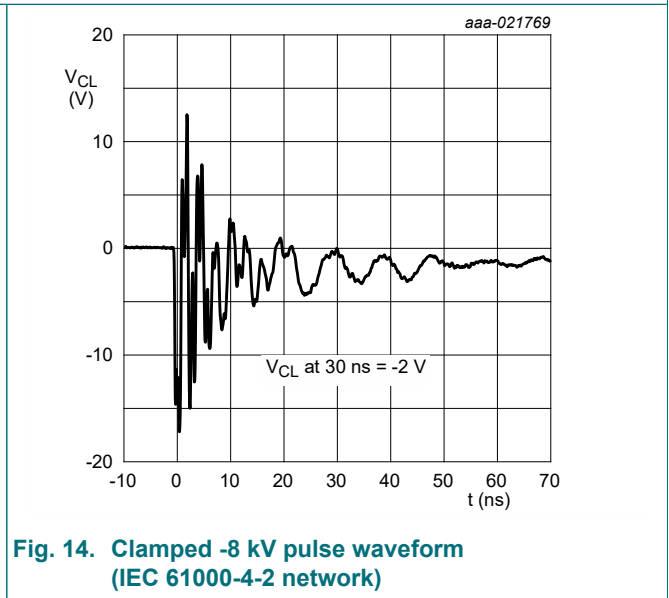
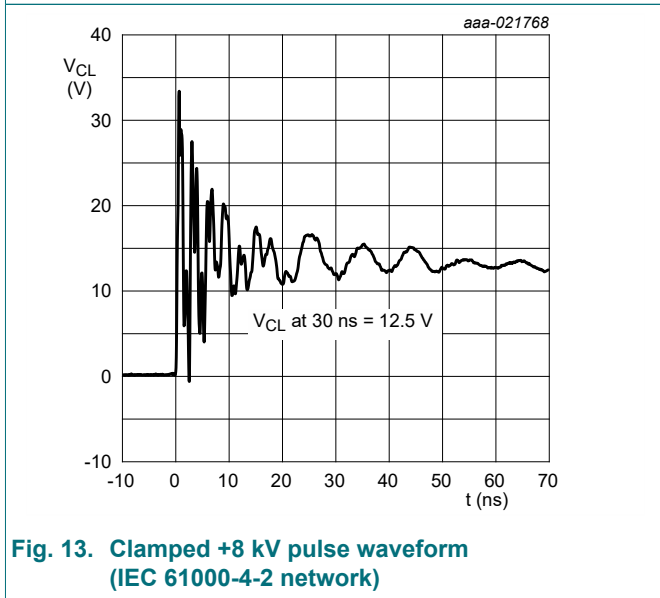


Fig. 12. ESD clamping test setup and waveforms



10. Application information

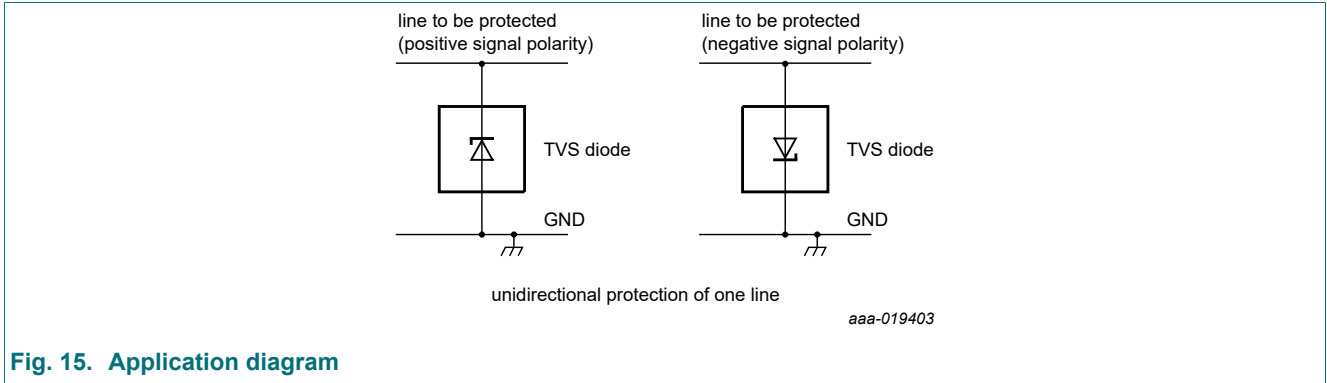


Fig. 15. Application diagram

11. Package outline

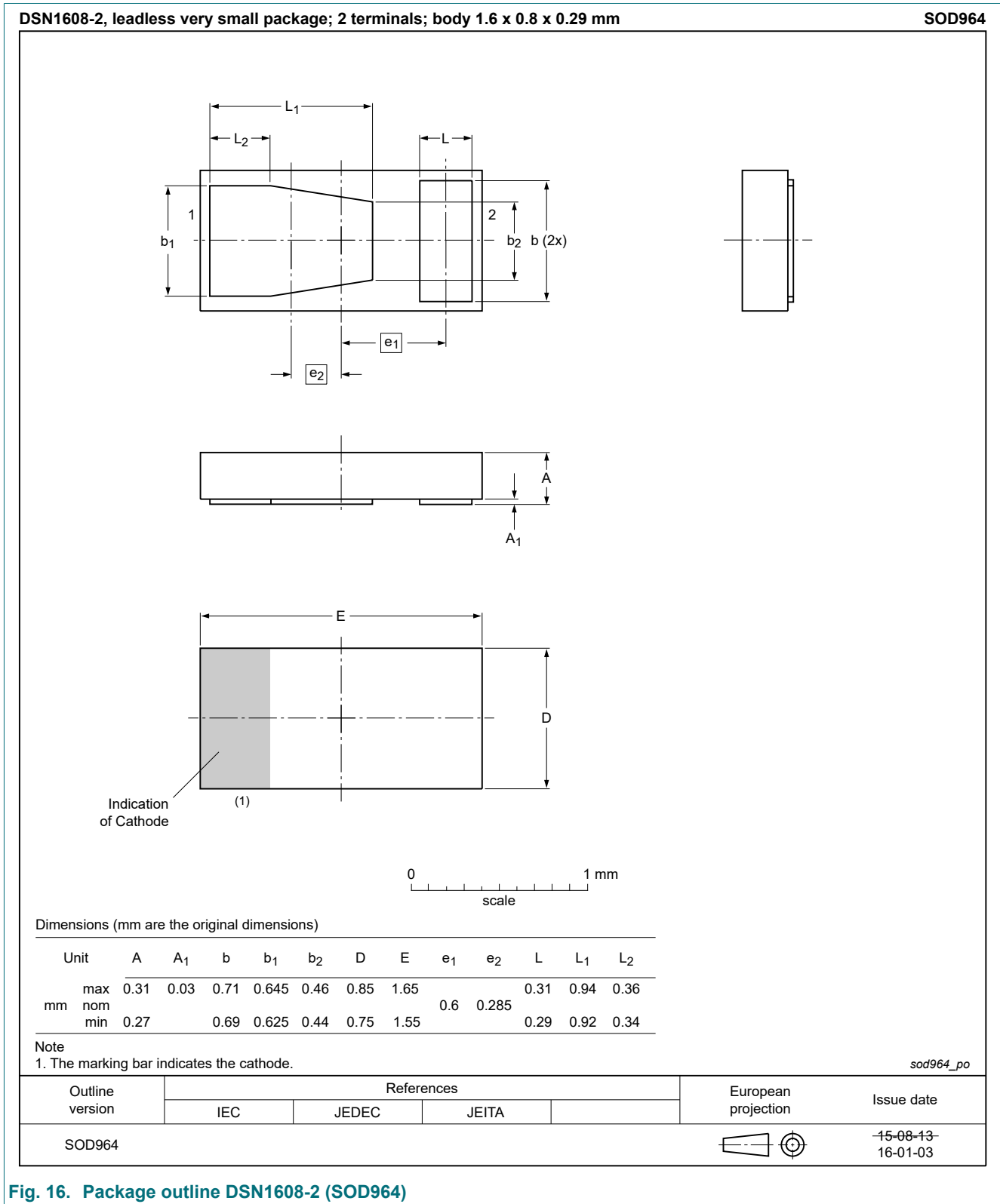
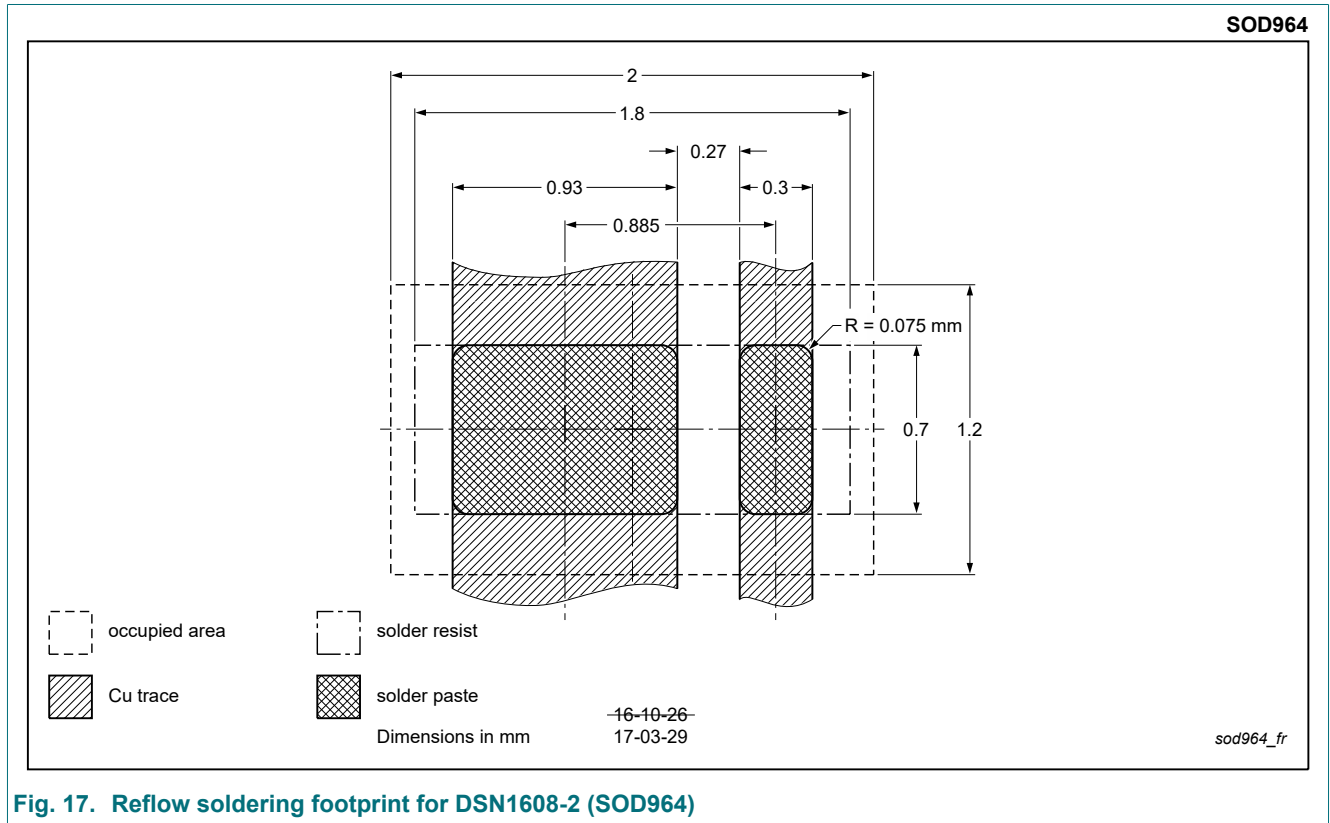


Fig. 16. Package outline DSN1608-2 (SOD964)

12. Soldering



13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PTVS10VZ1USK v.3	20200911	Product data sheet	-	PTVS10VZ1USK v.2
Modifications:	<ul style="list-style-type: none">• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.• Legal texts have been adapted to the new company name where appropriate.• Chapter "Soldering": Figure for reflow soldering footprint updated.			
PTVS10VZ1USK v.2	20160822	Product data sheet	-	PTVS10VZ1USK v.1
PTVS10VZ1USK v.1	20160212	Preliminary data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
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