## 1. General description

Transient voltage suppressor in an ultra small and leadless DSN1006-2 (SOD993B) Surface-Mounted Device (SMD) package designed to protect one line against high surge currents and other transients.

### 2. Features and benefits

- · Bidirectional ESD protection of one line
- Very high surge robustness; I<sub>PPM</sub> = 82 A (measured) for 8/20 μs pulse
- Very low clamping voltage: V<sub>CL</sub> = 11 V typ. for 70 A at 8/20µs pulse
- ESD protection up to 30 kV

## 3. Applications

Surge protection for

- supply and battery lines
- audio interfaces

in portable communication, consumer and computing devices.

## 4. Quick reference data

Table 1. Quick reference data

| Symbol           | Parameter                | Conditions   |     | Min | Тур | Max | Unit |
|------------------|--------------------------|--|-----|-----|-----|-----|------|
| $V_{RWM}$        | reverse standoff voltage | T <sub>amb</sub> = 25 °C   |     | -   | -   | 3.3 | V    |
| I <sub>PPM</sub> | rated peak pulse current | $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C   | [1] | -   | -   | 70  | А    |
| V <sub>CL</sub>  | clamping voltage         | $I_{PPM} = 70 \text{ A}; t_p = 8/20  \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$ | [1] | -   | 11  | 13  | V    |

[1] In accordance with IEC 61000-4-5 (8/20  $\mu s$  current waveform).



# 5. Pinning information

#### **Table 2. Pinning information**

| Pin | Symbol | Description       | Simplified outline                       | Graphic symbol |
|-----|--------|-------------------|--|----------------|
| 1   | K1     | cathode (diode 1) |  | K1 F4 DJ K2    |
| 2   | K2     | cathode (diode 2) | 1 2                                      | sym045         |
|     |        |                   | Transparent top view DSN1006-2 (SOD993B) |                |

# 6. Ordering information

#### **Table 3. Ordering information**

| Type number  | Package |  |         |  |  |
|--------------|---------|--|---------|--|--|
|              | Name    | Description  | Version |  |  |
| PTVS3V3Z1BSC |         | DSN1006-2, leadless ultra small package; 2 terminals; body 1.0 x 0.6 x 0.27 mm | SOD993B |  |  |

# 7. Marking

## Table 4. Marking codes

| Type number  | Marking code |
|--------------|--------------|
| PTVS3V3Z1BSC | S3           |

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                | Conditions   |     | Min | Max | Unit |
|------------------|--------------------------|--|-----|-----|-----|------|
| I <sub>PPM</sub> | rated peak pulse current | t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C | [1] | -   | 70  | Α    |
| Tj               | junction temperature     |  |     | -   | 150 | °C   |
| T <sub>amb</sub> | ambient temperature      |  |     | -55 | 150 | °C   |
| T <sub>stg</sub> | storage temperature      |  |     | -65 | 150 | °C   |
| ESD maximum      | ratings                  |  |     |     |     |      |
| V <sub>ESD</sub> | voltago                  | IEC 61000-4-2; contact discharge                   | [2] | -   | 30  | kV   |
|                  |                          | IEC 61000-4-2; air discharge                       | [2] | -   | 30  | kV   |

<sup>[1]</sup> In accordance with IEC 61000-4-5 (8/20 µs current waveform).

<sup>[2]</sup> Device stressed with ten non-repetitive ESD pulses.

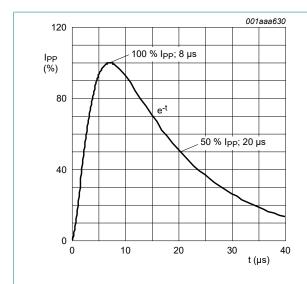


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

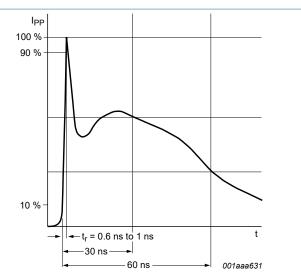


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

**Table 6. Characteristics** 

| Symbol           | Parameter                | Conditions   |     | Min | Тур  | Max | Unit |
|------------------|--------------------------|--|-----|-----|------|-----|------|
| $V_{RWM}$        | reverse standoff voltage | T <sub>amb</sub> = 25 °C   |     | -   | -    | 3.3 | V    |
| $V_{BR}$         | breakdown voltage        | I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C                                 |     | 3.8 | 4.9  | 6.8 | V    |
| I <sub>RM</sub>  | reverse leakage current  | V <sub>R</sub> = 3.3 V; T <sub>amb</sub> = 25 °C                                 |     | -   | 1    | 100 | nA   |
| C <sub>d</sub>   | diode capacitance        | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C                        |     | -   | 180  | 200 | pF   |
| V <sub>CL</sub>  | clamping voltage         | I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C        | [1] | -   | 5    | -   | V    |
|                  |                          | $I_{PPM} = 70 \text{ A}; t_p = 8/20  \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$ | [1] | -   | 11   | 13  | V    |
|                  |                          | I <sub>PP</sub> = 16 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C           | [2] | -   | 5.7  | -   | V    |
| R <sub>dyn</sub> | dynamic resistance       | I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C                                  | [2] | -   | 0.05 | -   | Ω    |

<sup>[1]</sup> In accordance with IEC 61000-4-5 (8/20 µs current waveform).

<sup>[2]</sup> Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI/ESD STM5.5.1-2008

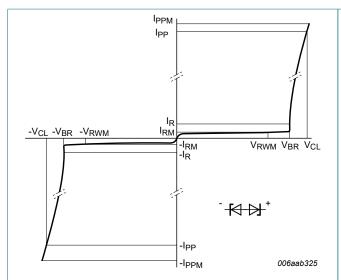


Fig. 3. V-I characteristics for a bidirectional TVS diode

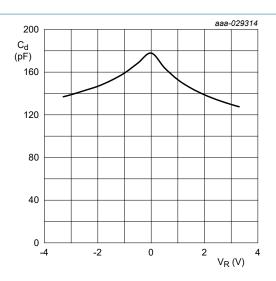


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

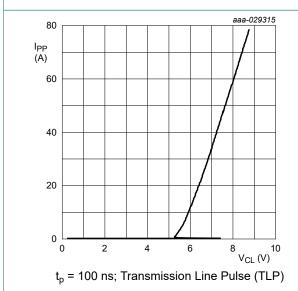
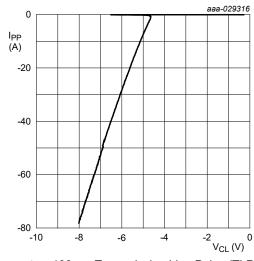


Fig. 5. Positive clamping voltage (TLP); typical values



t<sub>p</sub> = 100 ns; Transmission Line Pulse (TLP)

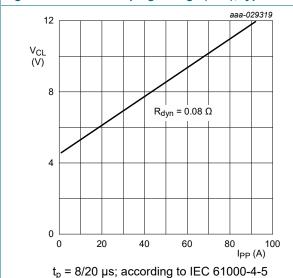
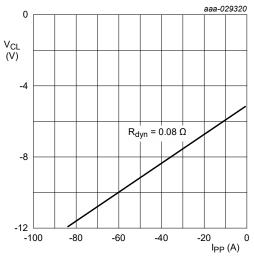


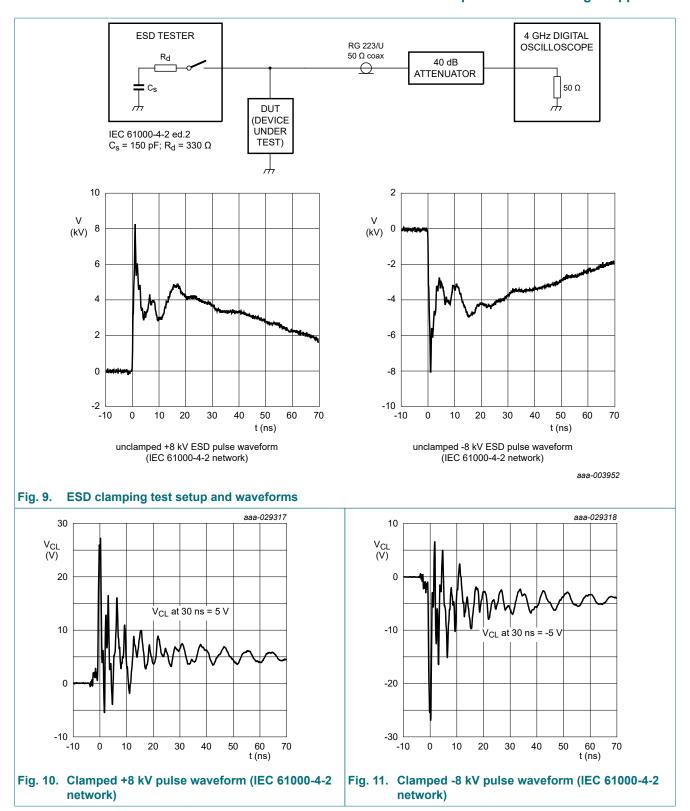
Fig. 7. Positive clamping voltage (8/20 μs pulse); typical values





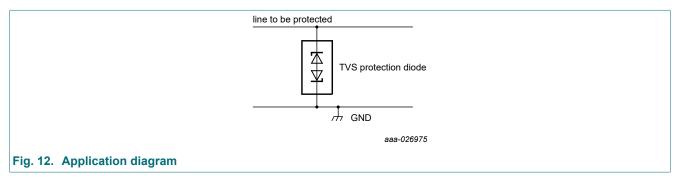
 $t_p$  = 8/20 µs; according to IEC 61000-4-5

Fig. 8. Negative clamping voltage (8/20 μs pulse); typical values



## 10. Application information

The device is designed for the protection of one bidirectional line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

# 11. Package outline

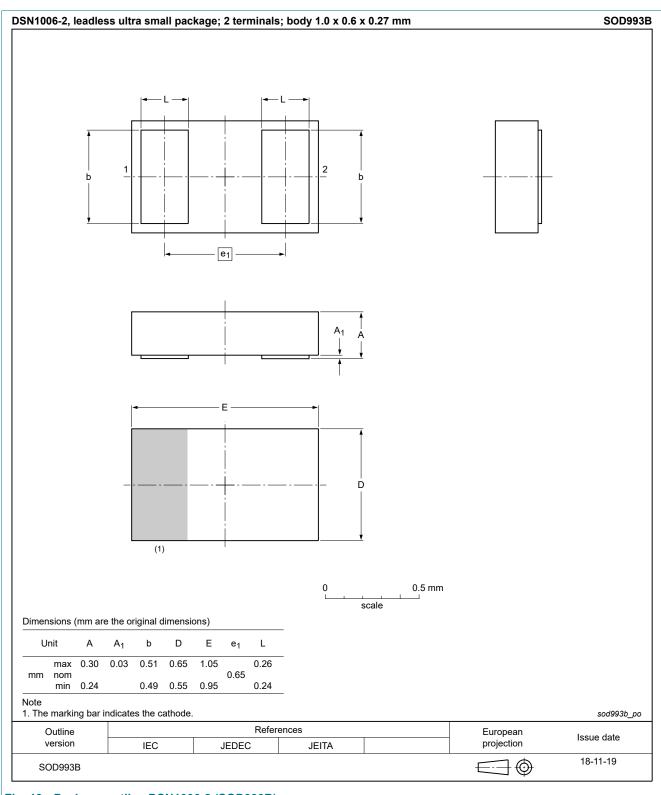
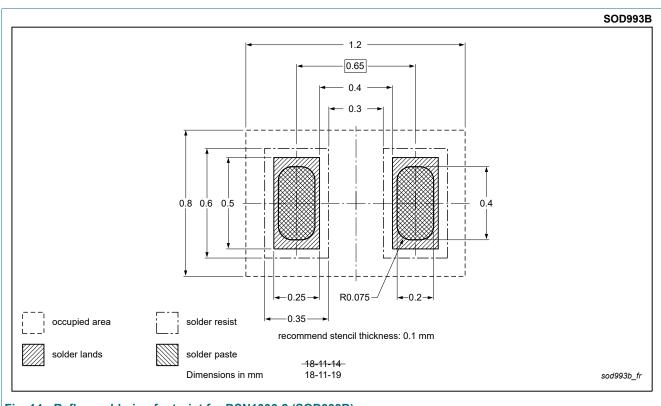


Fig. 13. Package outline DSN1006-2 (SOD993B)

# 12. Soldering



# 13. Revision history

### **Table 7. Revision history**

| Data sheet ID    | Release date                                    | Data sheet status      | Change notice | Supersedes       |  |  |  |
|------------------|---|------------------------|---------------|------------------|--|--|--|
| PTVS3V3Z1BSC v.2 | 20190206  | Product data sheet     | -             | PTVS3V3Z1BSC v.1 |  |  |  |
| Modifications:   | Updated document status to "Product data sheet" |                        |               |                  |  |  |  |
| PTVS3V3Z1BSC v.1 | 20190125  | Preliminary data sheet | -             | -                |  |  |  |

## 14. Legal information

#### **Data sheet status**

| Document status [1][2]         | Product<br>status [3] | Definition  |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet   | Development           | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification         | This document contains data from the preliminary specification.                       |
| Product [short]<br>data sheet  | Production            | This document contains the product specification.                                     |

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