

Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of http://www.nxp.com, http://www.nxp.com, http://www.nexperia.com/, http://www.nexperia.com/, use http://www.nexperia.com/

Instead of sales.addresses@www.nxp.com or sales.addresses@www.semiconductors.philips.com, use salesaddresses@nexperia.com (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © Nexperia B.V. (year). All rights reserved.

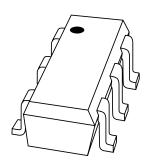
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PUMZ1 NPN/PNP general purpose transistors

Product data sheet Supersedes data of 2002 May 6 2004 Oct 15



NPN/PNP general purpose transistors

PUMZ1

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 40 V)
- Reduces number of components and boardspace.

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

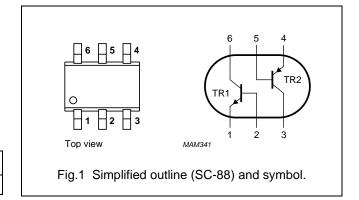
Two independently operating NPN/PNP transistors in an SC-88; SOT363 plastic package.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
PUMZ1	F*Z	

PINNING

PIN		DESCRIPTION
1, 4	emitter	TR1; TR2
2, 5	base	TR1; TR2
3, 6	collector	TR2; TR1



Note

- 1. * = -: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

ORDERING INFORMATION

TYPE NUMBER		PACKAGE			
ITPE NUMBER	NAME DESCRIPTION		VERSION		
PUMZ1	_	plastic surface mounted package; 6 leads	SOT363		

NPN/PNP general purpose transistors

PUMZ1

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
Per transis	Per transistor; for the PNP transistor with negative polarity					
V _{CBO}	collector-base voltage	open emitter	_	50	V	
V _{CEO}	collector-emitter voltage	open base	_	40	V	
V _{EBO}	emitter-base voltage	open collector	_	5	V	
I _C	collector current (DC)		_	100	mA	
I _{CM}	peak collector current		_	200	mA	
I _{BM}	peak base current		_	200	mA	
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	200	mW	
T _{stg}	storage temperature		-65	+150	°C	
Tj	junction temperature		_	150	°C	
T _{amb}	operating ambient temperature		-65	+150	°C	
Per device	Per device					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	300	mW	

Note

THERMAL CHARACTERISTICS

SYMBOL PARAMETER		CONDITIONS	VALUE	UNIT
Per device				
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	416	K/W

Note

1. Device mounted on an FR4 printed-circuit board.

^{1.} Device mounted on an FR4 printed-circuit board.

NPN/PNP general purpose transistors

PUMZ1

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transist	Per transistor; for the PNP transistor with negative polarity				
I _{CBO}	collector-base cut-off current	I _E = 0 A; V _{CB} = 30 V	_	100	nA
		I _E = 0 A; V _{CB} = 30 V; T _j = 150 °C	_	10	μΑ
I _{EBO}	emitter-base cut-off current	I _C = 0 A; V _{EB} = 4 V	_	100	nA
h _{FE}	DC current gain	I _C = 1 mA; V _{CE} = 6 V	120	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}; \text{ note 1}$	_	200	mV
C _c	collector capacitance	$I_E = i_e = 0 A; V_{CB} = 12 V; f = 1 MHz$			
	TR1		_	1.5	pF
	TR2		_	2.2	pF
f _T	transition frequency	I _C = 2 mA; V _{CE} = 12 V; f = 100 MHz	100	_	MHz

Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

2004 Oct 15

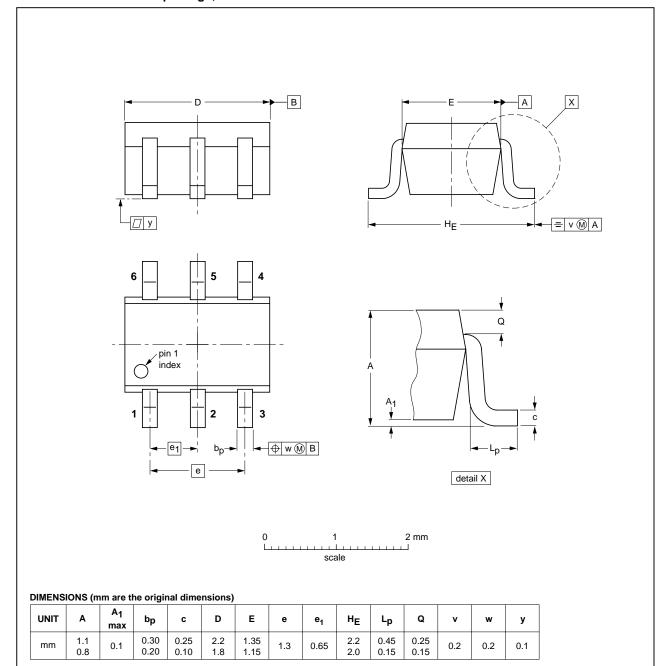
NPN/PNP general purpose transistors

PUMZ1

PACKAGE OUTLINE

Plastic surface-mounted package; 6 leads

SOT363



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT363			SC-88			04-11-08 06-03-16

NPN/PNP general purpose transistors

PUMZ1

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands R75/05/pp7 Date of release: 2004 Oct 15 Document order number: 9397 750 13967



X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Bipolar Transistors - BJT category:

Click to view products by Nexperia manufacturer:

Other Similar products are found below:

619691C MCH4017-TL-H BC546/116 BC557/116 BSW67A NTE158 NTE187A NTE195A NTE2302 NTE2330 NTE63 C4460
2SA1419T-TD-H 2SA1721-O(TE85L,F) 2SA2126-E 2SB1204S-TL-E 2SD2150T100R SP000011176 FMMTA92QTA 2N2369ADCSM
2N5769 2SC2412KT146S 2SC5490A-TL-H 2SD1816S-TL-E 2SD1816T-TL-E CMXT2207 TR CPH6501-TL-E MCH4021-TL-E
US6T6TR NJL0281DG 732314D CMXT3906 TR CPH3121-TL-E CPH6021-TL-H 873787E IMZ2AT108 UMX21NTR MCH6102-TL-E
NJL0302DG 2N3583 2SA1434-TB-E 2SC3143-4-TB-E 2SD1621S-TD-E NTE103 30A02MH-TL-E NSV40301MZ4T1G NTE101 NTE13
NTE15 NTE16001