**Product data sheet** 

# 1. General description

PNP switching transistor in a medium power flat lead SOT89 (SC-62/TO-243) Surface-Mounted Device (SMD) plastic package.

NPN complement: PXT2222A

### 2. Features and benefits

High current: max. 600 mA Low voltage: max. 60 V AEC-Q101 qualified

# 3. Applications

Switching and linear amplification

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-60	V
I <sub>C</sub>	collector current		-	-	-600	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	100	-	-	

# 5. Pinning information

Table 2. Pinning information

10.010 =1				
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		В
3	В	base	3 2 1	E sym132
			SOT89	·



60 V, 600 mA, PNP switching transistor

# 6. Ordering information

#### Table 3. Ordering information

3						
Type number	Package	ge				
	Name	Description	Version			
PXT2907A	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89			

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code [1]
PXT2907A	%2F

[1] % = placeholder for manufacturing site code

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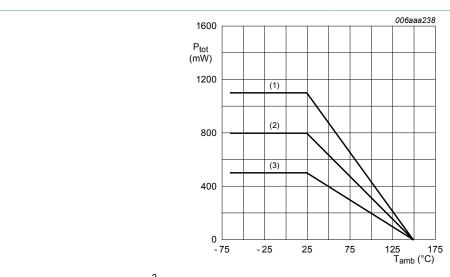
# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-600	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-800	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.5	W
			[2]	-	0.8	W
			[ <u>3]</u>	-	1.1	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB; 6 cm<sup>2</sup> mounting pad for collector.
- (2) FR4 PCB; 1 cm<sup>2</sup> mounting pad for collector.
- (3) FR4 PCB; standard footprint.

Fig. 1. Power derating curves

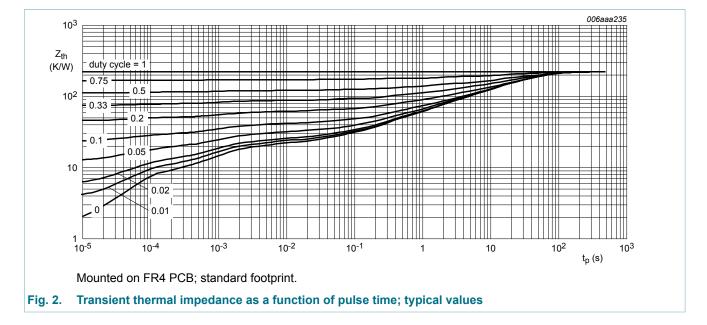
60 V, 600 mA, PNP switching transistor

## 9. Thermal characteristics

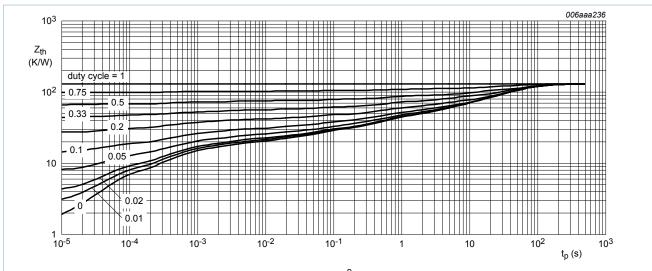
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ang a)	thermal resistance		[1]	-	-	250	K/W
	from junction to		[2]	-	-	156	K/W
	ambient		[3]	-	-	113	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	30	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

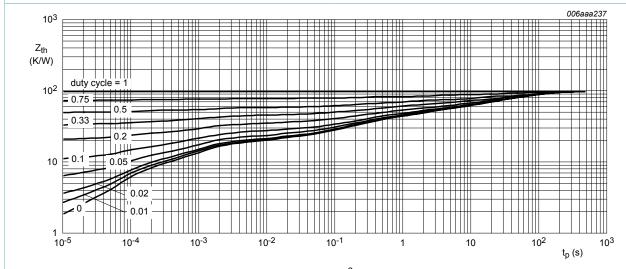


### 60 V, 600 mA, PNP switching transistor



Mounted on FR4 PCB; mounting pad for collector 1 cm<sup>2</sup>.

Fig. 3. Transient thermal impedance as a function of pulse time; typical values



Mounted on FR4 PCB; mounting pad for collector 6 cm<sup>2</sup>.

Fig. 4. Transient thermal impedance as a function of pulse time; typical values

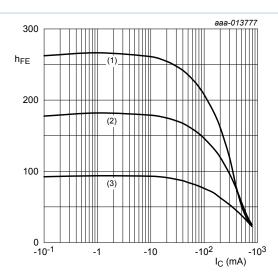
60 V, 600 mA, PNP switching transistor

# 10. Characteristics

#### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-10	nA
	current	$V_{CB}$ = -50 V; $I_{E}$ = 0 A; $T_{j}$ = 125 °C	-	-	-10	μA
ЕВО	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-50	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; $I_{C}$ = -0.1 mA; $T_{amb}$ = 25 °C	75	-	-	
		$V_{CE}$ = -1 V; $I_{C}$ = -1 mA; $T_{amb}$ = 25 °C	100	-	-	
		$V_{CE}$ = -1 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	100	-	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -150 mA; $T_{amb}$ = 25 °C	100	-	300	
		$V_{CE}$ = -10 V; $I_{C}$ = -500 mA; $T_{amb}$ = 25 °C	50	-	-	
OLOGI	collector-emitter saturation voltage	$I_{C}$ = -150 mA; $I_{B}$ = -15 mA; $T_{amb}$ = 25 °C	-	-	-400	mV
		$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; $T_{amb}$ = 25 °C	-	-	-1.6	V
DLout	base-emitter saturation voltage	$I_{C}$ = -150 mA; $I_{B}$ = -15 mA; $T_{amb}$ = 25 °C	-	-	-1.3	V
		$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; $T_{amb}$ = 25 °C	-	-	-2.6	V
d	delay time	I <sub>C</sub> = -150 mA; I <sub>Bon</sub> = -15 mA;	-	-	12	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = 15 mA; T <sub>amb</sub> = 25 °C	-	-	30	ns
on	turn-on time		-	-	40	ns
t <sub>s</sub>	storage time	_	-	-	300	ns
f	fall time	_	-	-	65	ns
off	turn-off time	_	-	-	365	ns
C <sub>C</sub>	collector capacitance	$V_{CB}$ = -10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C	-	-	8	pF
C <sub>E</sub>	emitter capacitance	$V_{EB}$ = -500 mV; $I_{C}$ = 0 A; $i_{c}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C	-	-	35	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -20 V; $I_{C}$ = -50 mA; f = 100 MHz; $T_{amb}$ = 25 °C	200	-	-	MHz

### 60 V, 600 mA, PNP switching transistor



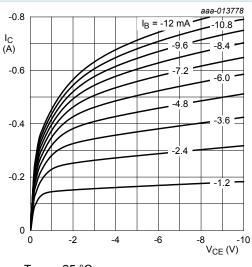
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

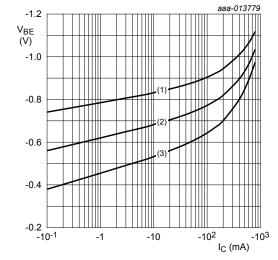
(3) 
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values



 $T_{amb} = 25 \, ^{\circ}C$ 

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



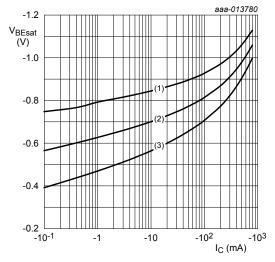
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

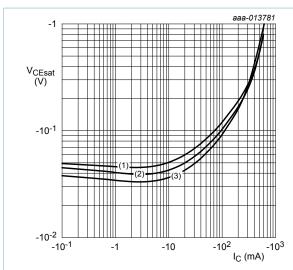
(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

### 60 V, 600 mA, PNP switching transistor



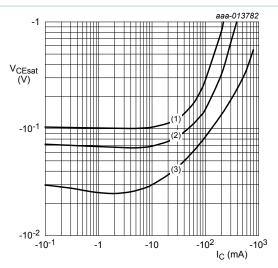
$$I_{\rm C}/I_{\rm B}=20$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

(1) 
$$I_C/I_B = 100$$

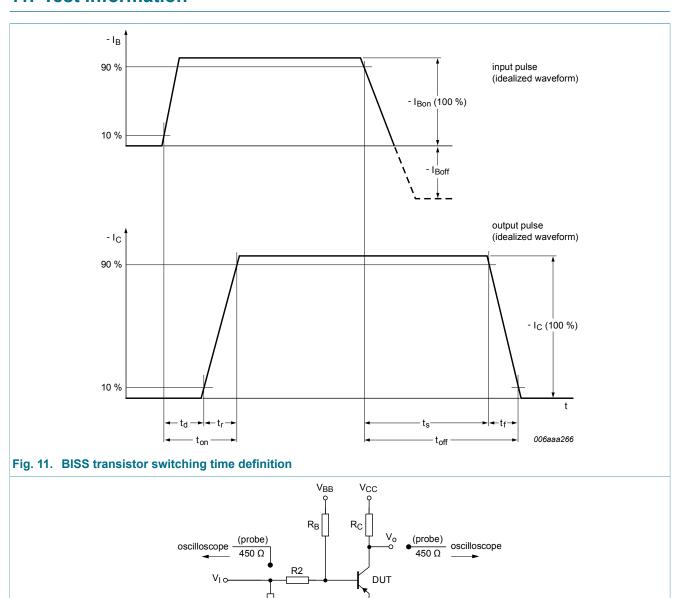
(2) 
$$I_C/I_B = 50$$

(3) 
$$I_C/I_B = 10$$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

60 V, 600 mA, PNP switching transistor

## 11. Test information



## 11.1 Quality information

Fig. 12. Test circuit for switching times

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

mgd624

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# 12. Package outline

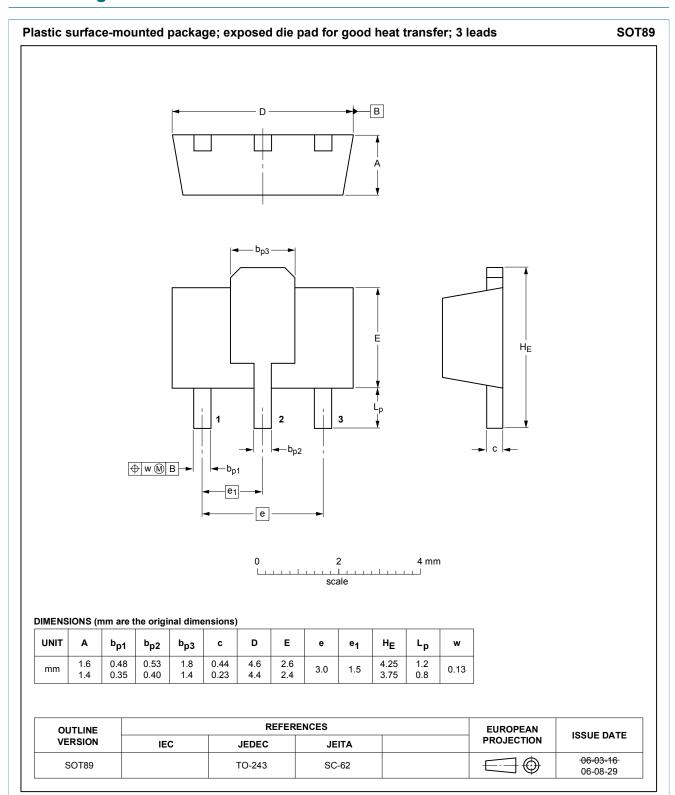


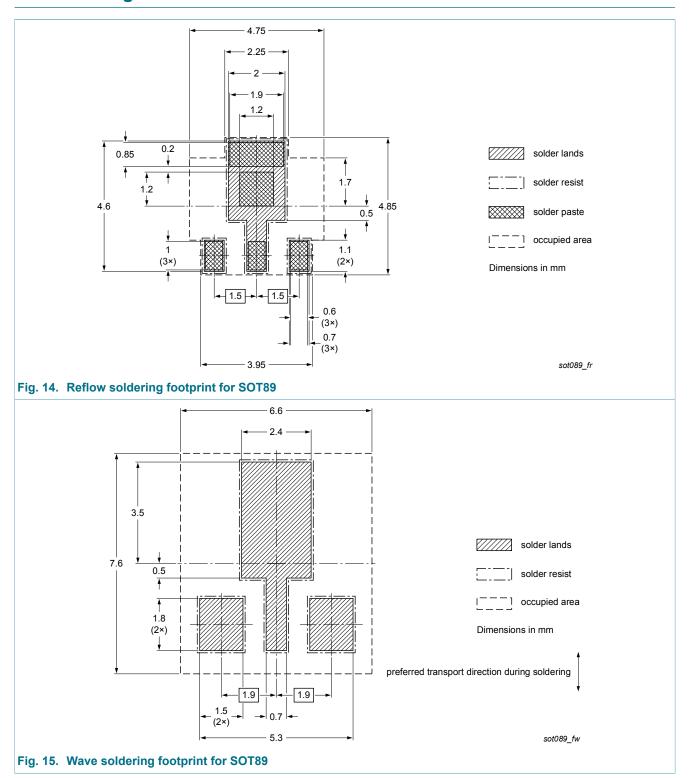
Fig. 13. Package outline SOT89

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# 13. Soldering



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# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PXT2907A v.7	20150803	Product data sheet	-	PXT2907A v.6
Modifications:	<ul> <li>Marking code corre</li> </ul>	cted		
PXT2907A v.6	20141010	Product data sheet	-	PXT2907A v.5
PXT2907A v.5	20041209	Product data sheet	-	PXT2907A v.4
PXT2907A v.4	20020320	Product data sheet	-	-

#### 60 V, 600 mA, PNP switching transistor

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#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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