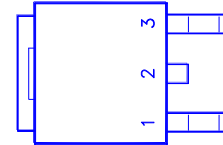
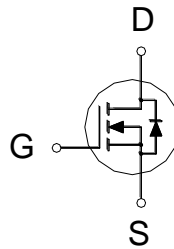




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	26.8m Ω	36A



- 1. GATE
- 2. DRAIN
- 3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ²	$T_C = 25\text{ }^\circ\text{C}$	I_D	36	A
	$T_C = 100\text{ }^\circ\text{C}$		23	
Pulsed Drain Current ¹		I_{DM}	80	
Avalanche Current		I_{AS}	13.9	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	9.7	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	78	W
	$T_C = 100\text{ }^\circ\text{C}$		31	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.6	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Calculated continuous current based on maximum allowable junction temperature.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}, T_J = 125\text{ }^\circ\text{C}$			10	
Drain-Source On-State Resistance ₁	$R_{DS(ON)}$	$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$		24	35	m Ω
		$V_{GS} = 10\text{V}, I_D = 10\text{A}$		22	26.8	

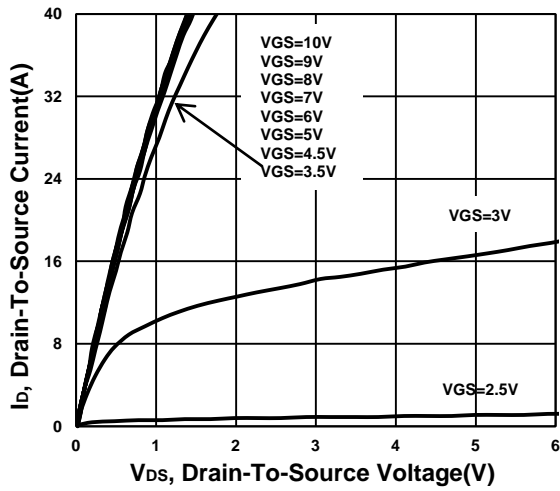
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 10A$		55		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		1918		pF
Output Capacitance	C_{oss}			139		
Reverse Transfer Capacitance	C_{rss}			88		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		0.8		Ω
Total Gate Charge ²	Q_g	$V_{GS} = 10V, V_{DS} = 50V, I_D = 10A$		41.5		nC
Gate-Source Charge ²	Q_{gs}			5.7		
Gate-Drain Charge ²	Q_{gd}			11.6		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = 50V, I_D \cong 10A, V_{GS} = 10V, R_{GEN} = 6\Omega$		14		nS
Rise Time ²	t_r			42		
Turn-Off Delay Time ²	$t_{d(off)}$			43		
Fall Time ²	t_f			34		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current ³	I_S				36	A
Forward Voltage ¹	V_{SD}	$I_F = 10A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 10A, di_F/dt = 100A / \mu S$		29.3		nS
Reverse Recovery Charge	Q_{rr}				29	

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

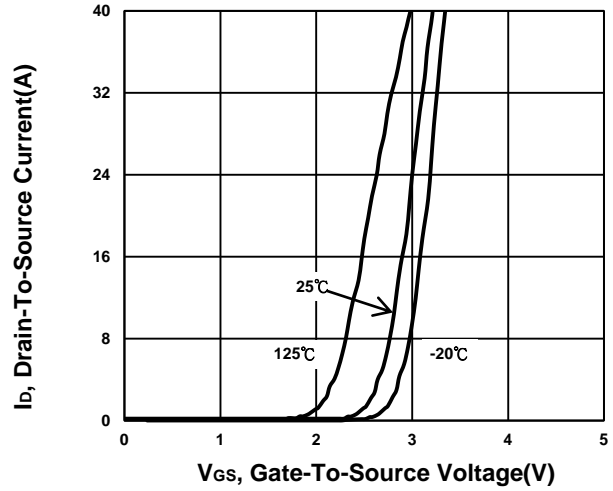
²Independent of operating temperature.

³Calculated continuous current based on maximum allowable junction temperature.

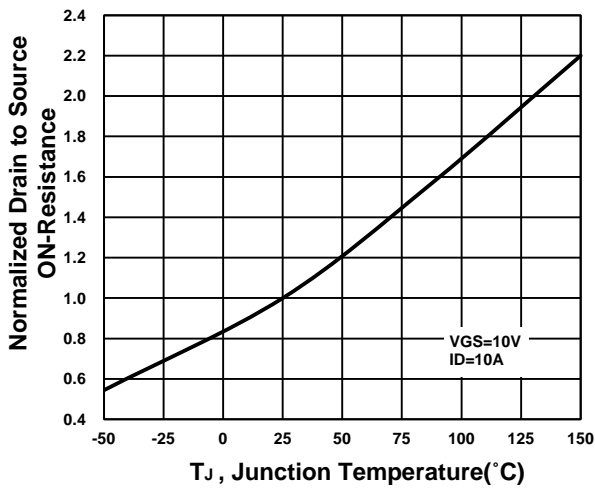
Output Characteristics



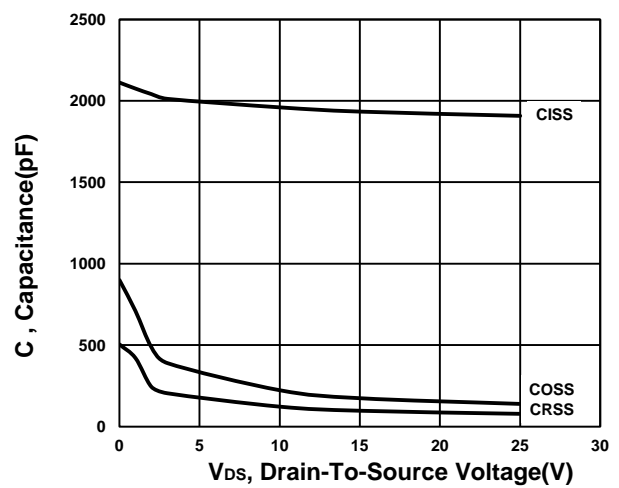
Transfer Characteristics



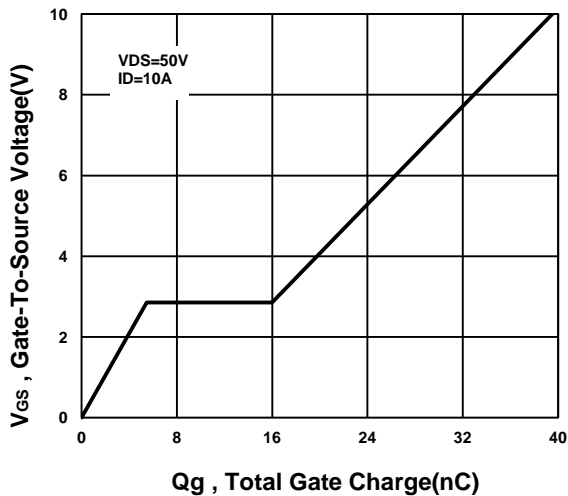
On-Resistance VS Temperature



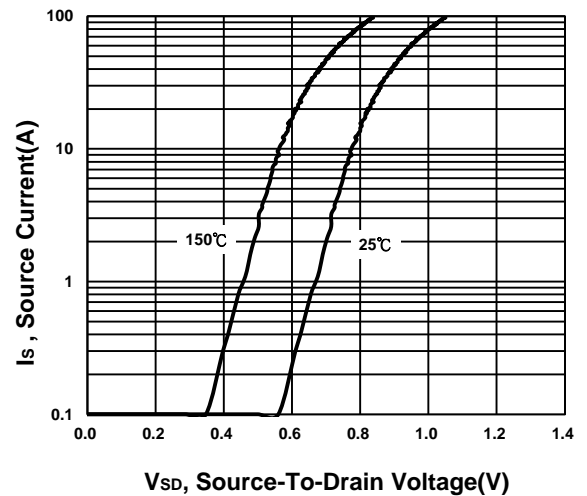
Capacitance Characteristic



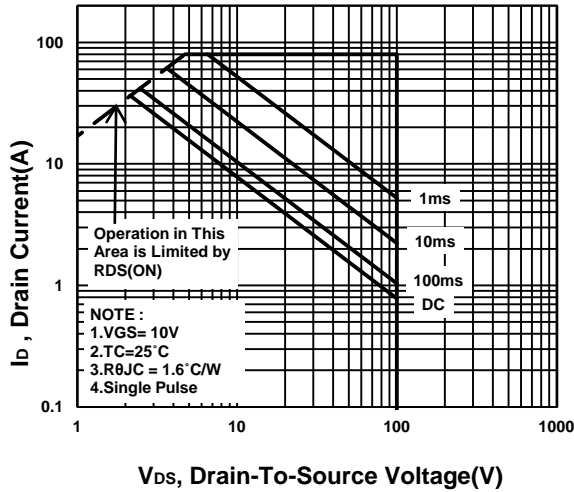
Gate charge Characteristics



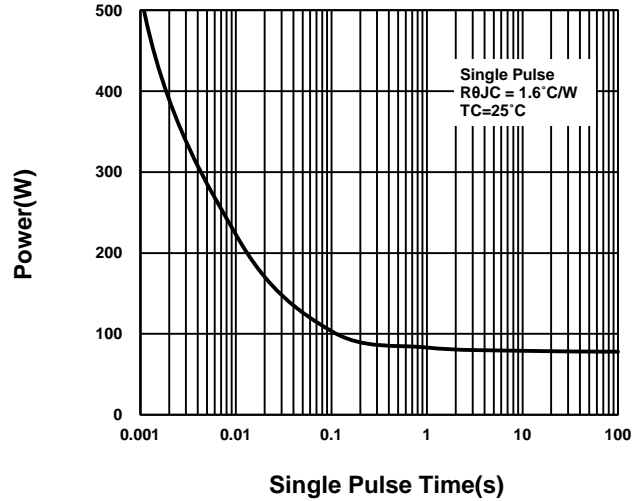
Source-Drain Diode Forward Voltage



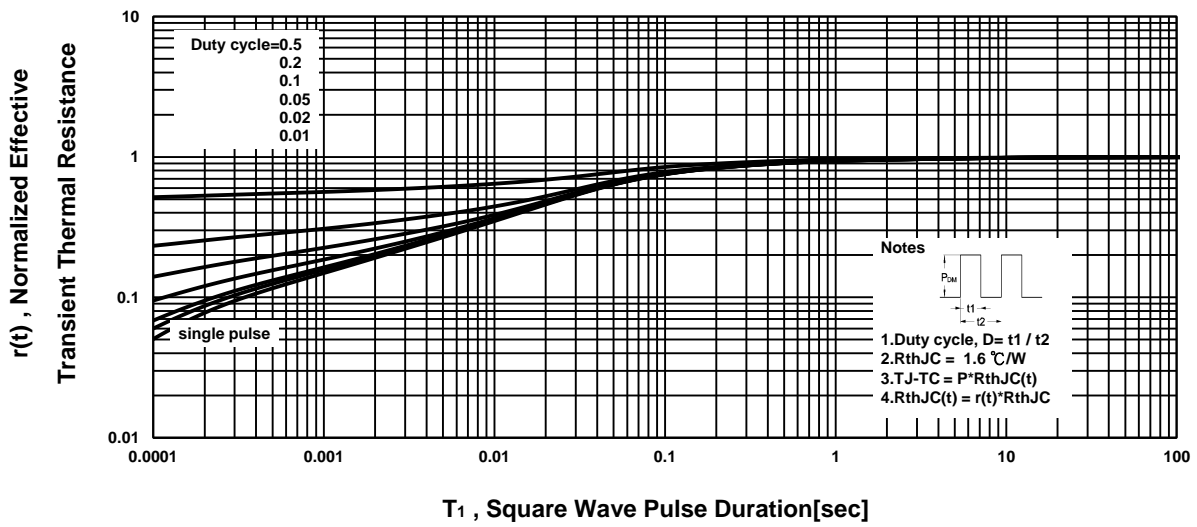
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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