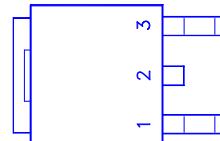
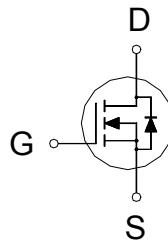


NIKO-SEM
**N-Channel Logic Level Enhancement
Mode Field Effect Transistor**
P5506BDG
TO-252
Halogen-Free & Lead-Free
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
60	55mΩ	22A


 1.GATE
 2.DRAIN
 3.SOURCE
ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	22	A
		18	
Pulsed Drain Current ¹	I_{DM}	80	
Power Dissipation	P_D	50	W
		32	
Operating Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	$^\circ\text{C}$
Lead Temperature (1/16" from case for 10 sec.)	T_L	275	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.5	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		55	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.
ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48V, V_{GS} = 0V$			1	μA
		$V_{DS} = 40V, V_{GS} = 0V, T_J = 55^\circ\text{C}$			10	
On-State Drain Current ¹	$I_{D(\text{ON})}$	$V_{DS} = 5V, V_{GS} = 10V$	22			A
Drain-Source On-State Resistance ¹	$R_{DS(\text{ON})}$	$V_{GS} = 4.5V, I_D = 8A$		59	75	$\text{m}\Omega$
		$V_{GS} = 10V, I_D = 10A$		42	55	

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Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 10A$	14		S
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DYNAMIC						
Input Capacitance	C_{iss}			587		
Output Capacitance	C_{oss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		80		pF
Reverse Transfer Capacitance	C_{rss}			46		
Total Gate Charge ²	Q_g	$V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = 10V, I_D = 10A$		12.5		nC
Gate-Source Charge ²	Q_{gs}			1.8		
Gate-Drain Charge ²	Q_{gd}			3.7		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 30V$		11		
Rise Time ²	t_r	$I_D \geq 1A, V_{GS} = 10V, R_{GEN} = 6\Omega$		8		
Turn-Off Delay Time ²	$t_{d(off)}$			19		
Fall Time ²	t_f			6		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_c = 25^\circ C$)						
Continuous Current	I_S			22	A	
Forward Voltage ¹	V_{SD}	$I_F = 1A, V_{GS} = 0V$		1	V	

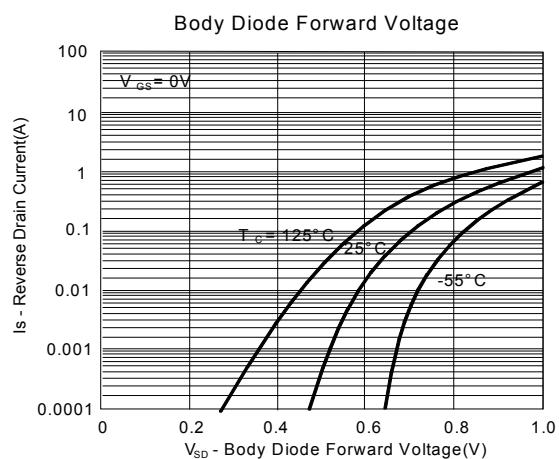
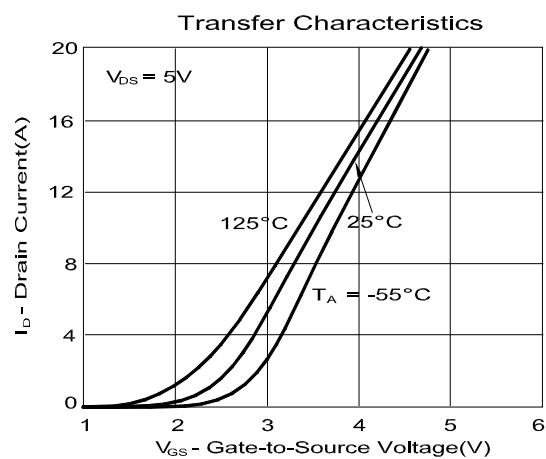
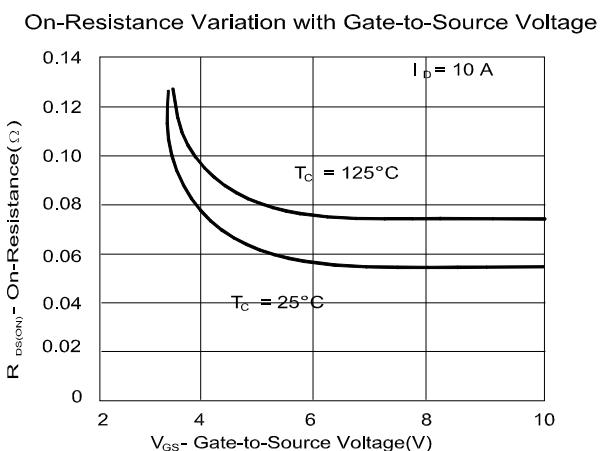
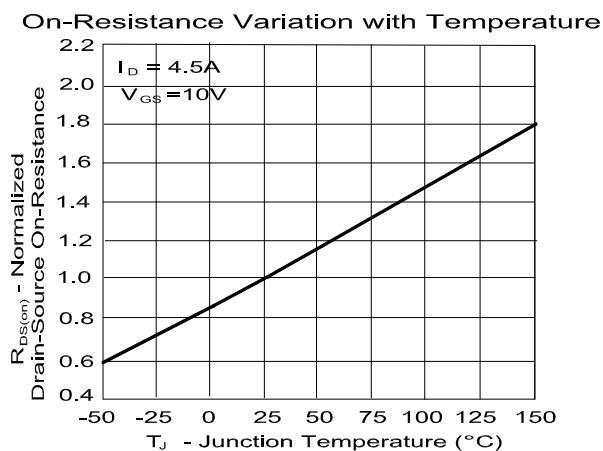
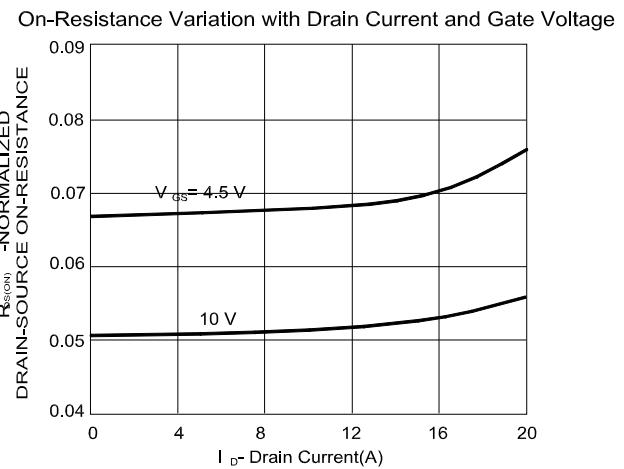
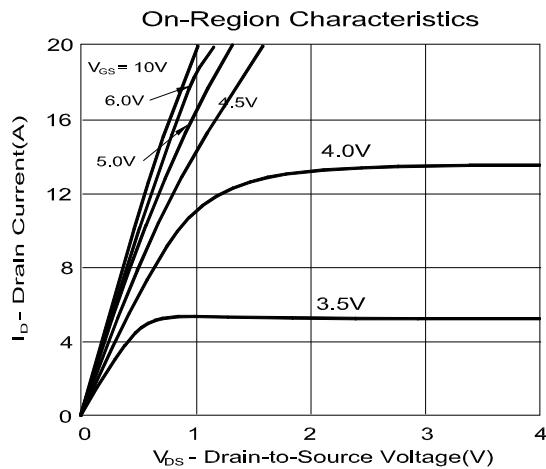
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

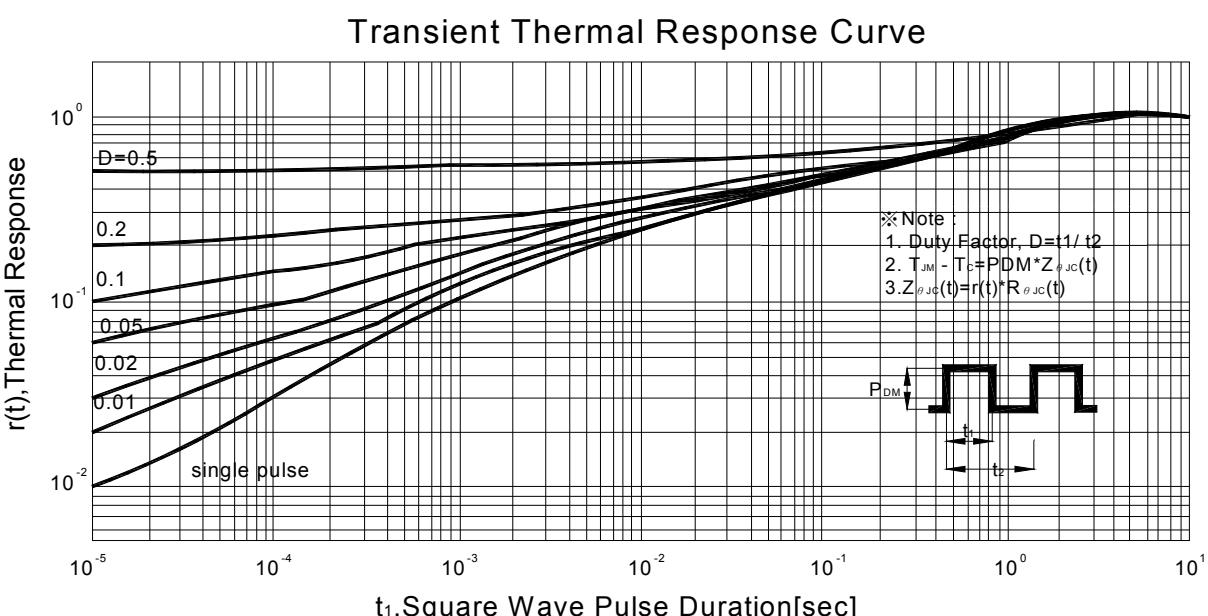
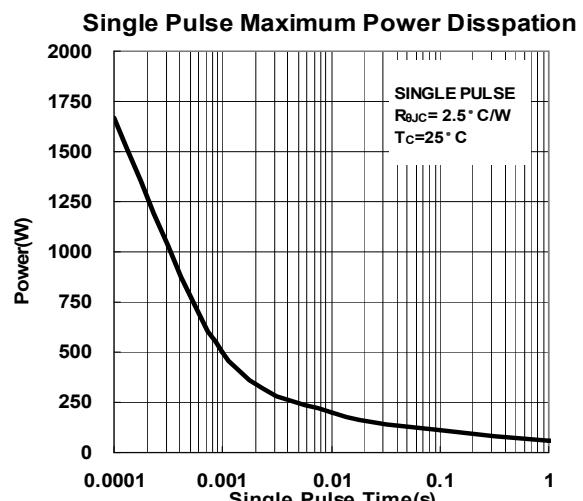
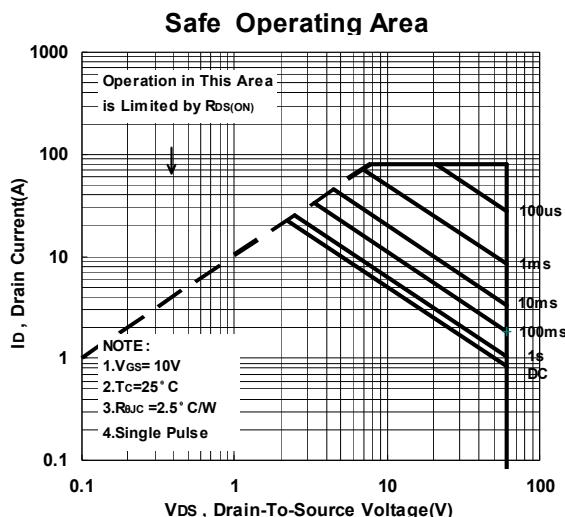
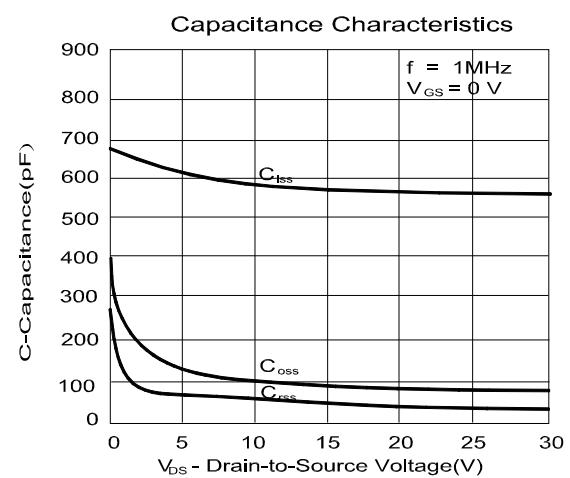
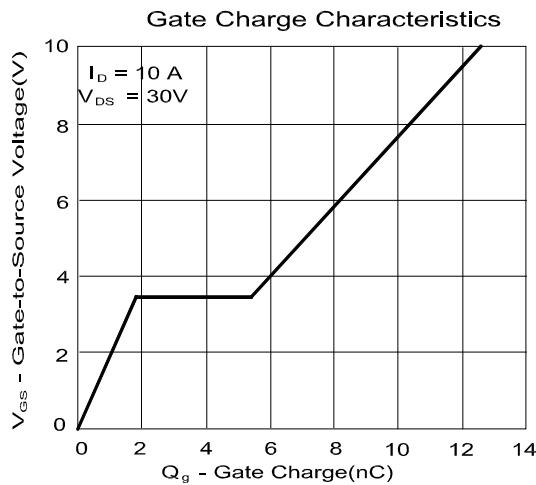
REMARK: THE PRODUCT MARKED WITH "P5506BDG", DATE CODE or LOT #

NIKO-SEM

**N-Channel Logic Level Enhancement
Mode Field Effect Transistor**

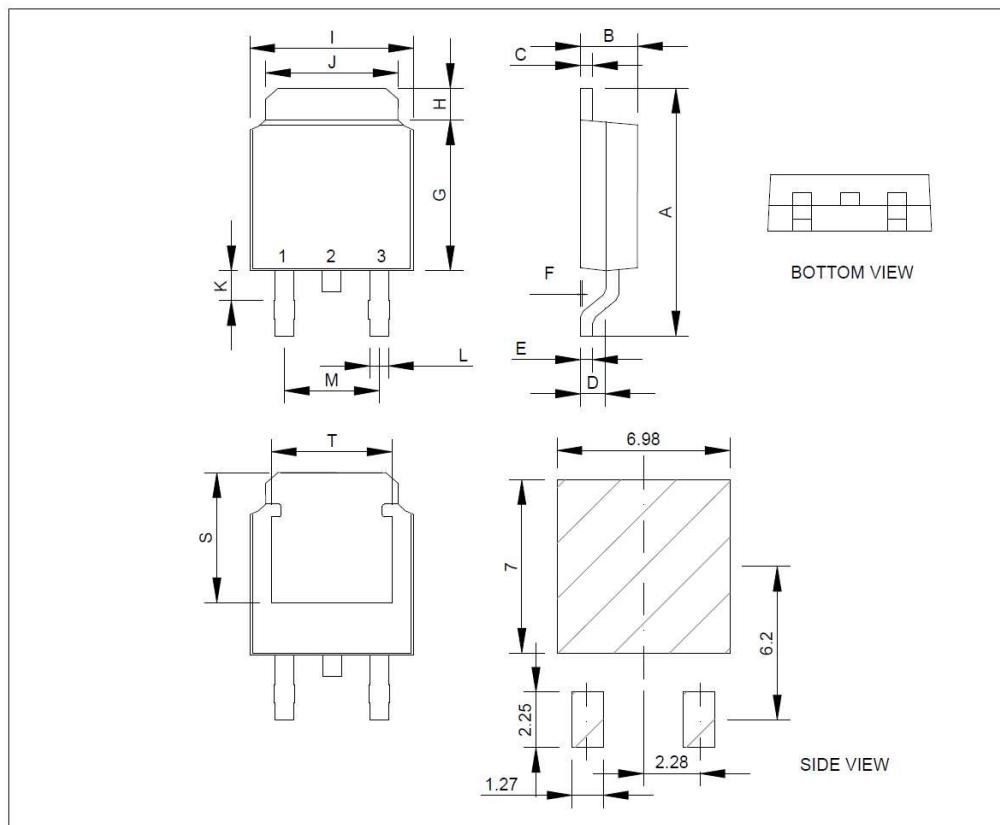
**P5506BDG
TO-252
Halogen-Free & Lead-Free**



NIKO-SEM**N-Channel Logic Level Enhancement
Mode Field Effect Transistor****P5506BDG
TO-252
Halogen-Free & Lead-Free**

NIKO-SEM**N-Channel Logic Level Enhancement
Mode Field Effect Transistor****P5506BDG
TO-252
Halogen-Free & Lead-Free****TO-252 (DPAK) MECHANICAL DATA**

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	9.5	10.4	H	0.8	1.27	2.03
B	2.19	2.3	2.435	I	6.35	6.6	6.8
C	0.35	0.5	0.65	J	4.8	5.34	5.5
D	0.89		1.5	K	0.5		1.5
E	0.35		0.65	L	0.4	0.76	0.89
F	0.0		0.23	M	3.96		5.18
G	5.4		6.2	N			



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