

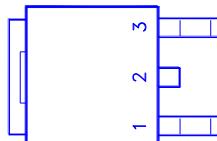
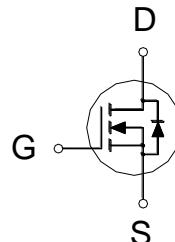
NIKO-SEM
**N-Channel Enhancement Mode
Field Effect Transistor**
PD636BA

TO-252

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	9mΩ	48A

**ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)**

100% RG Test , 100% UIL Test

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ²	$T_C = 25^\circ\text{C}$	I_D	48	A
	$T_C = 100^\circ\text{C}$		30.5	
Pulsed Drain Current ¹		I_{DM}	120	A
Avalanche Current		I_{AS}	20	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	20.8	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	37.9	W
	$T_C = 100^\circ\text{C}$		15	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3.3	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.²Package limitation current is 20A**ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ₁	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		10	12	$\text{m}\Omega$
		$V_{GS} = 10\text{V}, I_D = 20\text{A}$		7	9	

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Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 20A$	45		S
DYNAMIC					
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	765		pF
Output Capacitance	C_{oss}		143		
Reverse Transfer Capacitance	C_{rss}		91		
Gate Resistance	R_g		3		
Total Gate Charge ²	Q_g	$V_{DS} = 15V, I_D = 20A$	16.5		nC
Gate-Source Charge ²	Q_{gs}		8.9		
Gate-Drain Charge ²	Q_{gd}		2.2		
Turn-On Delay Time ²	$t_{d(on)}$		5		
Rise Time ²	t_r	$I_D \approx 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$	26		nS
Turn-Off Delay Time ²	$t_{d(off)}$		22		
Fall Time ²	t_f		42		
			25		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)					
Continuous Current ³	I_S			48	A
Forward Voltage ¹	V_{SD}	$I_F = 20A, V_{GS} = 0V$		1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 20A, dI_F/dt = 100A / \mu S$	21		nS
Reverse Recovery Charge	Q_{rr}		9		nC

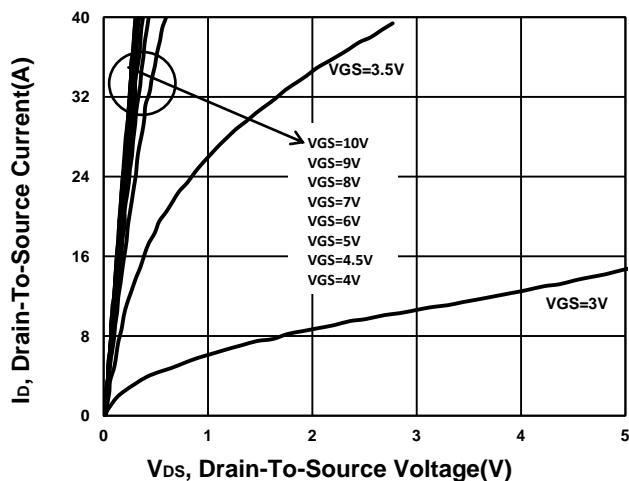
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.³Package limitation current is 20A

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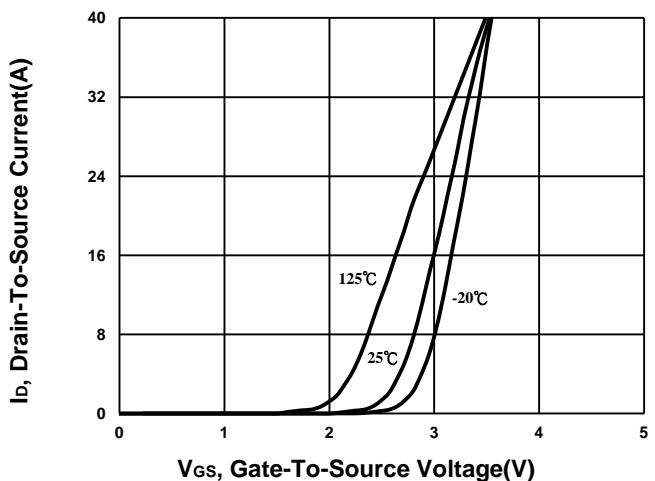
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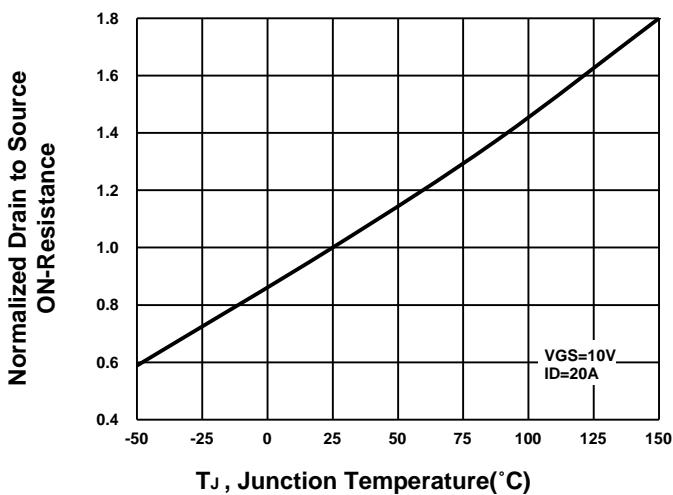
Output Characteristics



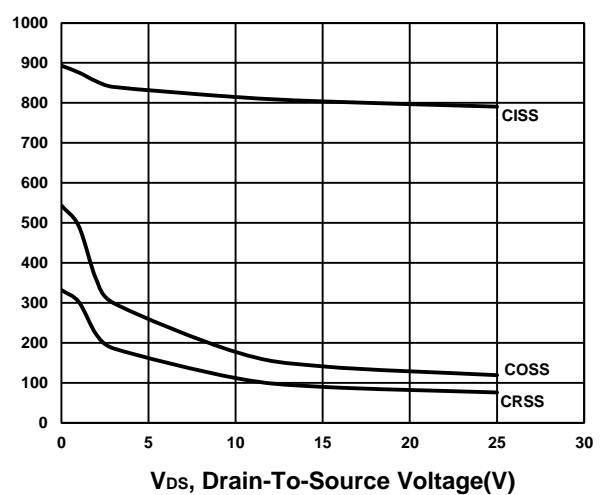
Transfer Characteristics



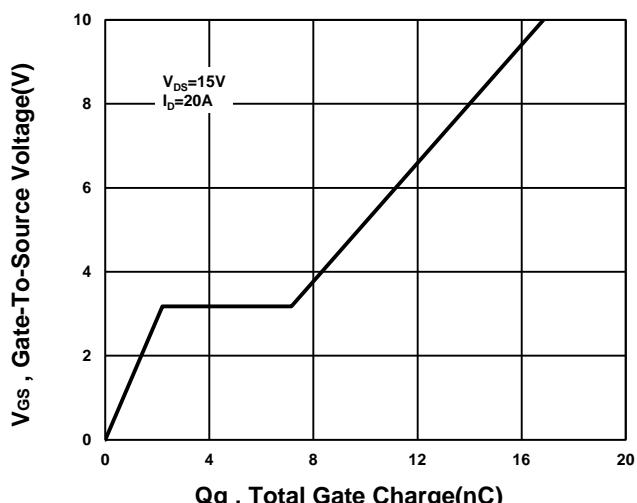
On-Resistance VS Temperature



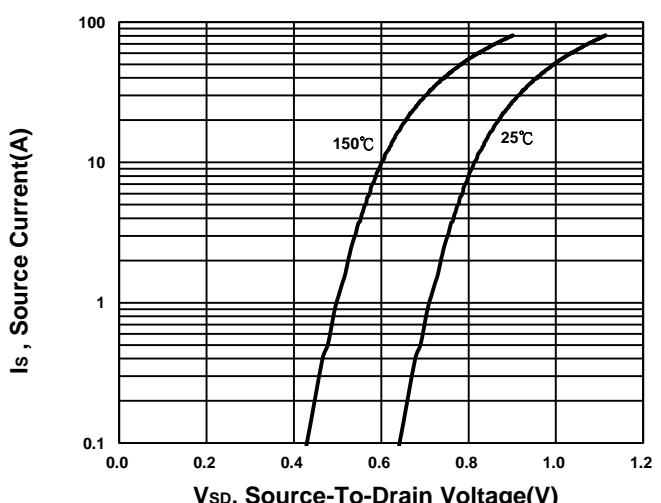
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage

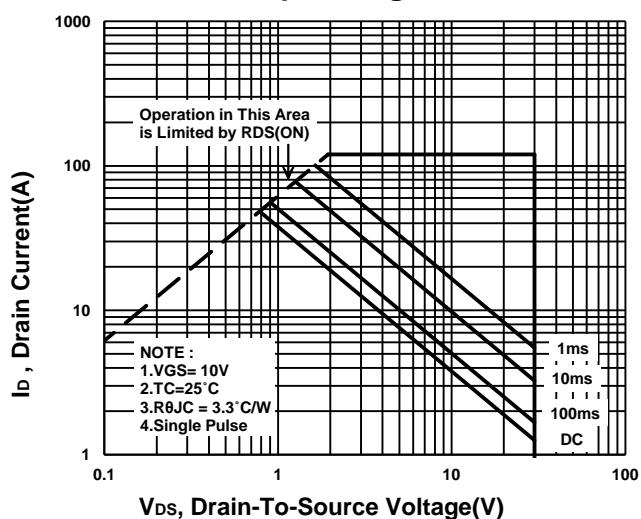


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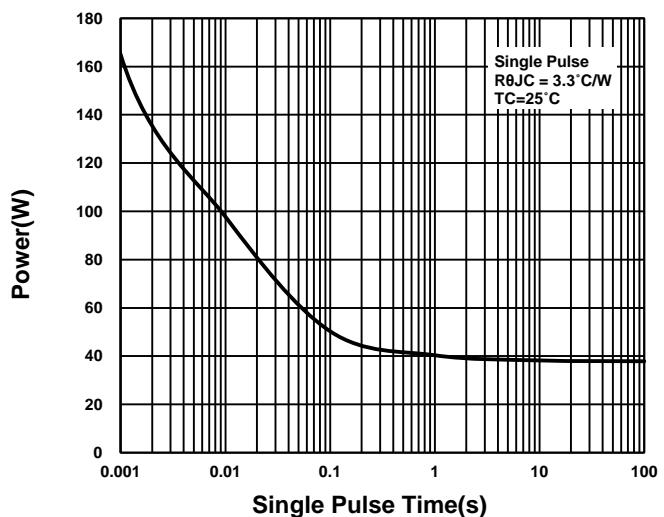
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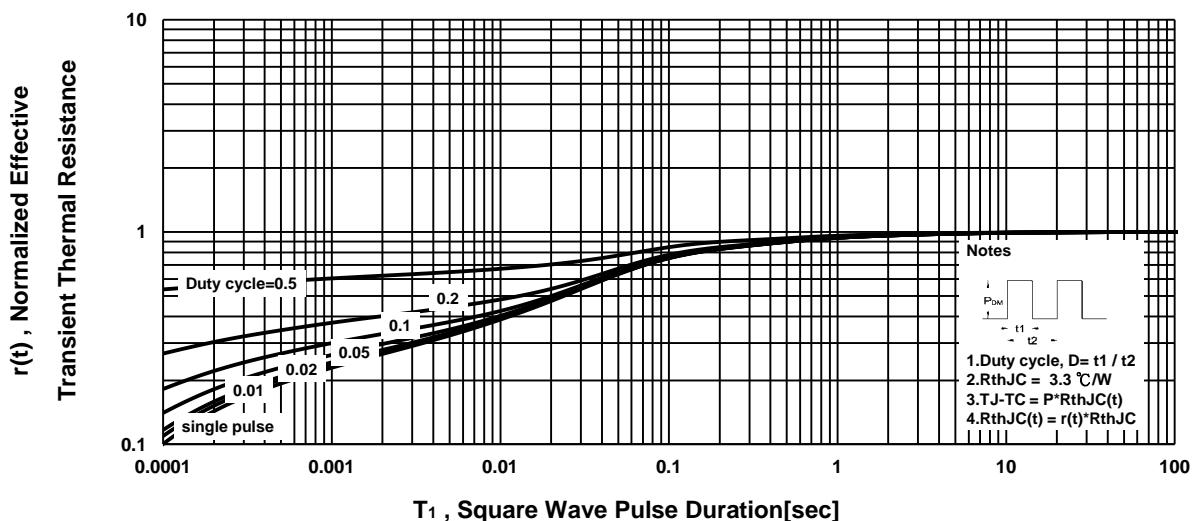
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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