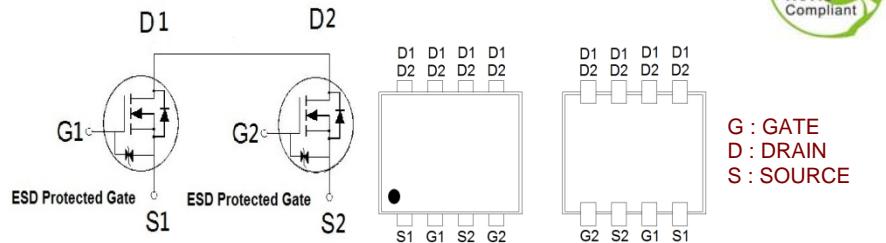


**NIKO-SEM**
**Dual N-Channel Enhancement Mode  
Field Effect Transistor**
**PJ614DA**  
**J-Lead**  
**Halogen-Free & Lead-Free**
**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
20V	11.8mΩ	10.5A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current <sup>2</sup>	$I_D$	10.5	A
$T_A = 70^\circ\text{C}$		8.4	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	28	
Avalanche Current	$I_{AS}$	22	
Avalanche Energy	$E_{AS}$	24	mJ
Power Dissipation <sup>3</sup>	$P_D$	2.1	W
$T_A = 70^\circ\text{C}$		1.3	
Operating Junction & Storage Temperature Range	$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		58	°C / W
Junction-to-Ambient	Steady-State		73	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Package limitation current is 7A.<sup>3</sup>The Power dissipation is based on  $R_{\theta JA}$  t ≤ 10s value.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.35	0.7	1	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 8V$			±30	uA

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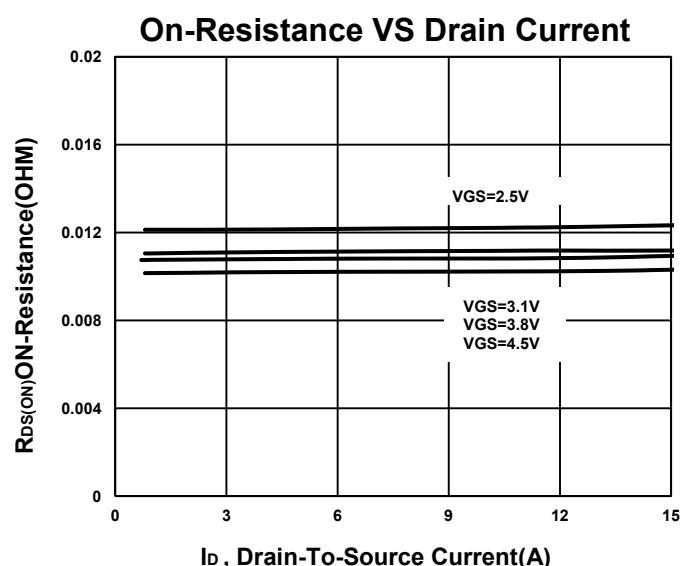
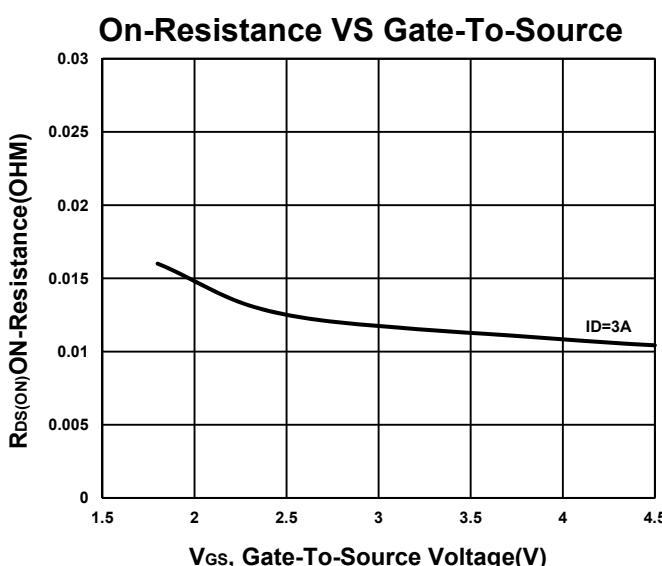
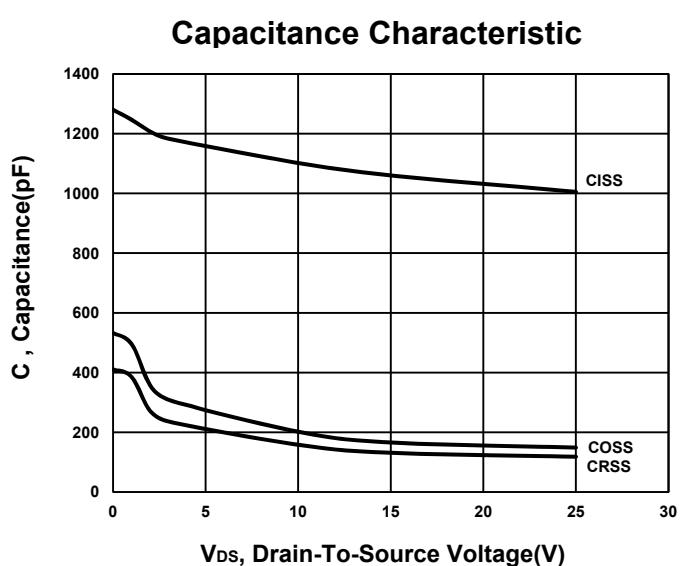
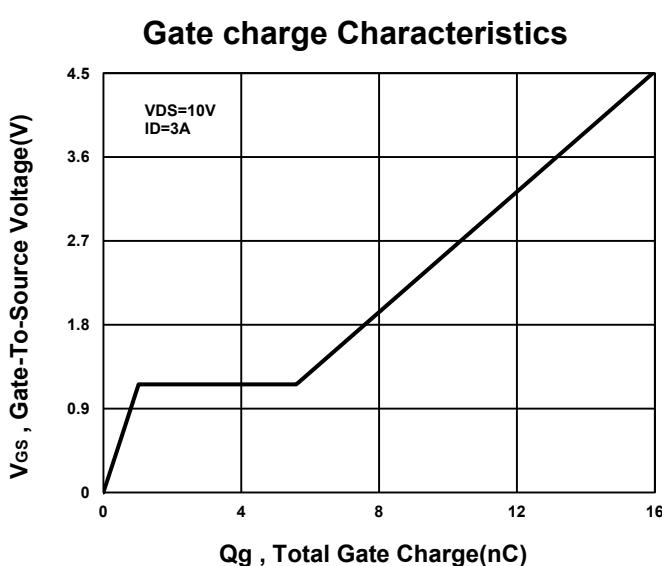
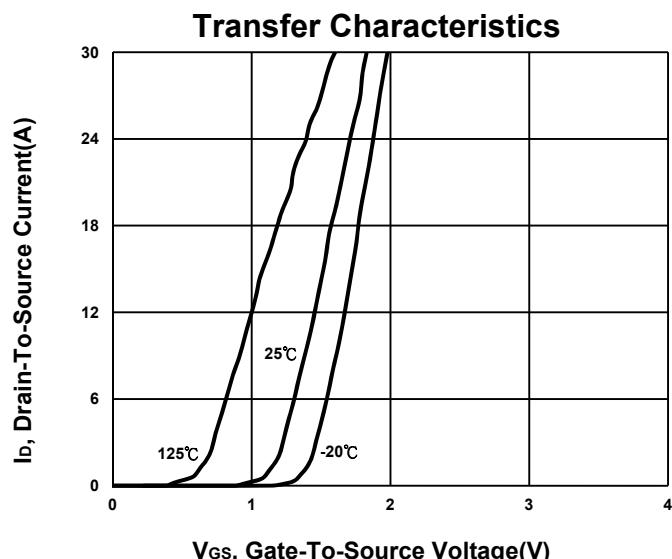
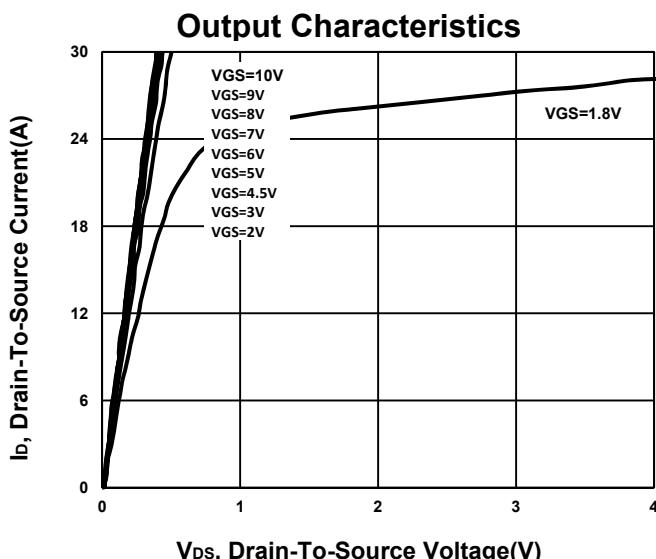
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 16V, V_{GS} = 0V$			1	$\mu A$
		$V_{DS} = 10V, V_{GS} = 0V, T_J = 70^\circ C$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 2.5V, I_D = 3A$	8.5	13	20	$m\Omega$
		$V_{GS} = 3.1V, I_D = 3A$	7.8	11.5	15.3	
		$V_{GS} = 3.8V, I_D = 3A$	7.2	10.6	12.9	
		$V_{GS} = 4.5V, I_D = 3A$	7.1	10	11.8	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 3A$		40		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$		1133		$pF$
Output Capacitance	$C_{oss}$			214		
Reverse Transfer Capacitance	$C_{rss}$			168		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.5		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 10V, V_{GS} = 4.5V, I_D = 3A$		17.4		$nC$
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			1.1		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			5.1		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 10V, I_D \approx 3A, V_{GS} = 4.5V, R_G = 6 \Omega$		24		$nS$
Rise Time <sup>2</sup>	$t_r$			32		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			66		
Fall Time <sup>2</sup>	$t_f$			35		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Continuous Current	$I_s$				1.7	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 3A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 3A, dI_F/dt = 100A / \mu S$		14		$nS$
Reverse Recovery Charge	$Q_{rr}$			5.4		$nC$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.<sup>3</sup>Pulse width limited by maximum junction temperature.

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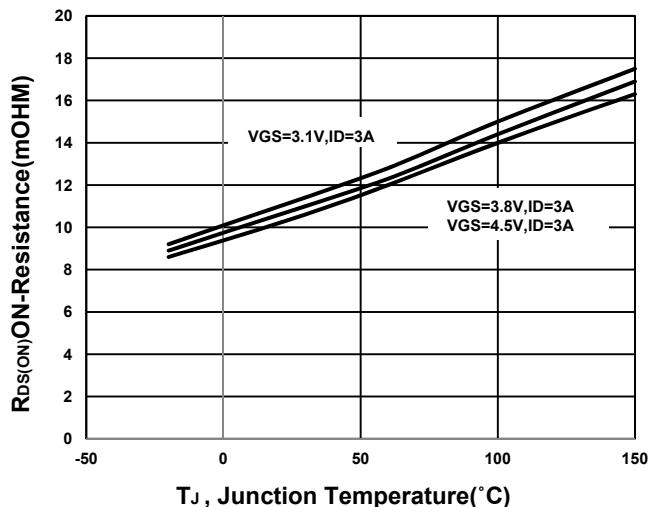


**NIKO-SEM**

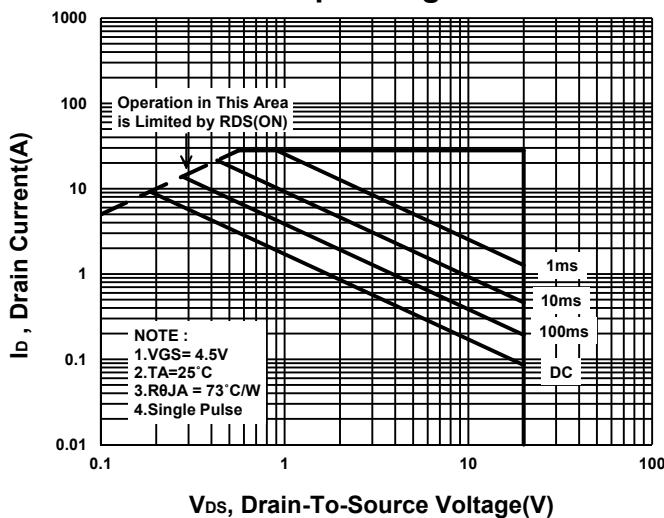
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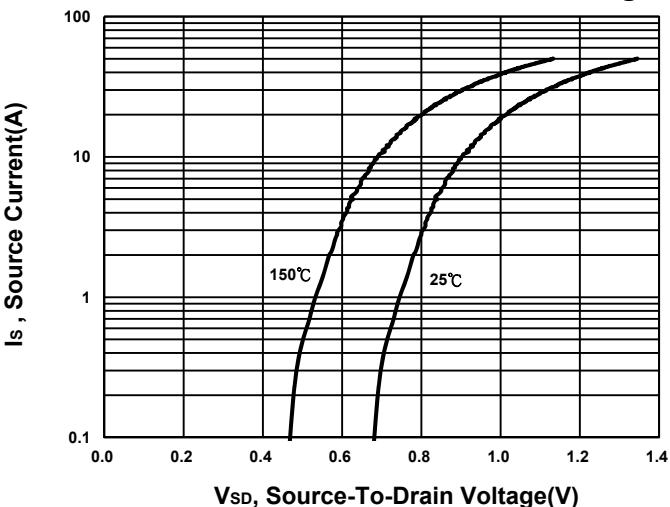
**On-Resistance VS Temperature**



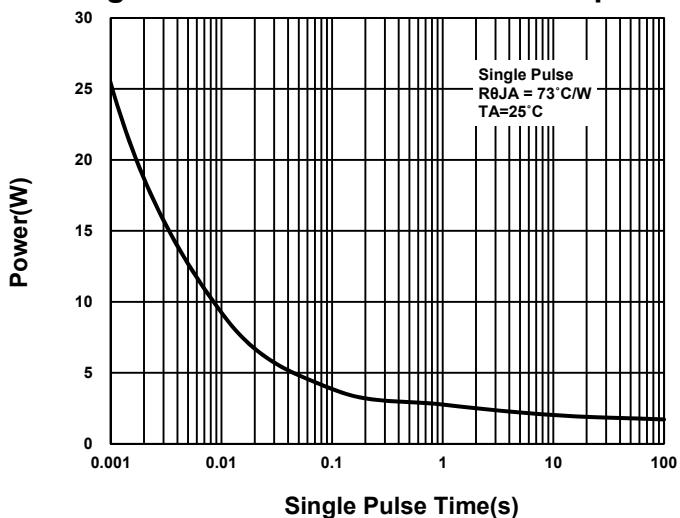
**Safe Operating Area**



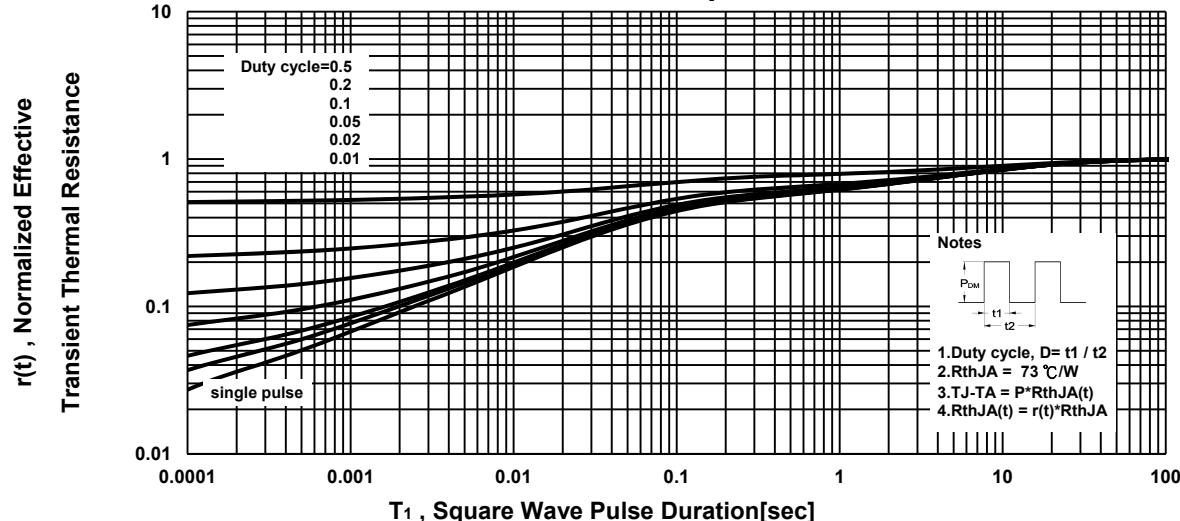
**Source-Drain Diode Forward Voltage**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



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